

GATE 2019 Electronics Engineering

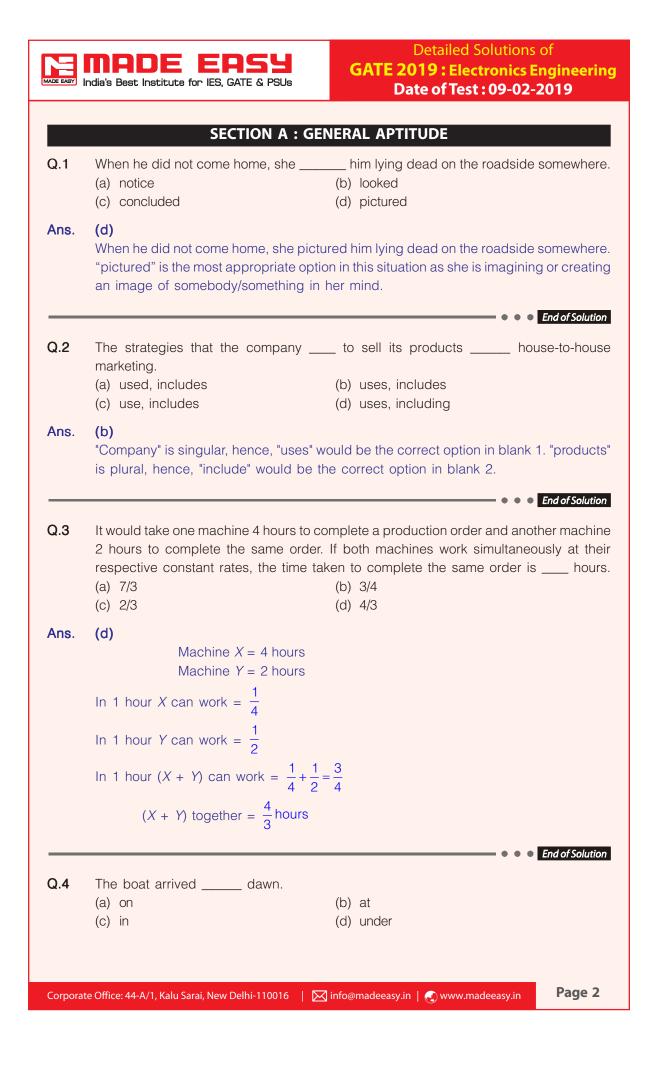
Click here for Questions and Solutions

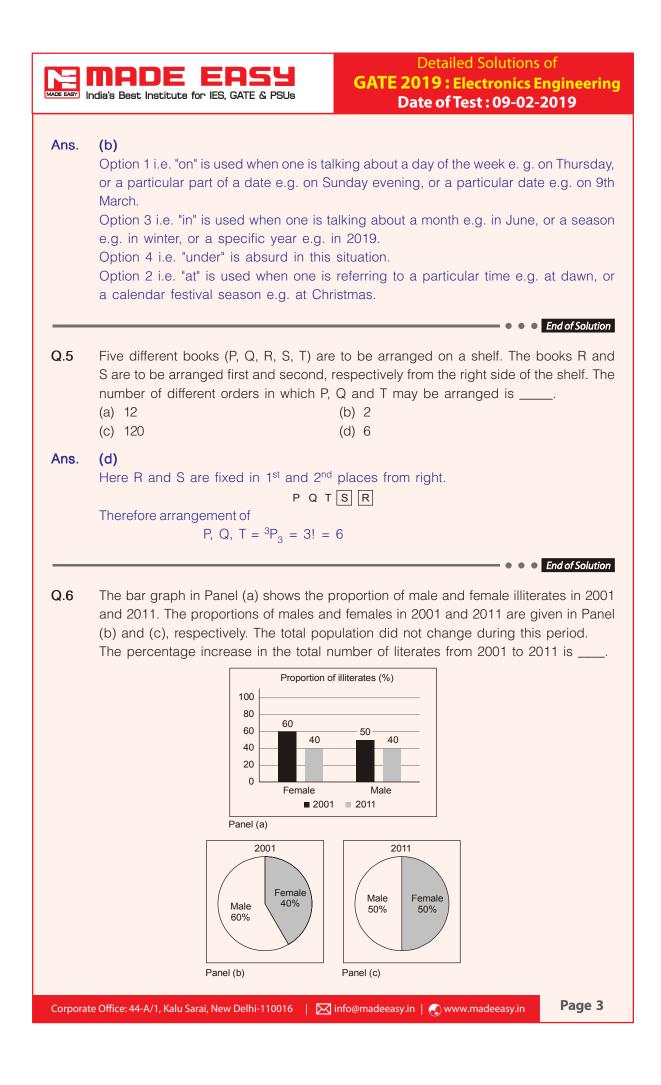
Date of Exam : 9/2/2019

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Features :

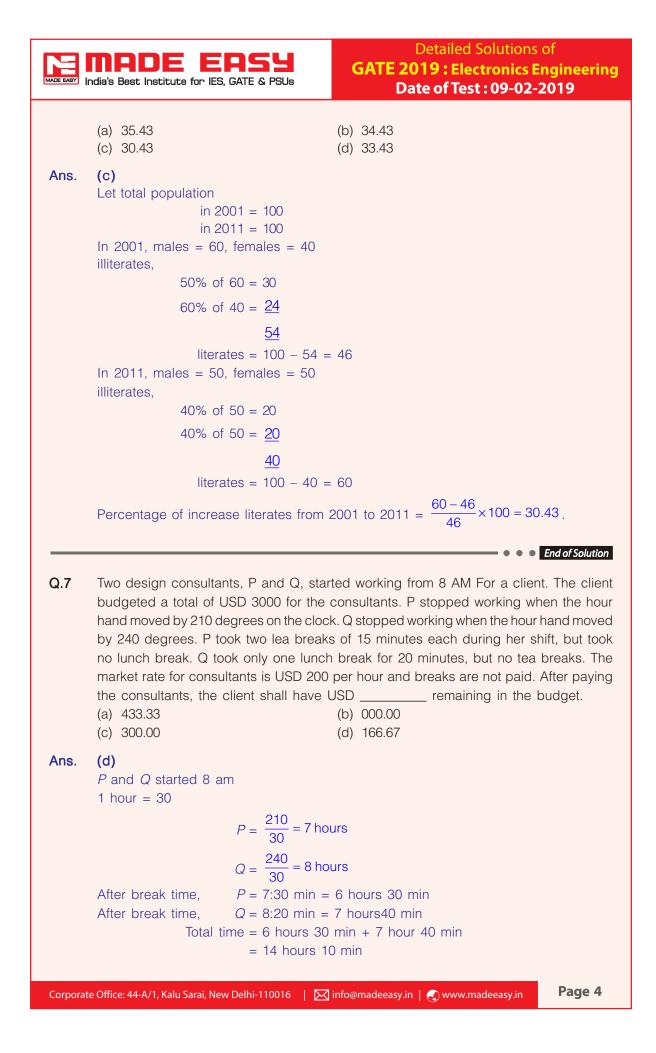
- Very useful to develop numerical solving approach & improving writing skills.
- Special focus on improving answer layout specially for theory questions.
- Classes will be delivered by senior faculties.
- Updated Mains workbook for every subject having varied practice question sets.
- Test series will be conducted on every Sunday in synchronisation with the subject taught in class.
- Discussion on probable questions.
- Comprehensive and in-depth discussion on variety of conventional questions, thus strengthening fundamental concepts.

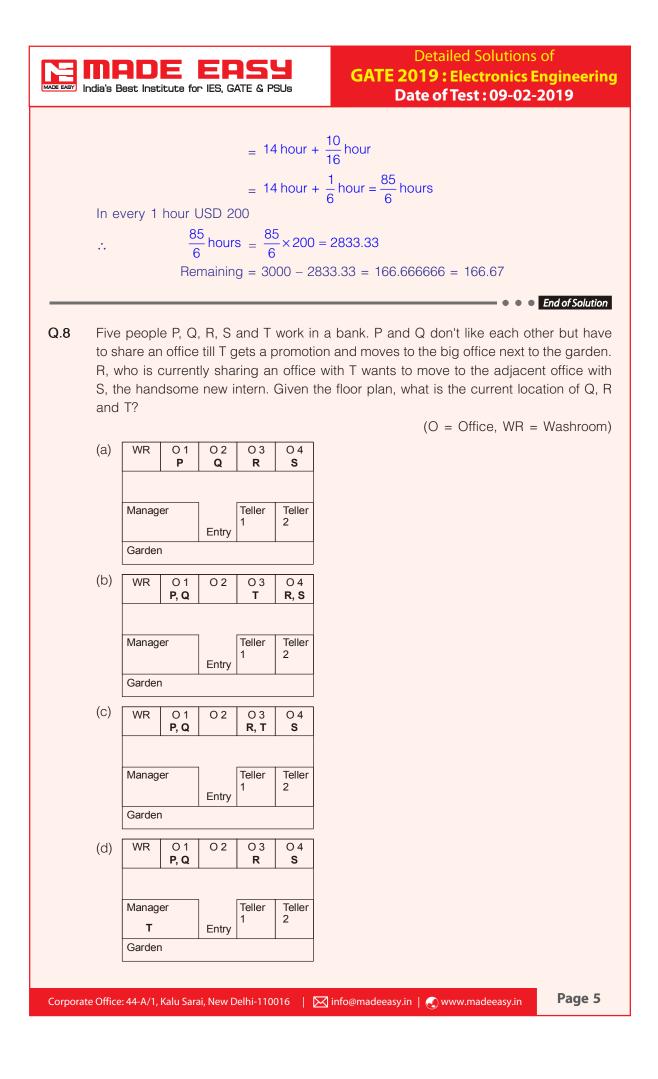
Batch Details		Course Duration 90 days 300 - 350 hours		Class Duration a week and 6-7 hours a day	Test Series Every Sunday	
Streams	Batch Code	Batch Comme	ncing Date	Venue (Delhi)	Timing	
ME	A	20-Feb-	2019	Ghitorni Centre	7:30 AM to 1:30 PM	
ME	В	20-Feb-	2019	Ghitorni Centre	3:00 PM to 9:00 PM	
ME	С	20-Feb-	2019	Saket Centre	7:30 AM to 1:30 PM	
CE	A	21-Feb-	2019	Ignou Road Centre	7:30 AM to 1:30 PM	
CE	В	21-Feb-	2019	Kalu Sarai Centre	3:00 PM to 9:00 PM	
EE	A	22-Feb-	2019	Lado Sarai Centre	7:30 AM to 1:30 PM	
EE	В	22-Feb-	2019	Kalu Sarai Centre	3:00 PM to 9:00 PM	
EC	A	22-Feb-	2019	Lado Sarai Centre	7:30 AM to 1:30 PM	

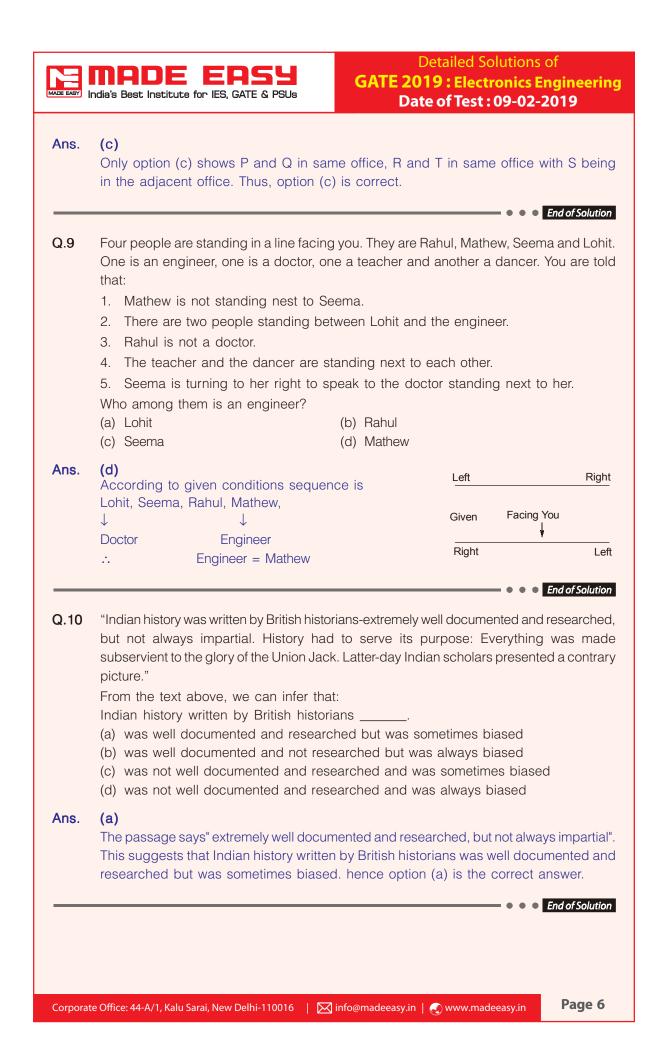
Fee	Program	Ex. MADE EASY Students Enrolled in Postal, Rank Improvement, Mains, GS, Post-GATE, ESE+ GATE, GATE Batches	Non MADE EASY students	
Structure	Mains Exclusive Batch (Inclusive of ESE-2019 Mains Offline Test Series)	₹ 18,500	₹ 22,500	
	ESE 2019 Mains Offline Test Series	Rs. 3,500/- Rs. 2,500/-	Rs. 4,500/- Rs. 3,500/-	
		Rs. 1000/- Discount on test series valid till 15-02-2019		

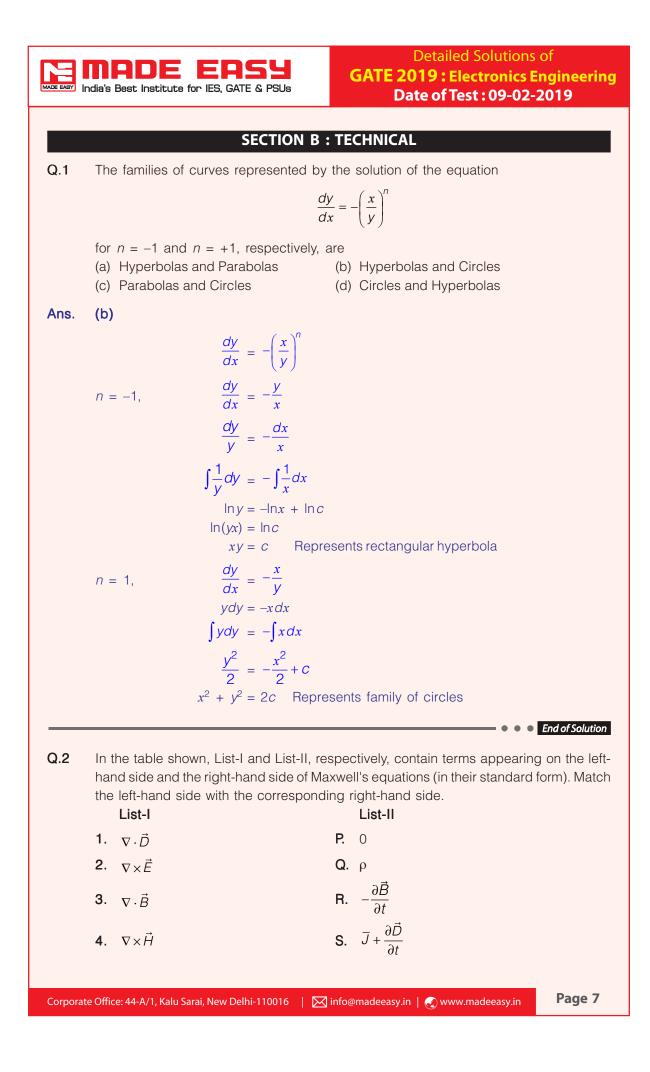
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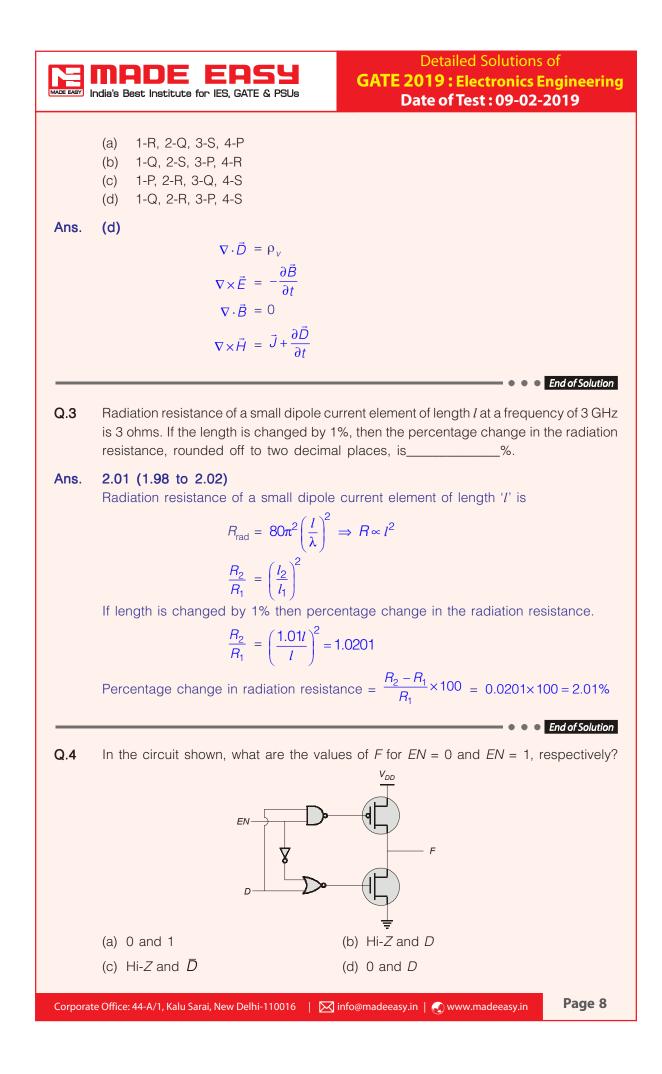
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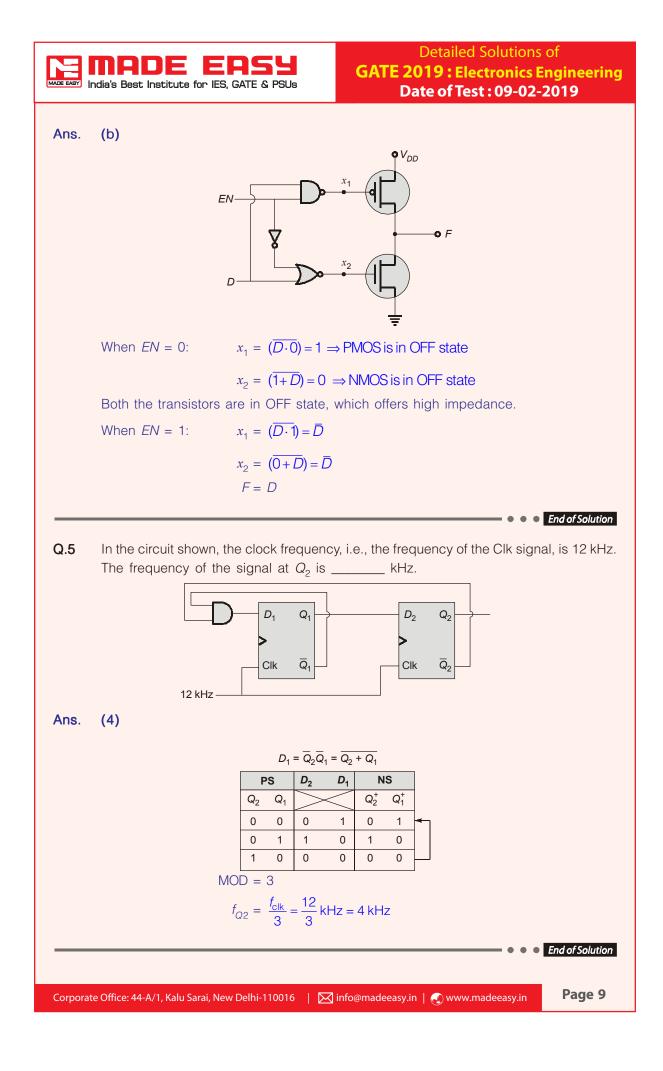














- Quality Teaching
- Comprehensive Study Material
- Well Planned Curriculum
- Professionally Managed

Announcing

NEW BATCHES for **ESE 2020** & **GATE 2020**

1 Yr/2Yrs CLASSROOM COURSES

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Batches Commencement Dates

Regular Batches		Weekend Batches			
Delhi		Delhi		Noida	
CE	14-Feb-2019	CE			
ME	14-Feb-2019	ME	NA	03-Feb-2019	
EE	18-Feb-2019		23-Feb-2019		
EC	Mid-Feb, 2019		23-Feb-2019	16-Feb-2019	
CS	16-May-2019		17-Feb-2019		

Rest of India (Regular Batches)

Patna 25-Feb'19	Lucknow	Bhopal	Indore	Jaipur
	20-Feb'19	25-Feb'19	20-Feb'19	17-Feb'19
Pune	Hyderabad	Bhubaneswar	Kolkata	
11-Feb'19	17-Feb'19	25-Feb'19	2-Mar'19	

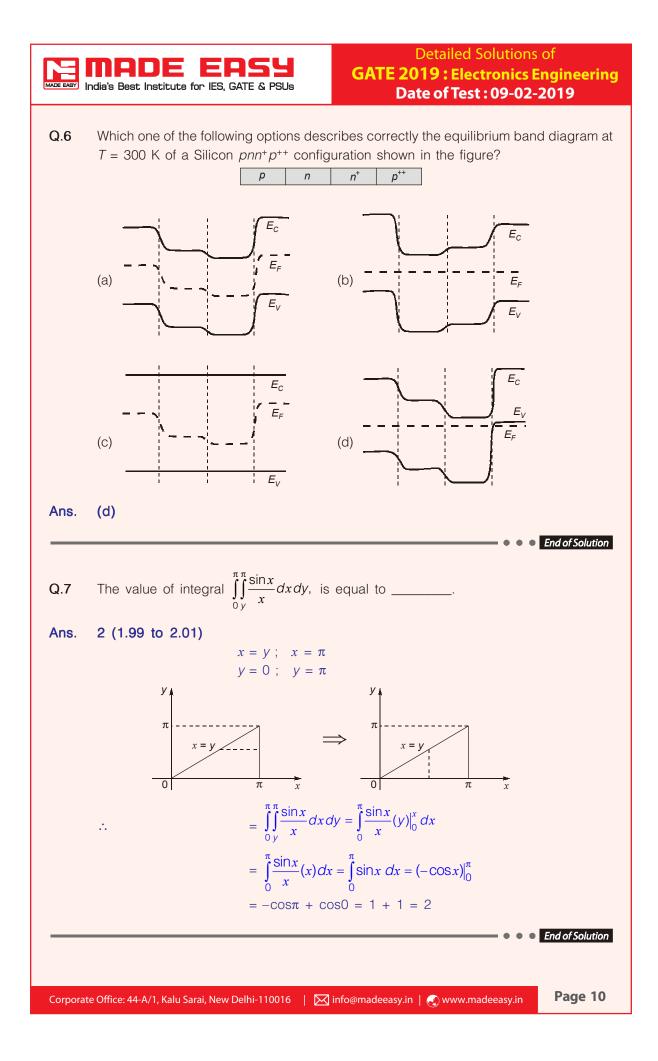
Admission open

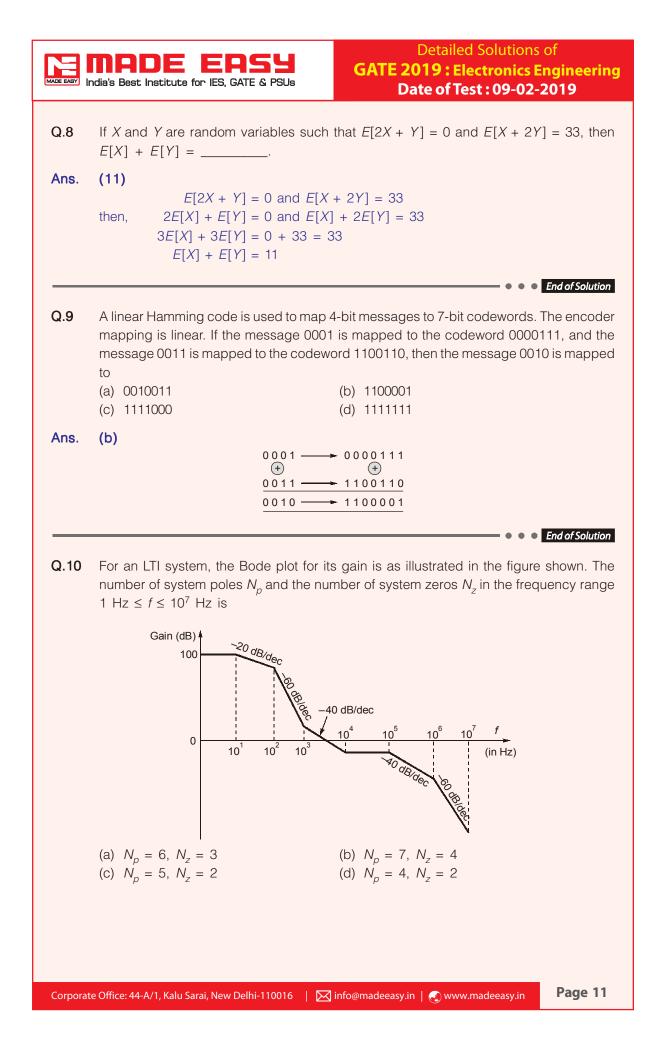
Corporate office : 44-A/1, Kalu Sarai, New Delhi - 1100 16 🕓 011-45124612, 9958995830 Bhopal Lucknow Indore Bhubaneswar Delhi Hyderabad Noida Jaipur Pune Kolkata Patna Centres) 011-45124612

09919111168 0731-4029612

0612-2356615

040-66774612





Detailed Solutions of GATE 2019 : Electronics Engineering Date of Test : 09-02-2019

Ans. (a)

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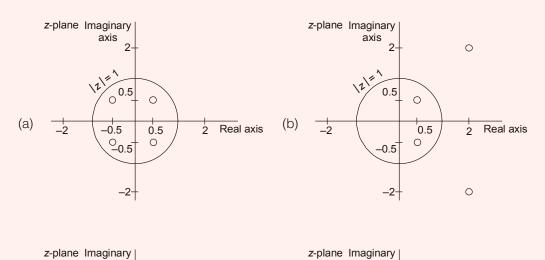
Corner frequency (in Hz)	No. of poles (or) zeros
10	1 pole
10 ²	2 poles
10 ³	1 zero
10 ⁴	2 zeros
10 ⁵	2 poles
10 ⁶	1 pole

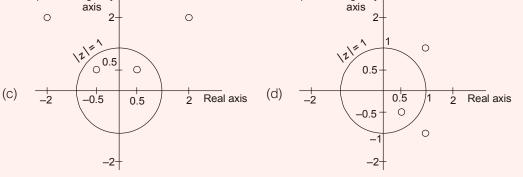
Number of poles $(N_p) = 6$ Number of zeros $(N_z) = 3$

End of Solution

Q.11 Let H(z) be the z-transform of a real-valued discrete-time signal h[n]. If $P(z) = H(z)H\left(\frac{1}{z}\right)$

has a zero at $z = \frac{1}{2} + \frac{1}{2}j$, and P(z) has a total of four zeros, which one of the following plots represents all the zeros correctly?





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Ans. (d)

MADE

$$P(Z) = H(z)H\left(\frac{1}{z}\right)$$

(i) h(n) is real. So, p(n) will be also real (ii) $P(z) = P(z^{-1})$

ER

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From (i): If z_1 is a zero of P(z), then z_1^* will be also a zero of P(z).

From (ii): If z_1 is a zero of P(z), then $\frac{1}{z_1}$ will be also a zero of P(z).

So, the 4 zeros are,

$$Z_{1} = \frac{1}{2} + \frac{1}{2}j$$

$$Z_{2} = Z_{1}^{*} = \frac{1}{2} - \frac{1}{2}j$$

$$Z_{3} = \frac{1}{Z_{1}} = \frac{1}{\frac{1}{2} + \frac{1}{2}j} = 1 - j$$

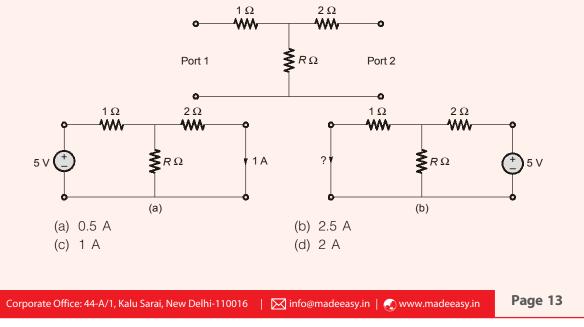
$$Z_{4} = \left(\frac{1}{Z_{1}}\right)^{*} = Z_{3}^{*} = 1 + j$$

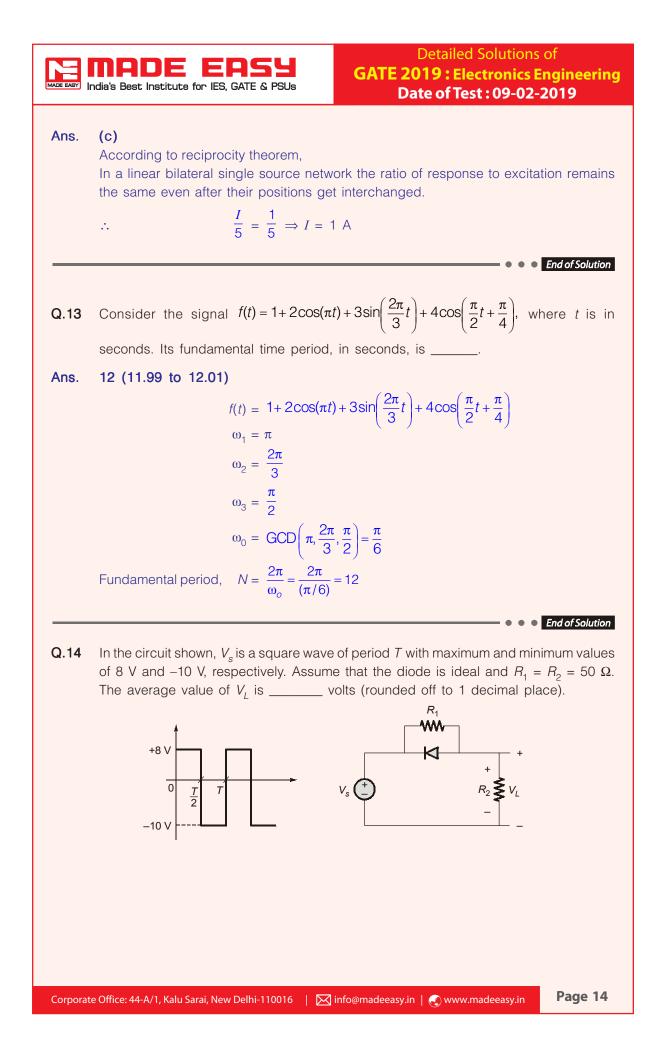
So, option (d) is correct.

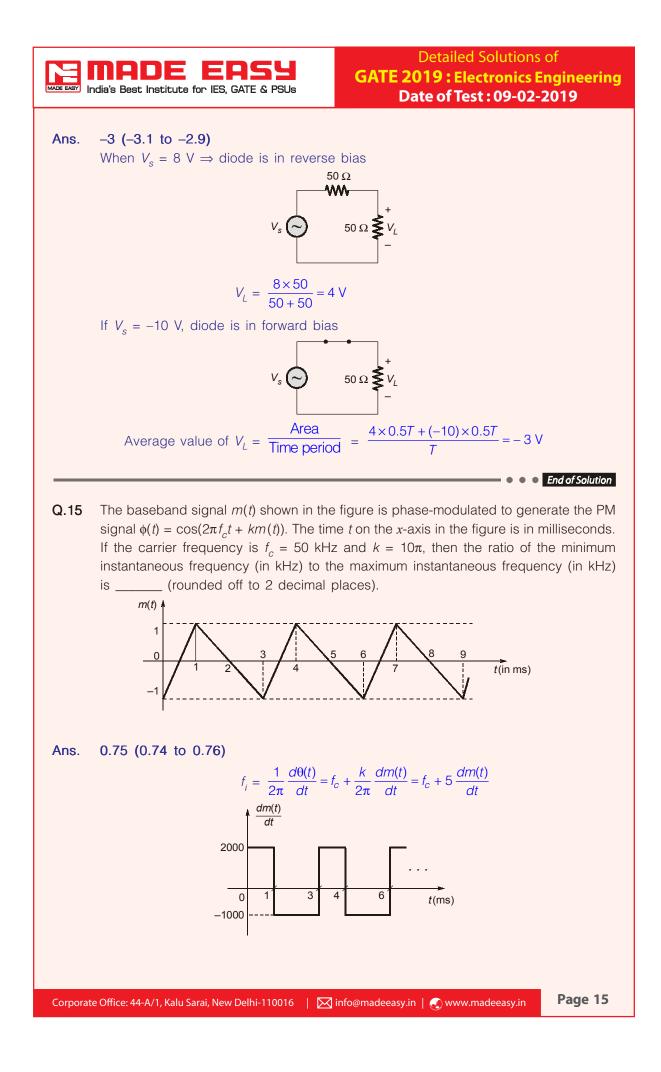
End of Solution

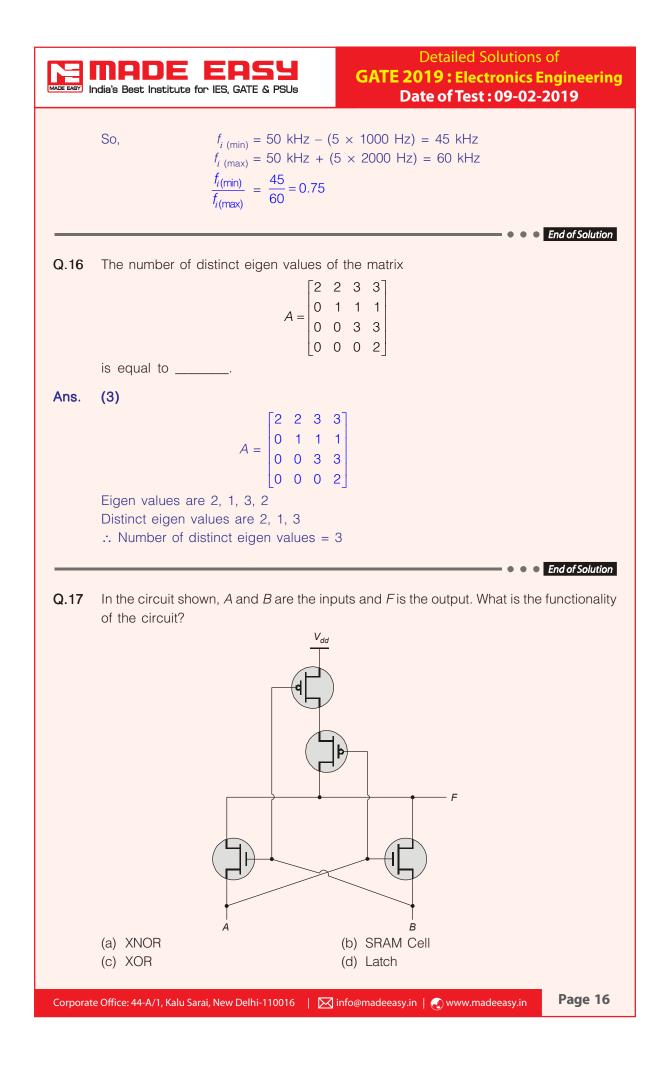
Q.12 Consider the two-port resistive network shown in the figure. When an excitation of 5 V is applied across Port 1, and Port 2 is shorted, the current through the short circuit at Port 2 is measured to be 1 A (see (a) in the figure).

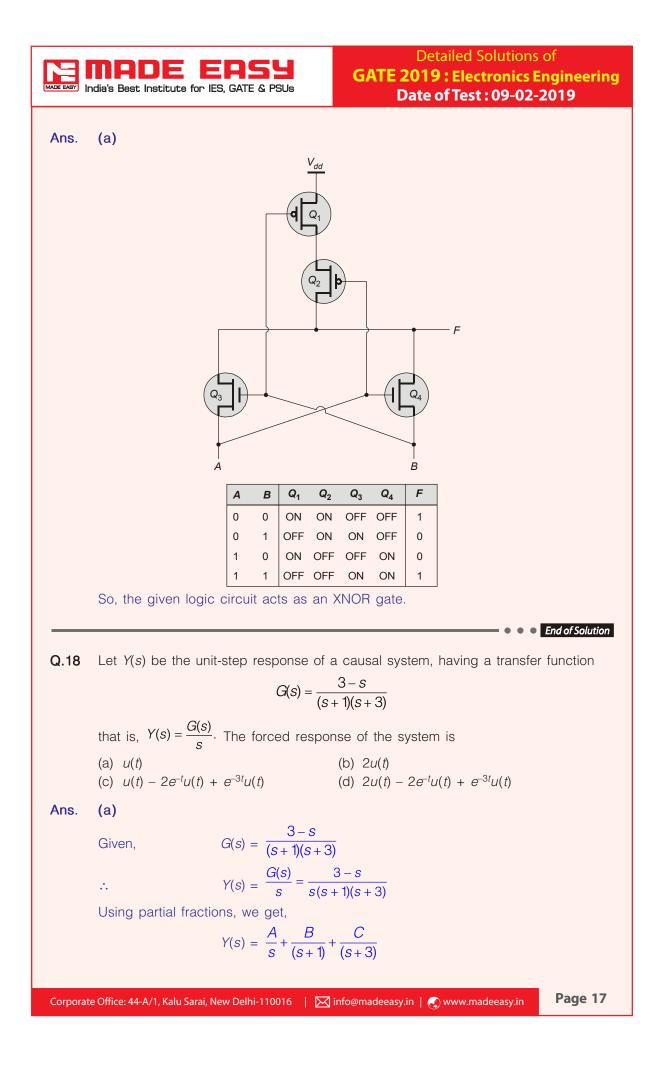
Now, if an excitation of 5 V is applied across Port 2, and Port 1 is shorted (see (b) in the figure), what is the current through the short circuit at Port 1?

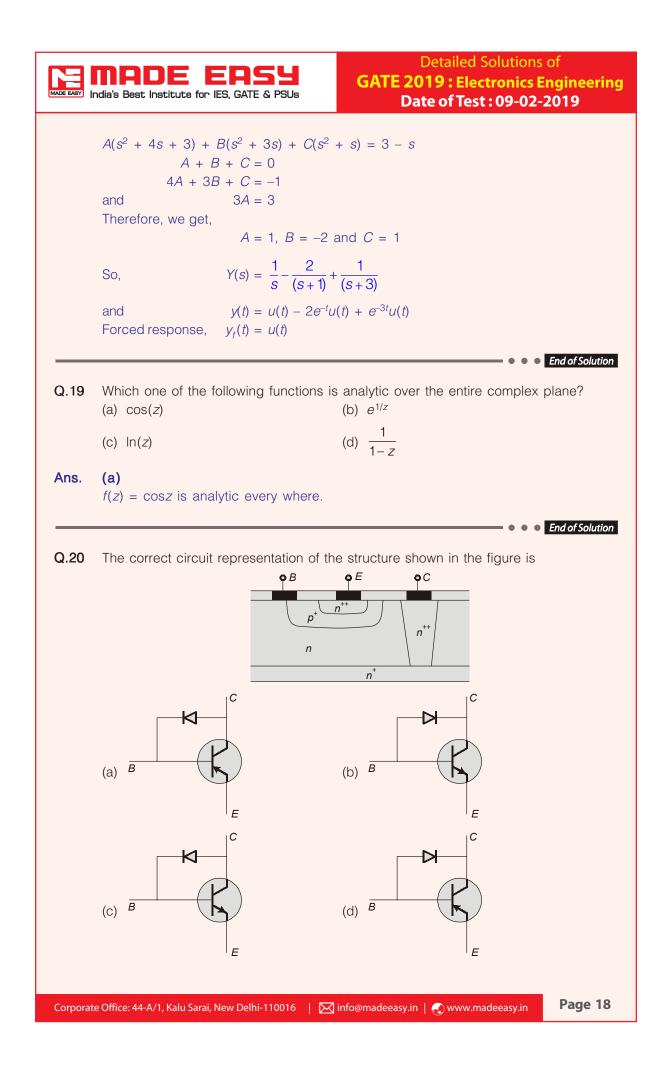


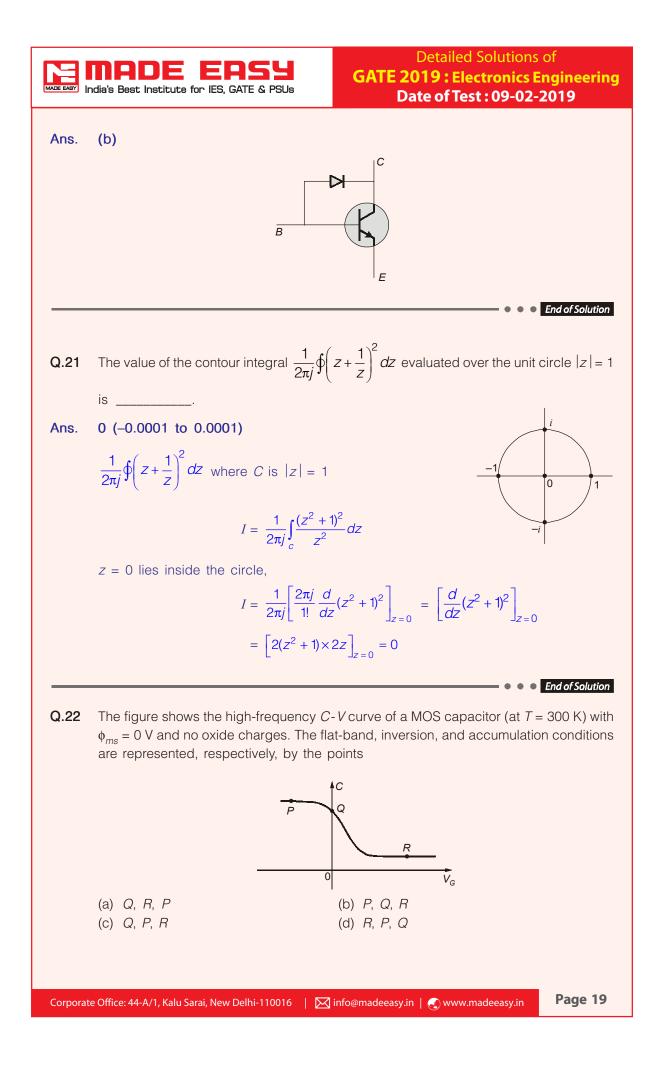
















Admission Open in Classroom Course for RRB-JE (CBT-1)

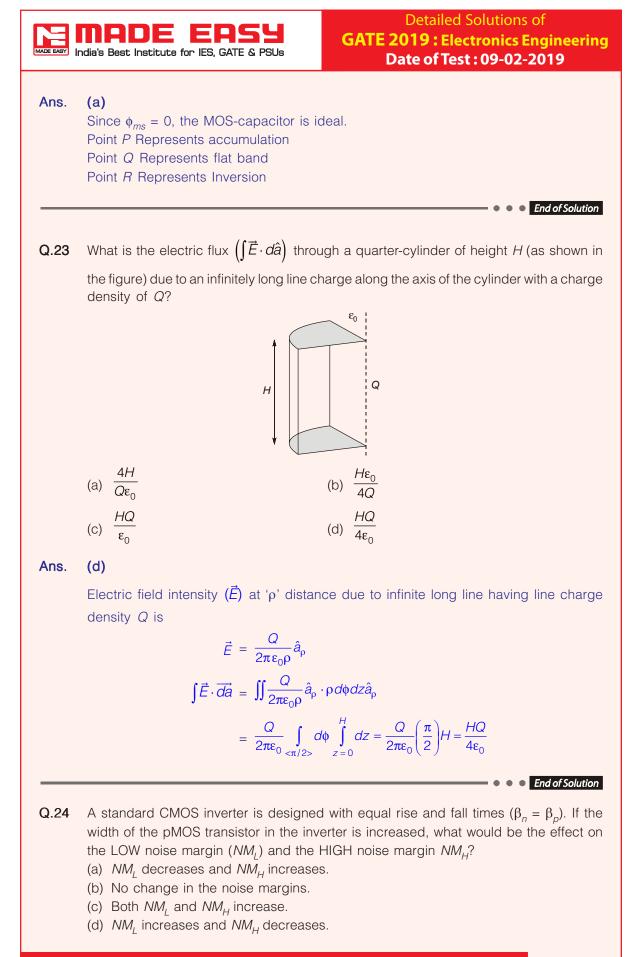
Classroom Centres : Delhi Lucknow Patna

Batches commencing from 15th Feb, 2019 All streams are eligible

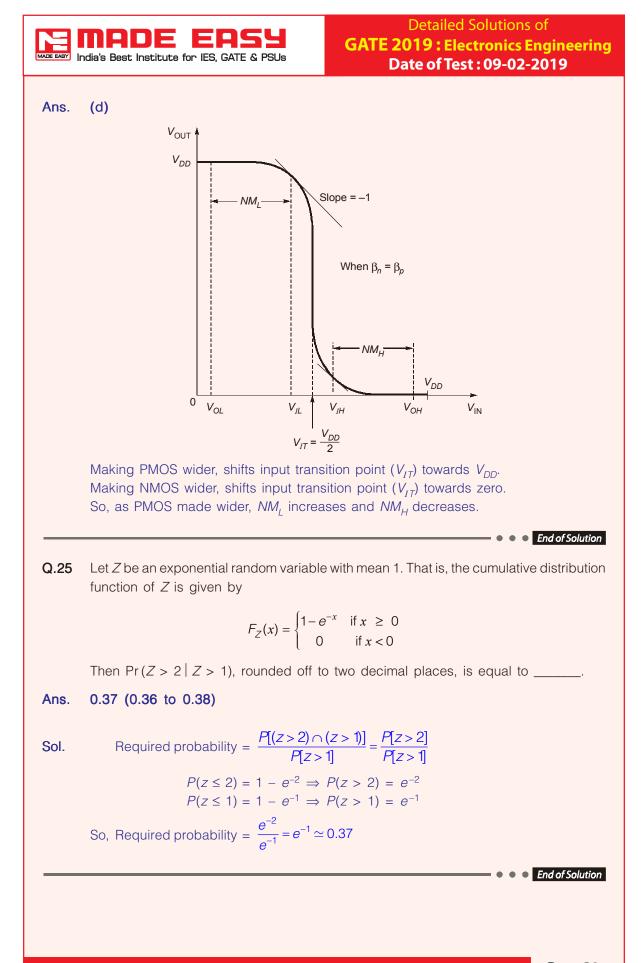
Fee Structure : Classroom Course (RRB-JE)					
Exam	Course Duration	Timing	Freshers	Ex. MADE EASY Students Enrolled in any classroom/postal courses	
CBT 1 (only)	50 Days (180-200 hours)	7 days a week 4 hours per day	Rs. 10,170 + GST = Rs. 12,000/-	Rs. 7,628 + GST = Rs. 9,000/-	

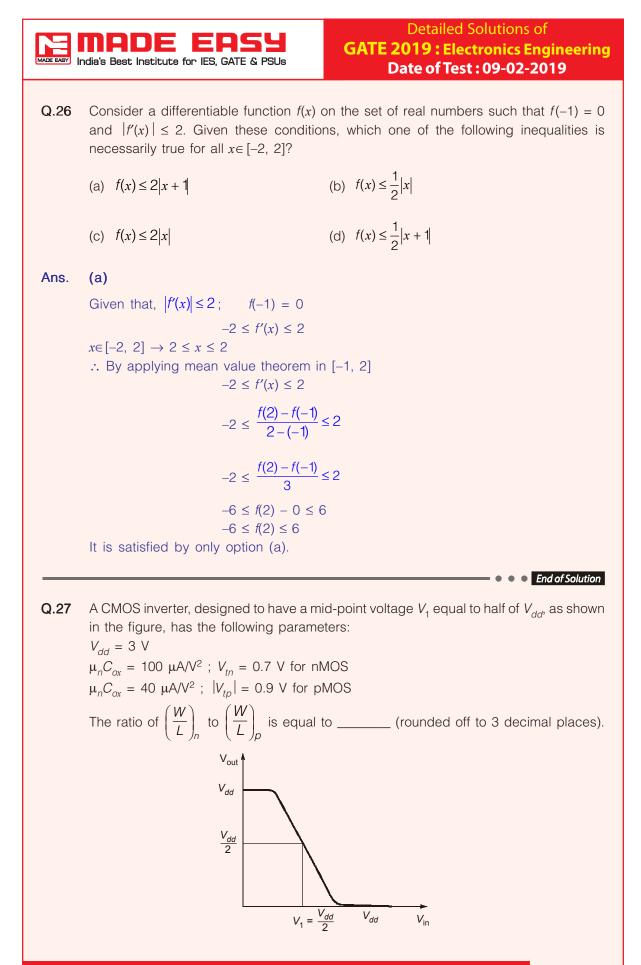
Delhi : 7303231082, 7303212325 Lucknow: 09919111168, 08400029422 **Patna :** 0612-2356615, 9955991166

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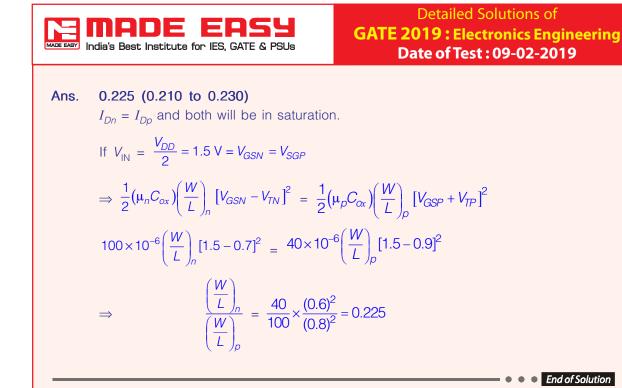


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Q.28 In an ideal pn junction with an ideality factor of 1 at T = 300 K, the magnitude of the reverse-bias voltage required to reach 75% of its reverse saturation current, rounded off to 2 decimal places, is_____ mV.

$$[k = 1.38 \times 10^{-23} \text{ JK}^{-1}, h = 6.625 \times 10^{-34} \text{ J-s}, q = 1.602 \times 10^{-19} \text{C}]$$

input of the form

 \Rightarrow

Q.29

$$V_{T} = \frac{kT}{q} = \frac{1.38 \times 10^{-23} \times 300}{1.602 \times 10^{-19}} \text{ V} = 25.843 \text{ mV}$$
$$I = I_{0} \left(e^{V/V_{T}} - 1 \right) = -\frac{3}{4} I_{0}$$
$$V = V_{T} \text{ In } 0.25 = -35.83 \text{ mV}$$
$$V_{R} = |V| = 35.83 \text{ mV}$$

It is desired to find a three-tap causal filter which gives zero signal as an output to an

$$x[n] = c_1 \exp\left(-\frac{j\pi n}{2}\right) + c_2 \exp\left(\frac{j\pi n}{2}\right),$$

where c_1 and c_2 are arbitrary real numbers. The desired three-tap filter is given by h[0] = 1, h[1] = a, h[2] = band

$$h[n] = 0$$
 for $n < 0$ or $n > 2$

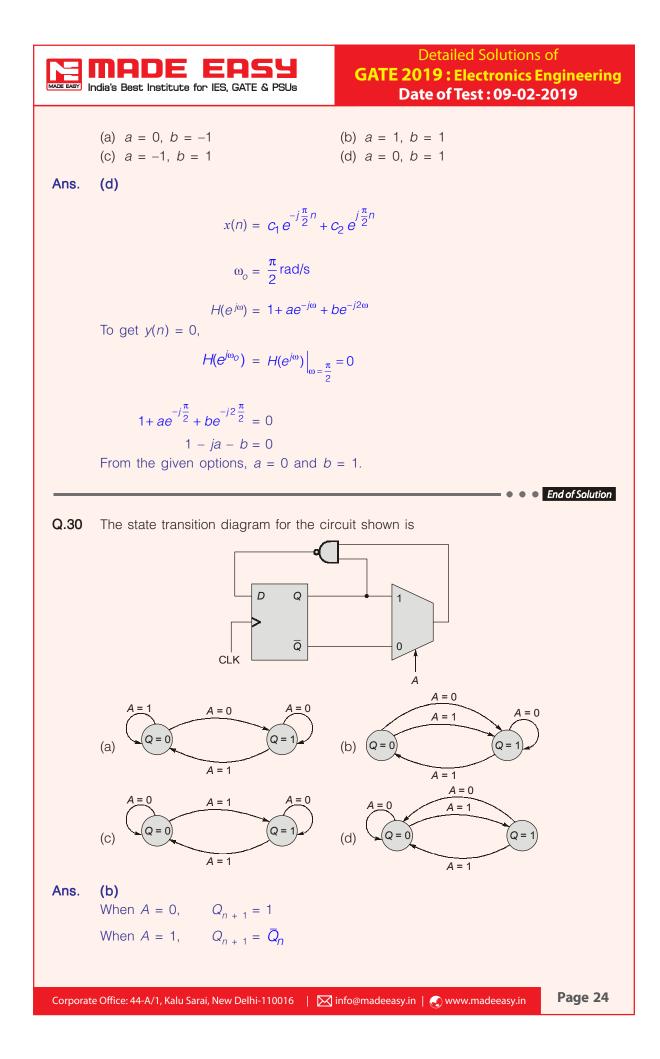
What are the values of the filter taps a and b if the output y[n] = 0 for all n, where x[n]is as given above?

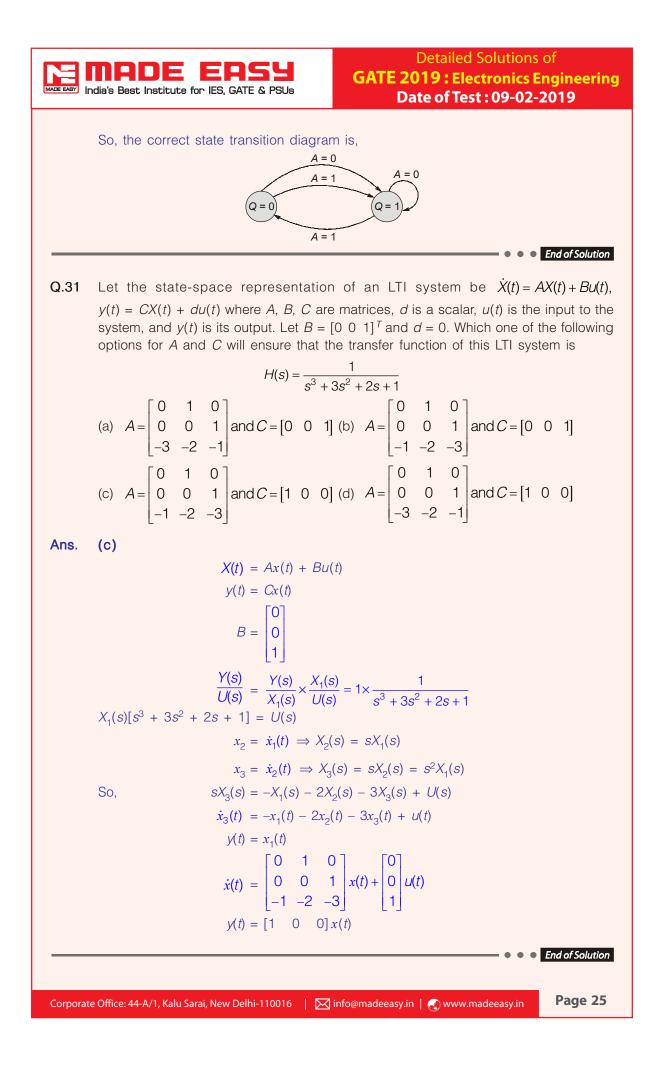
$$x[n] \qquad n = 0 \qquad y[n] = 0 \\ h[n] = \{1, a, b\}$$

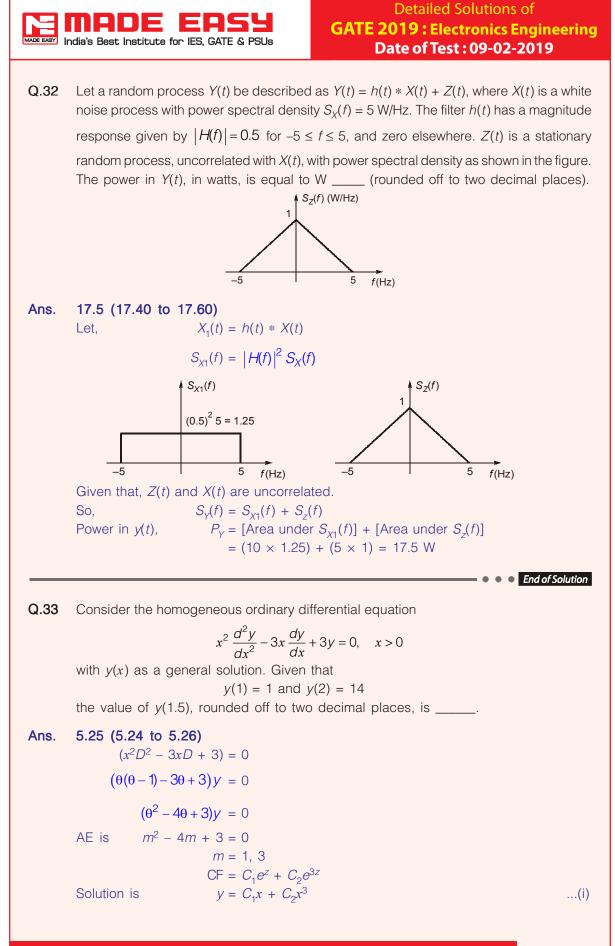
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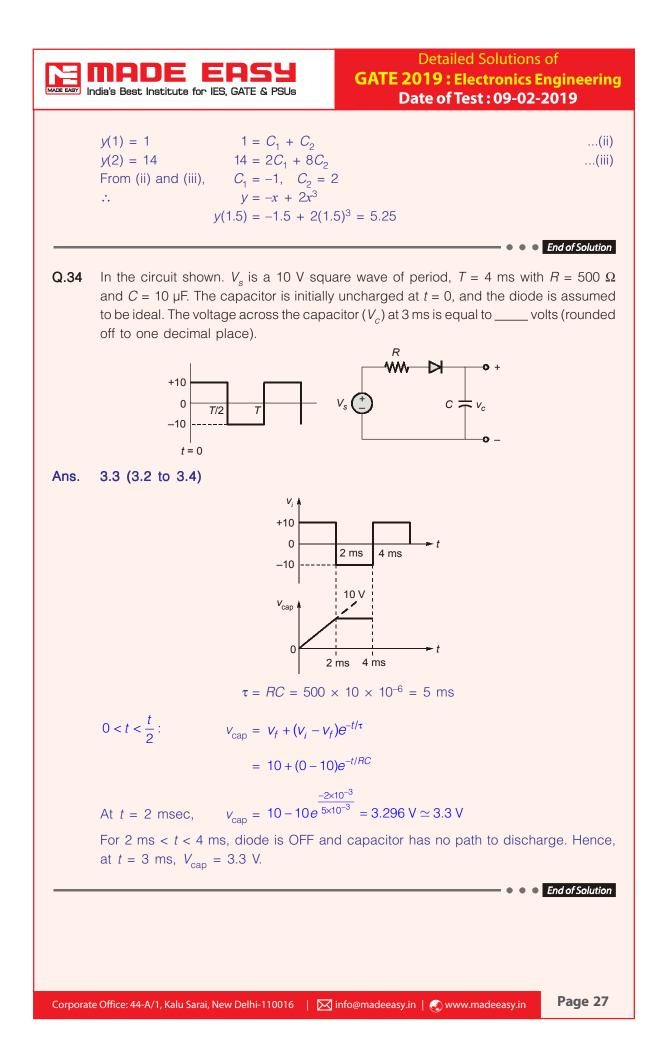
Page 23

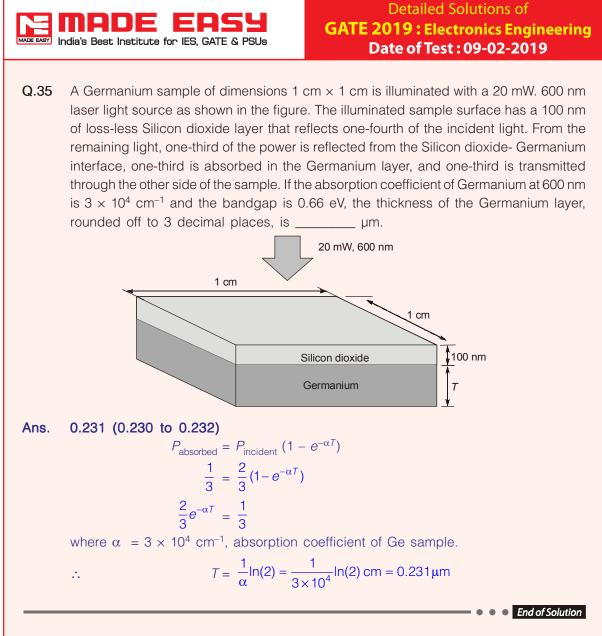
End of Solution











Q.36 A rectangular waveguide of width w and height h has cut-off frequencies for TE₁₀ and TE₁₁ modes in the ratio 1 : 2. The aspect ratio w/h, rounded off to two decimal places, is _____.

Ans. 1.732 (1.71 to 1.75)

For TE₁₀ mode,

$$f_{cmn} = \frac{c}{2} \sqrt{\left(\frac{m}{a}\right)^2 + \left(\frac{n}{b}\right)^2}$$
$$f_{c10} = \frac{c}{2w}$$

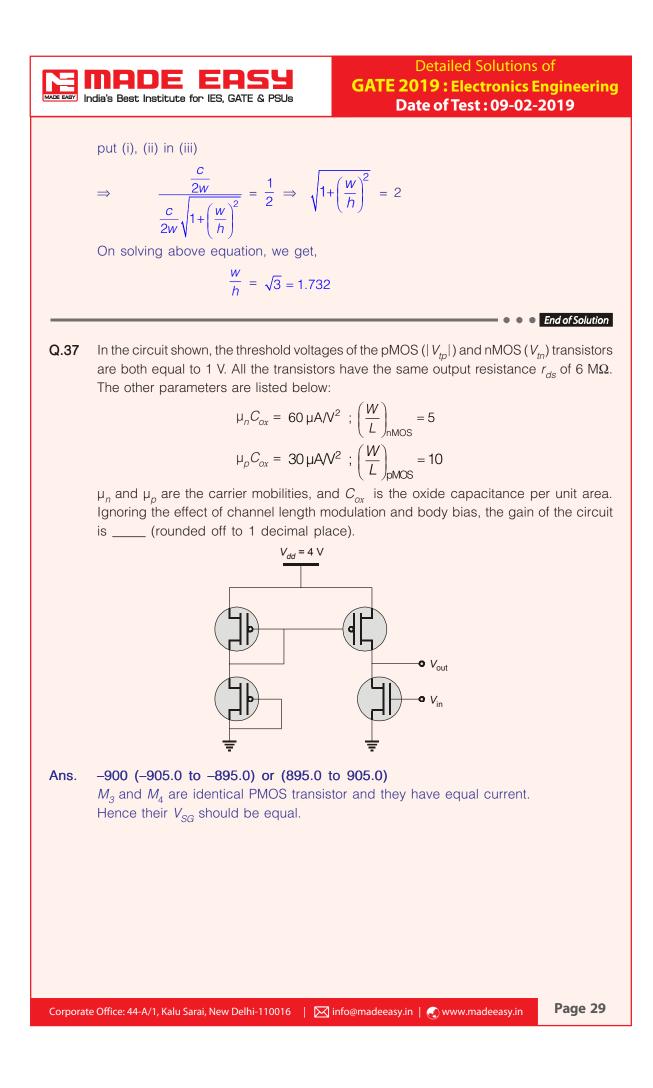
...(i)

and For TE₁₁ mode,
$$f_{c11} = \frac{c}{2}\sqrt{\left(\frac{1}{w}\right)^2 + \left(\frac{1}{h}\right)^2} = \frac{c}{2w}\sqrt{1 + \left(\frac{w}{h}\right)^2}$$
 ...(ii)

given,

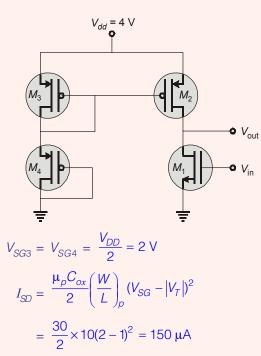
 $\frac{f_{c10}}{f_{c11}} = \frac{1}{2}$...(iii)

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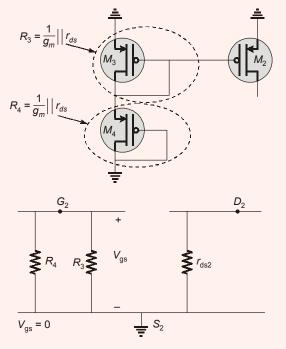
For M_1

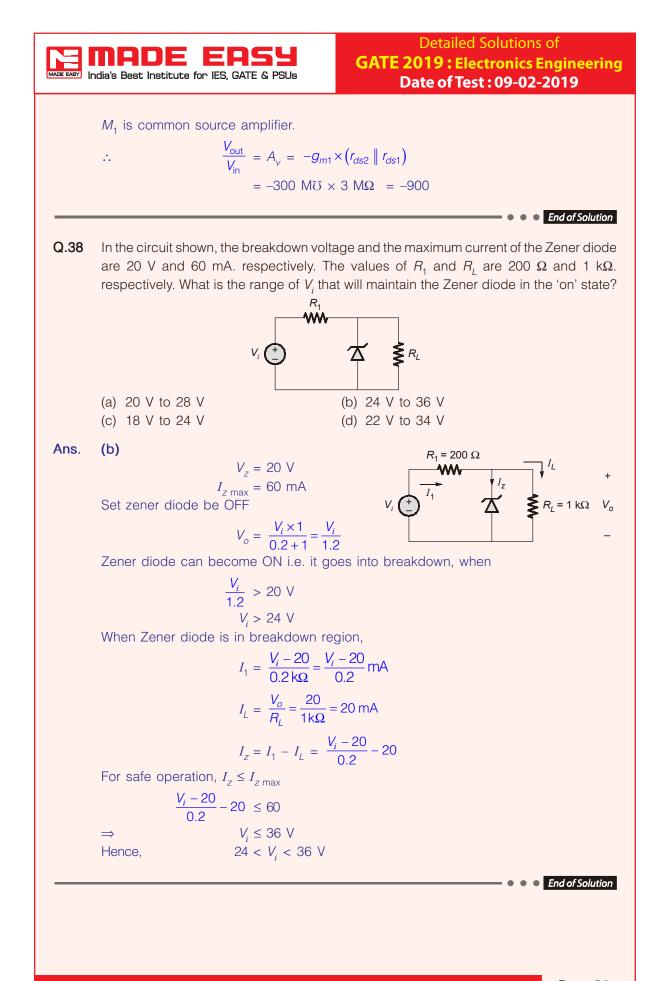


now, by using current mirror property all transistor should have equal current.

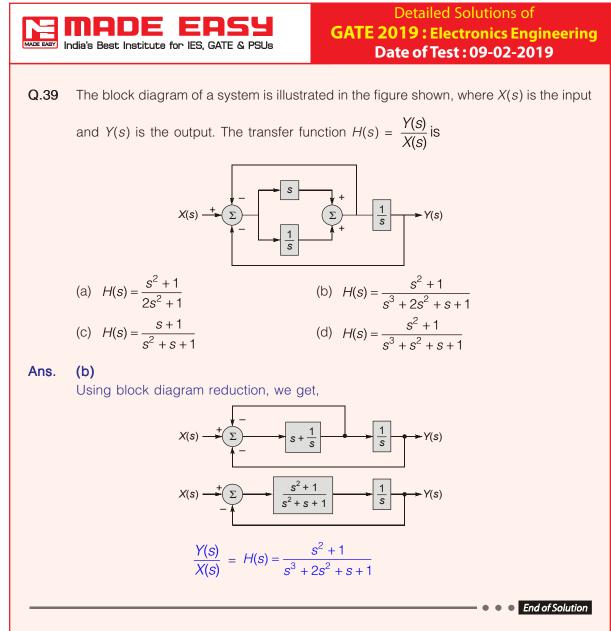
$$I_{\text{DSN}} = I_{\text{SDP}} = 150 \ \mu\text{A}$$
$$g_{m1} = \sqrt{2\mu_n C_{ox} \frac{W}{L} \times I_{DS}}$$
$$= \sqrt{2\mu_n C_{ox} \frac{W}{L} \times I_{DS}} = \sqrt{2 \times 60 \times 5 \times 150} = 300 \ \mu\text{G}$$

 M_2 , M_3 and M_4 from active load for M_1 . This active load in equivalent to resistance r_{ds2} i.e. 6 M Ω .





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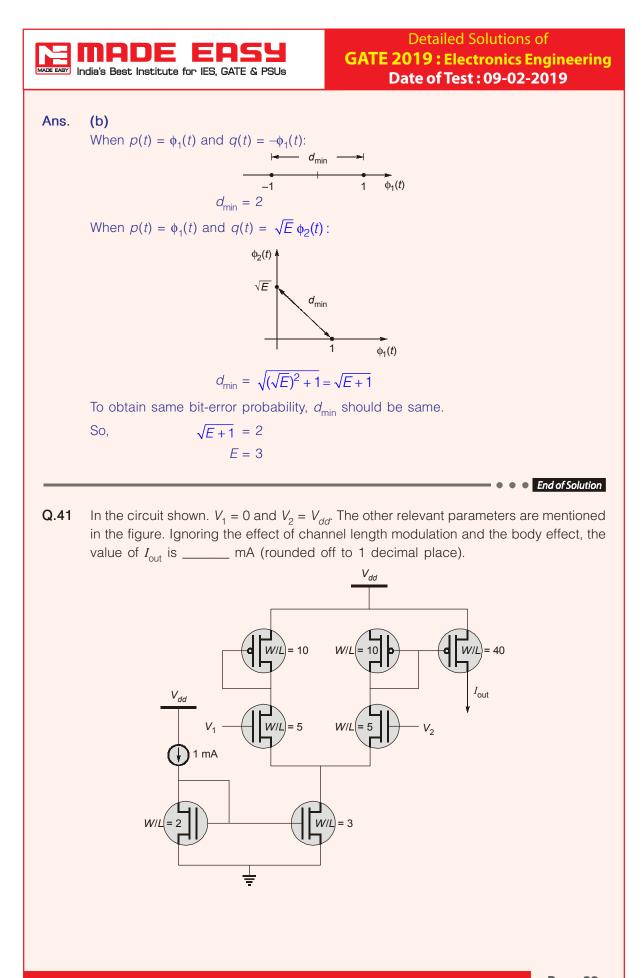
Q.40 A single bit, equally likely to be 0 and 1, is to be sent across an additive white Gaussian noise (AWGN) channel with power spectral density $N_o/2$. Binary signaling, with $0 \rightarrow p(t)$ and $1 \rightarrow q(t)$, is used for the transmission, along with an optimal receiver that minimizes the bit-error probability.

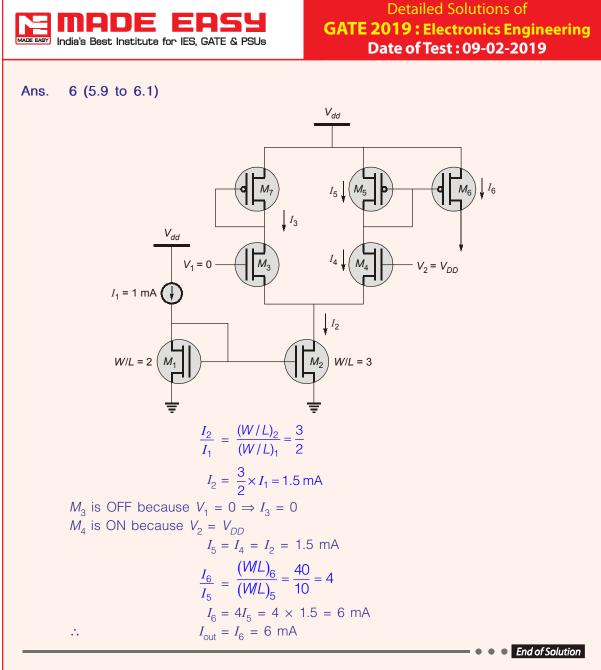
Let $\varphi_1(t), \varphi_2(t)$ form an orthonormal signal set.

If we choose $p(t) = \varphi_1(t)$ and $q(t) = -\varphi_1(t)$, we would obtain a certain bit-error probability P_b .

If we keep $p(t) = \varphi_1(t)$, but take $q(t) = \sqrt{E} \varphi_2(t)$, for what value of *E* would we obtain the same bit-error probability P_b ?

(a)	0	(b)	3
(C)	1	(d)	2



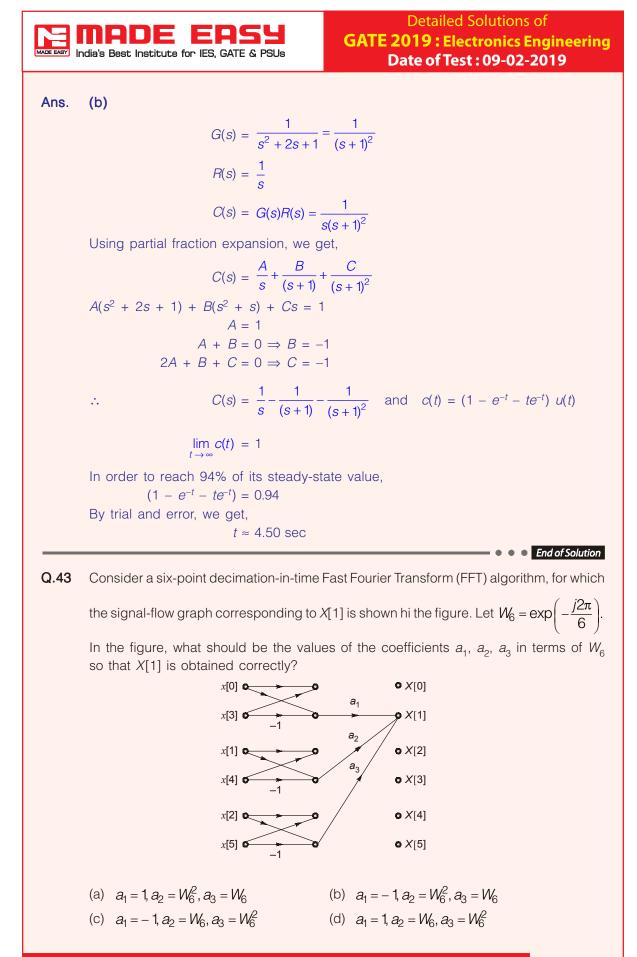


Q.42 Consider a causal second-order system with the transfer function

$$G(s) = \frac{1}{1+2s+s^2}$$

with a unit-step $R(s) = \frac{1}{s}$ as an input. Let C(s) be the corresponding output. The time taken by the system output c(t) to reach 94% of its steady-state value $\lim_{t \to \infty} c(t)$, rounded off to two decimal places, is (a) 5.25 (b) 4.50 (c) 2.81 (d) 3.89

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Step 1 Select Your Stream



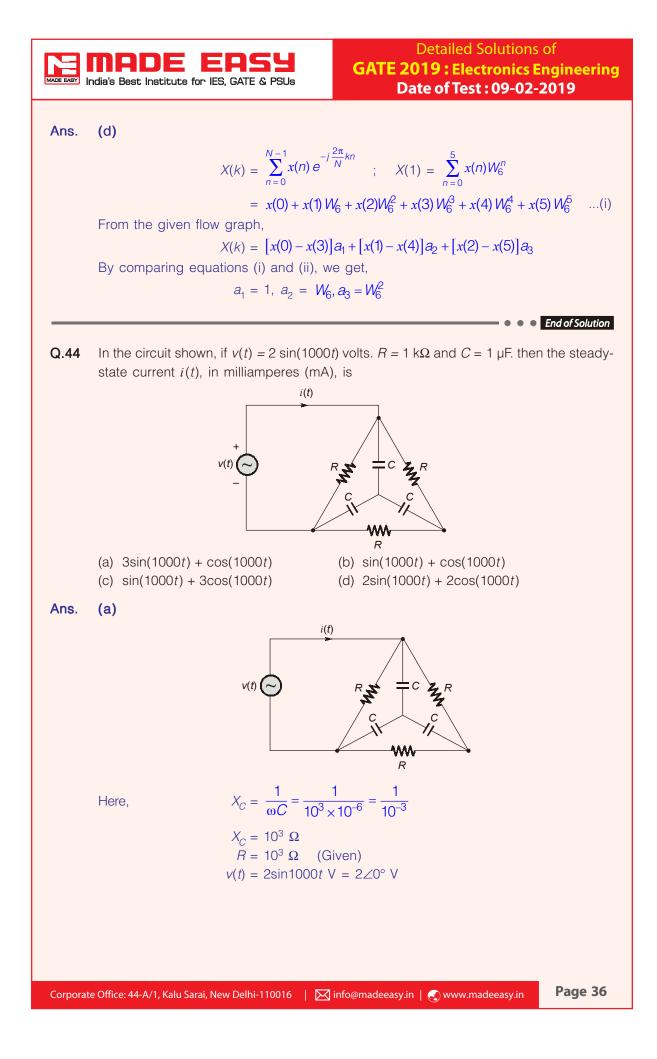
Step 2 Select the session you appeared for

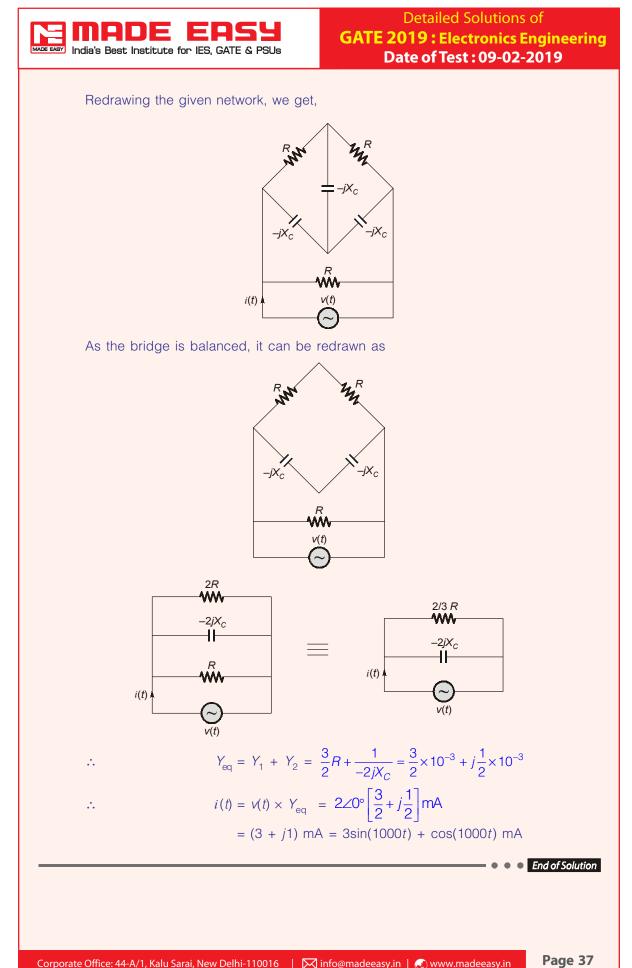


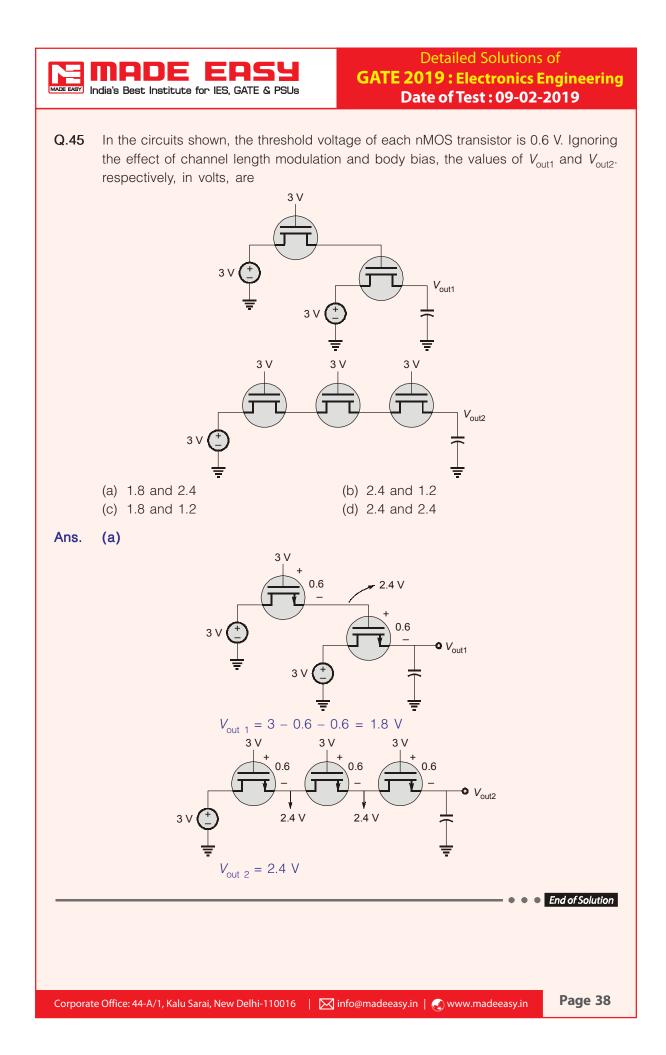
Step 3 Paste the URL of your Response Sheet

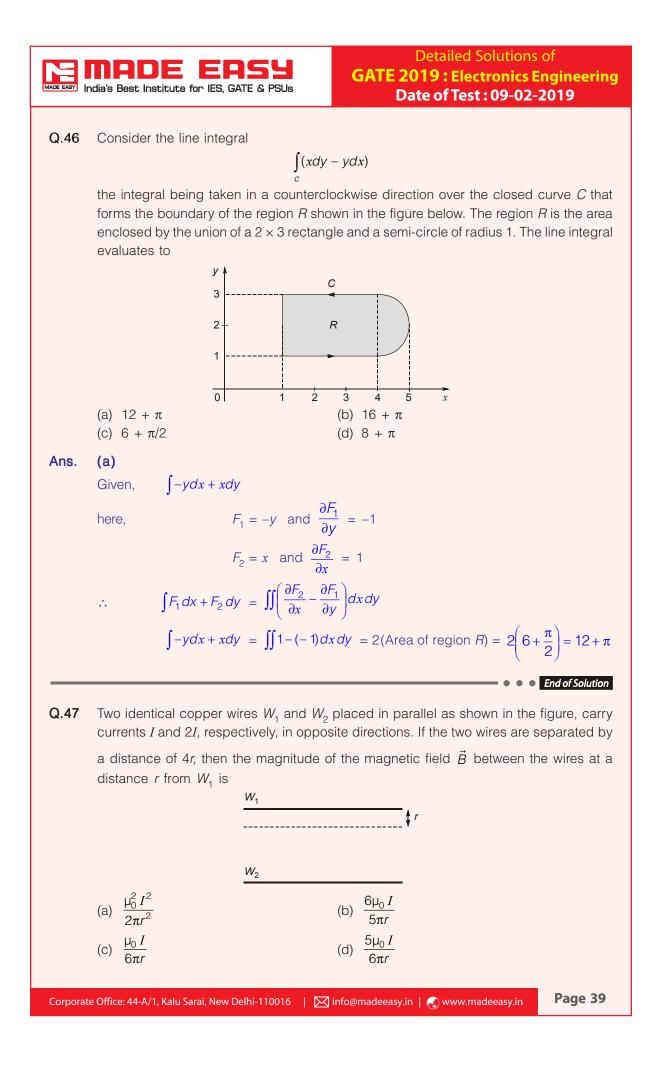
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General Studies & Engineering Aptitude Batches for ESE 2020 (Preliminary Examination)



ADMISSION OPEN

Syllabus Covered

- 1. Current issues of national and international importance relating to social economic and industrial development.
- 2. Engineering Aptitude covering Logical reasoning and Analytical ability.
- 3. Engineering Mathematics and Numerical Analysis.
- 4. General Principles of Design, Drawing, Importance of Safety.
- 5. Standards and Quality practices in production, construction, maintenance and services.
- 6. Basic of Energy and Environment : Conservation, Environmental pollution and degradation, Climate Change, Environmental impact assessment.
- 7. Basic of Project Management.
- 8. Basics of Material Science and Engineering.
- 9. Information and Communication Technologies (ICT) based tools and their applications in Engineering such as networking, e-governance and technology based education.
- 10. Ethics and values in engineering profession.

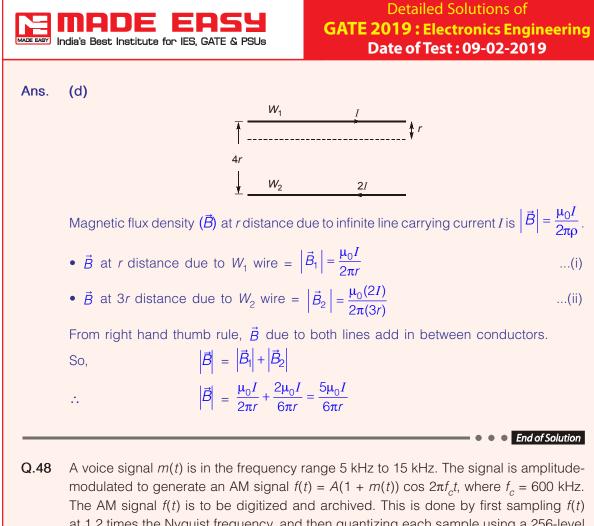
Course Duration	Ì	Timings	i	Teaching Hours
Regular Batches : 2.5 months Weekend Batches : 4 months		Regular : 6 to 7 days a week and 4-6 hours a day Weekend : Sat, Sun & public holiday, 8 hours each day		250-300 hours

Batch Type	Commencing Dates	Venue	Timing
Regular Batch	20 th Feb, 2019	Ghitorni (Delhi)	8:00 AM to 12:00 PM
Weekend Batch	24 th Feb, 2019	Ghitorni (Delhi)	8:00 AM to 5:00 PM
Weekend Batch	24 th Feb, 2019	Noida Centre	8:00 AM to 5:00 PM

Fee Structure				
Non-MADE EASY Students	Ex. MADE EASY Students Enrolled in Postal, Rank Improvement, Mains, GS, GATE, GATE + ESE Batches			
₹ 25,000 • GS & Engg Aptitude Books will be issued.	 • GS & Engg Aptitude Books will NOT be issued. • Interested students can avail books by paying the fee of Rs. 2,000/- 			

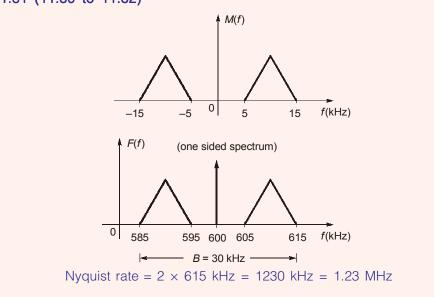
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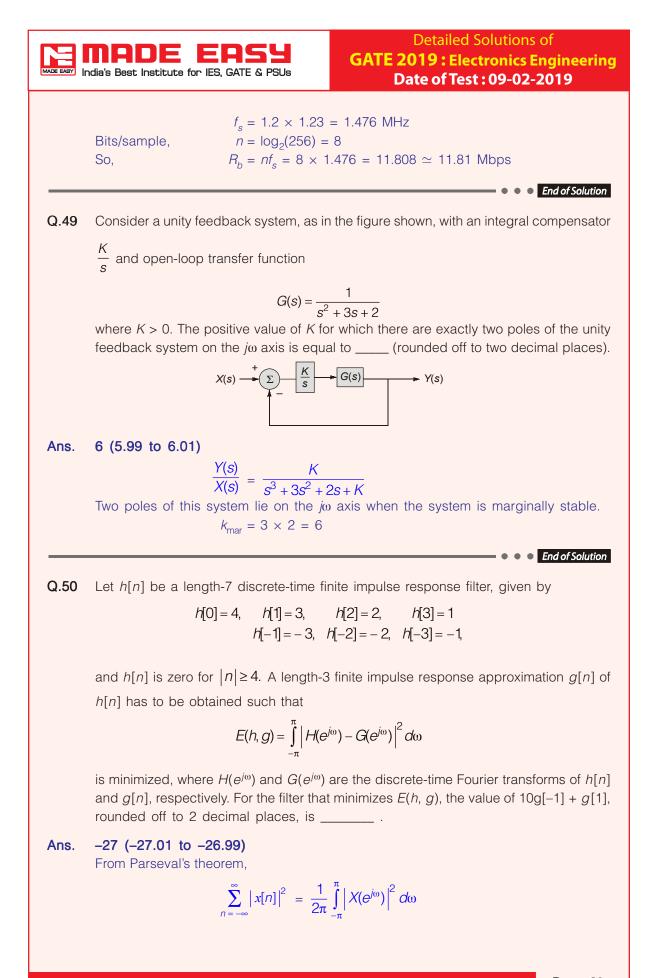
Noida office : D-28, Sector - 63, Noida, Uttar Pradesh - 201301 🕓 0120-6524612, 08860378009

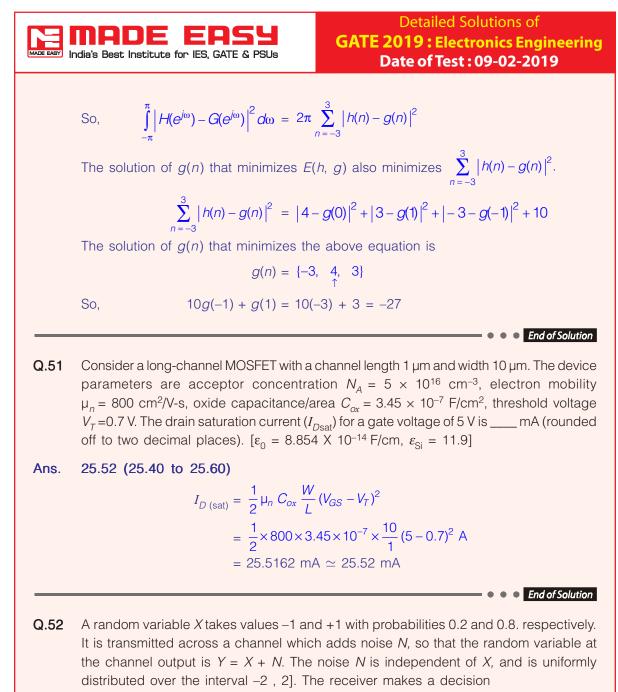


at 1.2 times the Nyquist frequency, and then quantizing each sample using a 256-level quantizer. Finally, each quantized sample is binary coded using *K* bits, where *K* is the minimum number of bits required for the encoding. The rate, in Megabits per second (rounded off to 2 decimal places), of the resulting stream of coded bits is _____ Mbps.

Ans. 11.81 (11.80 to 11.82)







$$\widehat{X} = \begin{cases} -1, & \text{if } Y \le \theta \\ +1, & \text{if } Y < \theta \end{cases}$$

where the threshold $\theta \in [-1, 1]$ is chosen so as to minimize the probability of error

 $\Pr[\hat{X} \neq X]$. The minimum probability of error, rounded off to 1 decimal place, is _____.

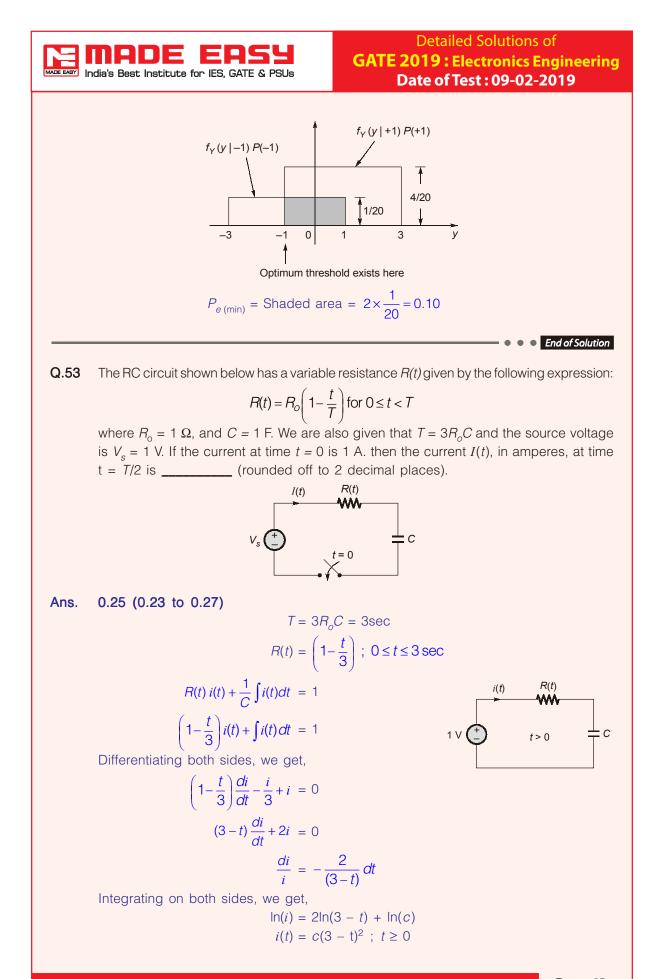
Ans. (0.10)

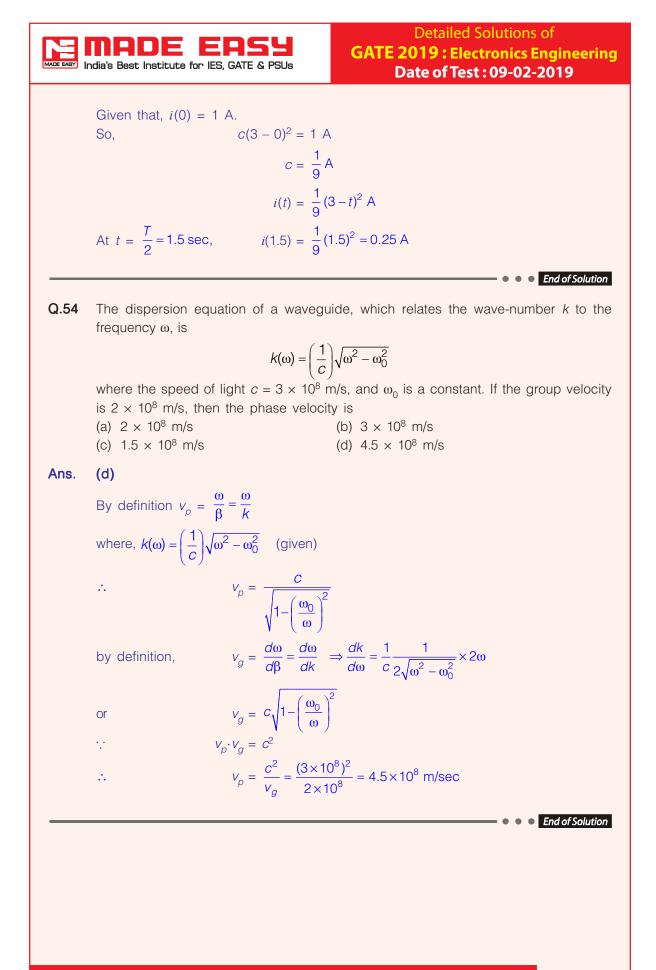
MAP criteria should be used to minimise the probability of error.

$$f_{Y}(y \mid +1) P(+1) \underset{-1}{\overset{+1}{\geq}} f_{Y}(y \mid -1) P(-1)$$

 $P(+1) = 0.80 \text{ and } P(-1) = 0.20$

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	MADE EASY ndia's Best Institute for IES, GATE & PSUs	Detailed Solutions of GATE 2019 : Electronics Engineering Date of Test : 09-02-2019
Q.55	The quantum efficiency (η) and response photodetector are related by (a) $R = \frac{1.24 \times \lambda}{\eta}$ (c) $R = \frac{\lambda}{\eta \times 1.24}$	nsivity (<i>R</i>) at a wavelength λ (in μ m) in a p-i-n (b) $R = \frac{\eta \times \lambda}{1.24}$ (d) $R = \frac{1.24}{\eta \times \lambda}$
Ans.	(b) $\eta = \frac{I_{out}}{q} \times \frac{hf}{P_{in}}$ $R = \frac{I_{out}}{P_{in}}$ So, $R = \eta \times \frac{q}{hf} = \eta \times$ If λ is given in μ m, then $R = \eta \lambda \times \frac{q \times 10}{hc}$ $\frac{hc}{q \times 10^{-6}} \simeq 1.24$	
	So, $R = \frac{\eta \lambda}{1.24}$	• • • End of Solution

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