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ESE 2020 : Mains Test Series

UPSC ENGINEERING SERVICES EXAMINATION

Electronics & Telecommunication Engineering

Test-2: Network Theory + Microprocessors and Microcontroller (All Topics)
Digital Circuits-1 + Control Systems-1 (Part Syllabus)

Name : _____

Roll No :

Test Centres

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Student's Signature**Instructions for Candidates**

- Do furnish the appropriate details in the answer sheet (viz. Name & Roll No).
- Answer must be written in English only.
- Use only black/blue pen.
- The space limit for every part of the question is specified in this Question Cum Answer Booklet. Candidate should write the answer in the space provided.
- Any page or portion of the page left blank in the Question Cum Answer Booklet must be clearly struck off.
- Last two pages of this booklet are provided for rough work. Strike off these two pages after completion of the examination.

FOR OFFICE USE

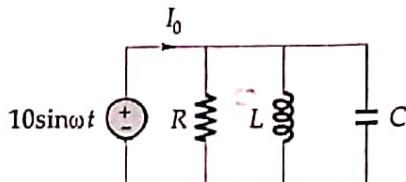
| Question No. | Marks Obtained |
|---------------------------------|----------------|
| Section-A | |
| • Q.1 | |
| Q.2 | |
| Q.3 | |
| • Q.4 | |
| Section-B | |
| • Q.5 | |
| • Q.6 | |
| • Q.7 | |
| Q.8 | |
| Total Marks Obtained | 248 |

Signature of Evaluator

Cross Checked by

Section A : Network Theory + Microprocessors and Microcontroller

Q.1 (a) In the parallel RLC circuit shown below, let $R = 8 \text{ k}\Omega$, $L = 0.2 \text{ mH}$ and $C = 8 \mu\text{F}$.



- Calculate resonance frequency ω_0 , quality factor Q and bandwidth B .
- Find half-power frequencies ω_1 and ω_2 .
- Determine the power dissipated at resonance frequency ω_0 , half-power frequencies ω_1 and ω_2 .

[12 marks]

Sol.

(i) For parallel R-L-C circuit, resonance frequency ω_0 is given as.

$$\omega_0 = \frac{1}{\sqrt{LC}} = \frac{1}{\sqrt{0.2 \times 10^{-3} \times 8 \times 10^{-6}}} \\ \boxed{\omega_0 = 25 \times 10^3 \text{ rad/sec}} \quad \text{Ans}$$

also

$$B.W = \frac{1}{RC} = \frac{1}{8 \times 10^3 \times 8 \times 10^{-6}} \\ \boxed{B.W = 15.625 \text{ rad/s}} \quad \text{Ans}$$

and

$$Q = \frac{\omega_0}{B.W} = \frac{25 \times 10^3}{15.625} \\ \boxed{Q = 1600} \quad \text{Ans}$$

(ii) we know $Q \geq 10$, so half power frequencies ω_1 & ω_2 are given as

$$\omega_1 = \omega_0 - \frac{B}{2} = \pm 6$$

$$\omega_1 = 25 \times 10^3 - \frac{15.625}{2}$$

$$\omega_1 = 24992.18 \text{ rad/sec} \quad \text{Ans.}$$

and $\omega_2 = \omega_0 + \frac{\beta}{2} = 25000 + \frac{15.625}{2}$

$$\omega_2 = 25007.81 \text{ rad/sec} \quad \text{Ans.}$$

(iii)

$$I_0 = \frac{10 \sin \omega t}{8 \text{ k}\Omega}$$



$$I_0 = 1.25 \sin \omega t \text{ mA}$$

$$|I_0| = 1.25 \text{ mA}$$

at ω_0 power dissipated

$$\begin{aligned} P_{\omega_0} &= \frac{1}{2} |I_0|^2 \cdot R \\ &= \frac{1}{2} |1.25 \text{ mA}|^2 \times 8 \text{ k}\Omega \end{aligned}$$

$$P_{\omega_0} = 6.25 \text{ mW} \quad \text{Answer.}$$

At half power $\omega_1, \omega_2, I'_0 = \frac{I_0}{\sqrt{2}}$.

$$\begin{aligned} P_{\omega_1} &= \frac{1}{2} |I'_0|^2 \cdot R \\ &= \frac{1}{2} \times \frac{|I_0|^2}{2} \times R \end{aligned}$$



$$P_{\omega_1} = \frac{P_{\omega_0}}{2}$$

$$P_{\omega_1} = \frac{6.25 \text{ mW}}{2}$$

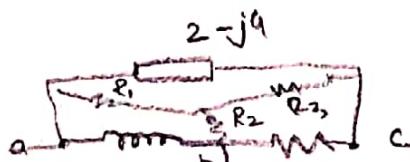
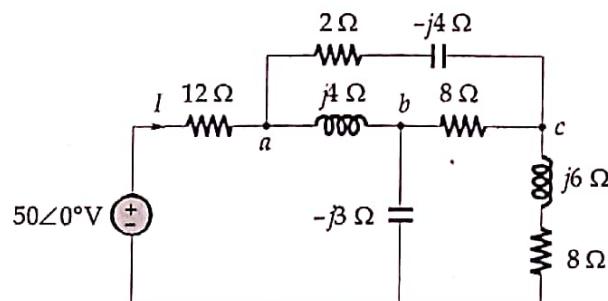
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$$P_{\omega_1} = 3.125 \text{ mW}$$

$$P_{\omega_L} = P_{\omega_1} = 3.125 \text{ mW} \quad \underline{\text{Ans.}}$$



Q.1(b) Find current I in the circuit shown below:



[12 marks]

$$Z_1 = \frac{(2-j4)j4}{2-j4 + j4 + 8} = \frac{(2-j4)j4}{10} = \frac{16+8j}{10}$$

$$Z_2 = \frac{j4 \cdot 8}{10} = \frac{32j}{10} \quad Z_3 = \frac{(2-j4)8}{10}$$

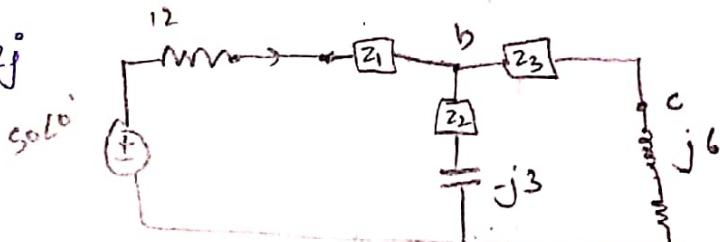
$$Z_3 = \frac{16-32j}{10}$$

Redraw given circuit

$$\bullet j6 + 8 + \frac{16-32j}{10} = \frac{48}{5} + 2.8j$$

$$\bullet \frac{32j}{10} + -j3 = 0.2j$$

$$\bullet \frac{16+8j}{10} + 12 \\ = 13.6 + 0.8j$$



Total impedance

$$Z = (13.6 + 0.8j) + \left(\frac{0.2j \times (9.6 + 2.8j)}{9.6 + 2.8j + 0.2j} \right)$$

$$Z = (13.6 + 0.8j) + \frac{-0.56 + 1.92j}{9.6 + 0.3j}$$

Using calculator we get

$$Z = 13.603 + 0.9988j$$

current $i = \frac{50 \angle 0^\circ V}{13.603 + 0.9988j}$

$$i = \frac{50 \angle 0^\circ}{13.6404 \angle 4.199^\circ}$$

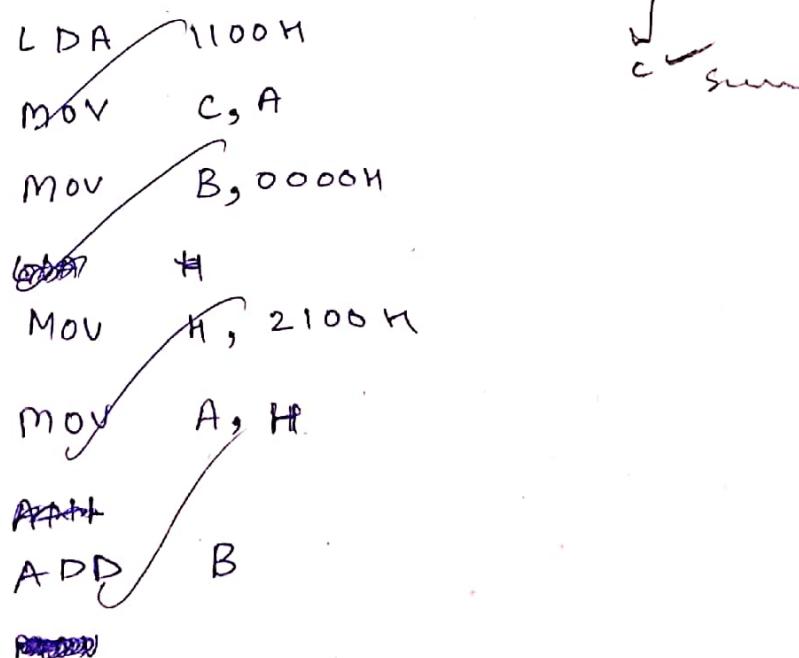
$$i = 3.66 \angle -4.2^\circ A \quad \text{Ans.}$$



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- Q.1 (c) Write a program for an 8085 microprocessor to calculate the sum of series of number. The length of series is stored in the memory location 1100H and the sequence is stored starting from location 2100H. Assume the sum to be 8-bit number without any carry and store the result in location 3200H.

[12 marks]



LDA 1100H → load location 1100H
MOV C, A → initialize counter
MOV A, 00H → A = 00 (sum = 0)
LXI H, 2100H → load location of data

LOOP : ADD M → adding data

INX H → increment pointer

DCR C → decrement counter

JNZ LOOP → condition

MOV B, A

STOP STA 3200H → store result at 3200H

HLT

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Q.1(d)

Write a program for 8051 microcontroller to find the sum of 5 BCD data items that are stored in RAM locations starting from 40H.

[12 marks]

MOV R0, #40H *initialise location 40H*
MOV R1, #05H *→ counter 5 BCD*
MOV A, #00H *→ clear A*
MOV R2, A *→ 00 to R2*

BCD
↓
A01 / Y0A
↓

Loop1 : ADD A, @R0

DAA A

JNC Loop2

INC R2

Loop2 : INC R0

JNZ loop1

HLT

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Q.1 (e) The reduced incidence matrix of an oriented graph is given as:

$$\begin{bmatrix} 0 & -1 & 1 & 0 & 0 \\ 0 & 0 & -1 & -1 & -1 \\ -1 & 0 & 0 & 0 & 1 \end{bmatrix}$$

- (i) Draw its graph.
(ii) How many trees are possible for this graph?

[12 marks]

Sol.

(i)

Branch

node ↓

reduced →

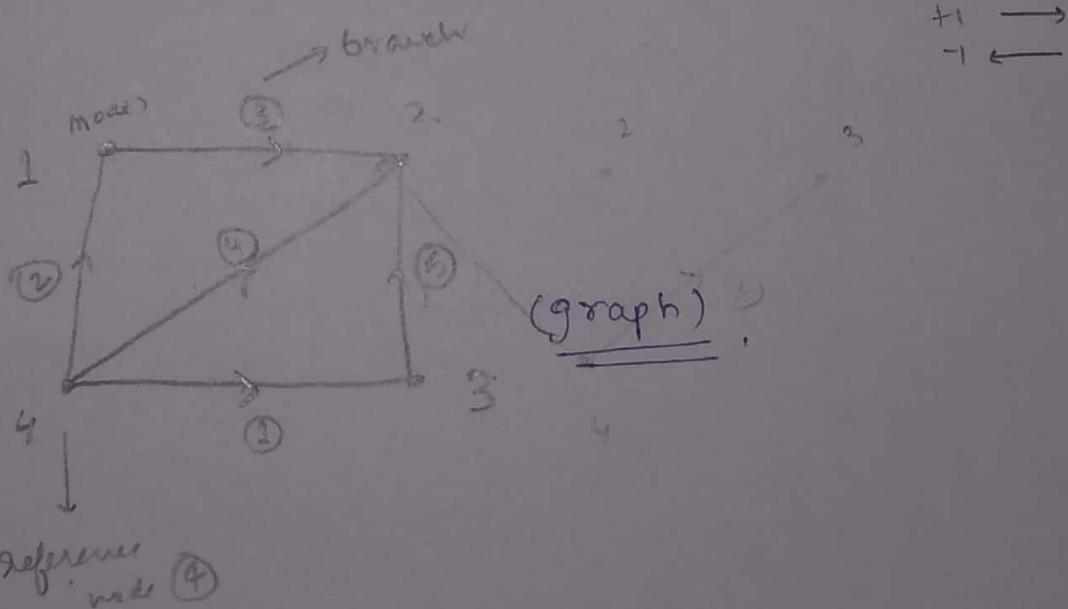
$$\begin{bmatrix} 0 & -1 & 1 & 0 & 0 \\ 0 & 0 & -1 & -1 & -1 \\ -1 & 0 & 0 & 0 & 1 \end{bmatrix}$$

full non reduced
incidence
matrix

node ↓

B

$$\begin{bmatrix} 1 & 2 & 3 & 4 & 5 \\ 0 & -1 & 1 & 0 & 0 \\ 0 & 0 & -1 & -1 & -1 \\ -1 & 0 & 0 & 0 & 0 \\ +1 & +1 & 0 & +1 & *0 \end{bmatrix}$$



$$(ii) \text{Tree} = \det(A A^T) = \begin{bmatrix} 0 & -1 & 1 & 0 & 0 \\ 0 & 0 & -1 & -1 & -1 \\ -1 & 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 & 0 & -1 \\ -1 & 0 & 0 \\ 1 & -1 & 0 \\ 0 & 1 & 0 \\ 0 & -1 & 1 \end{bmatrix}$$

Tree = $\det \begin{bmatrix} 2 & -1 & 0 \\ -1 & 3 & -1 \\ 0 & -1 & 2 \end{bmatrix}$

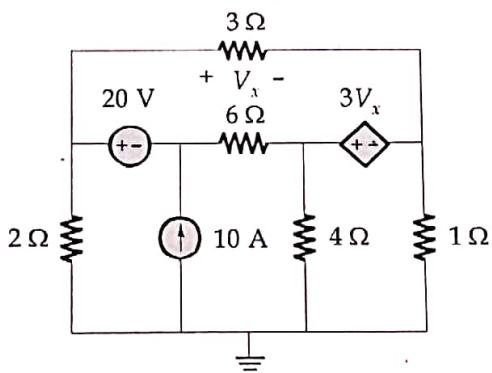
$$\text{Tree} = 2(6 - 1) + 1(-2 - 0) + 0()$$

$$\text{Tree} = 10 - 2$$

B Total no.s of tree = 8 Answe✓

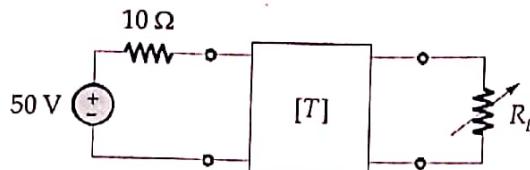
11

Q.2 (a) Find the node voltages in the circuit shown below:



[20 marks]

- Q.4 (a) The ABCD parameters of the two-port network in figure are $\begin{bmatrix} 4 & 20 \Omega \\ 0.1S & 2 \end{bmatrix}$



The output port is connected to a variable load for maximum power transfer. Find R_L and the maximum power transferred.

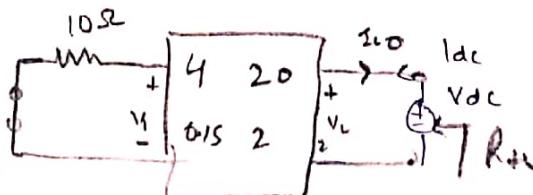
[20 marks]

Maximum power transferred when load is equal to its short Thevenin resistance equivalent and

$$\text{Power} = \frac{V_{th}^2}{4R_{th}}$$

- For R_{th} ($R_L = R_{th}$)

$$V = 0;$$



From given ABCD parameters,

$$\begin{aligned} V_1 &= 4V_2 - 20I_2 \\ I_1 &= 0.1V_2 - 2I_2 \end{aligned} \quad] \rightarrow (A)$$

from figure, $V_1 = -10I_1$

$$V_2 = V_{dc}, I_2 = I_{dc}$$

∴ we get

$$-10I_1 = 4V_{dc} + 20I_{dc} \quad (i)$$

$$I_1 = 0.1V_{dc} + 2I_{dc} \quad (ii)$$

$$\text{or } -10I_1 = 4V_{dc} + 20I_{dc}$$

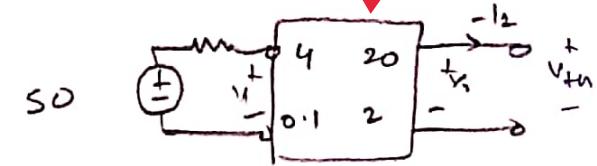
$$\begin{array}{l} 10I_1 = 10V_{dc} + 20I_{dc} \\ 0 = 5V_{dc} + 40I_{dc} \end{array}$$

$$\therefore \frac{V_{dc}}{I_{dc}} = 8 \Omega \Rightarrow R_{th}$$

$$R_L = R_{th} = 8 \Omega \quad \text{Ans.}$$

for V_{th} .

$$V_2 = V_{th} \quad \text{so} \quad I_2 = 0$$



from ACB parameters, equation A;

$$V_1 = 4V_{th}$$

$$I_1 = 0.1V_{th}$$

also from figure,

$$50 = 10I_1 + V_1$$

$$\therefore 50 = 10(0.1V_{th}) + 4V_{th}$$

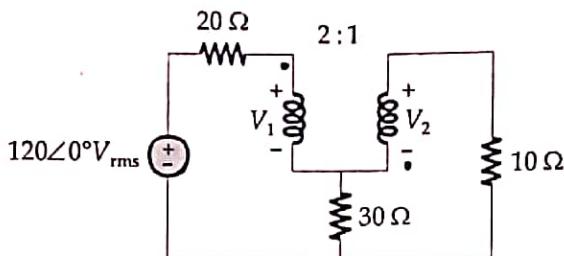
$$1V_{th} + 4V_{th} = 50$$

$$V_{th} = 10 \text{ V}$$

$$\text{Power transferred } P_{max} = \frac{V_{th}^2}{4R_{th}} = \frac{10^2}{4 \times 8}$$

$$P_{max} = 3.125 \text{ W} \quad \text{Ans.}$$

- Q.4(b) Calculate the power supplied to the $10\ \Omega$ resistor in the ideal transformer circuit of figure shown below:



[20 marks]

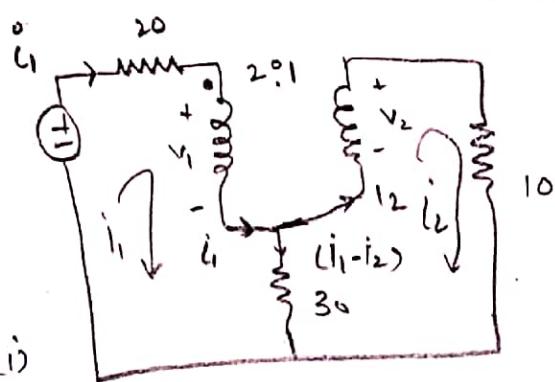
Sol

Applying KVL in

loop 1.

$$-120^{\circ} + 20i_1 + v_1 + 30i_1 - 30i_2 = 0$$

$$50i_1 - 30i_2 + v_1 = 120 \quad -(i)$$



Applying KVL in loop 2

$$-v_2 + 10i_2 + 30i_2 - 30i_1 = 0$$

$$40i_2 - 30i_1 = v_2 \quad -(ii)$$

and

$$-\frac{v_1}{e v_2} = \frac{2}{1}$$

$$v_1 = -2v_2 \quad \text{putting } v_2 \text{ from (ii)}$$

$$v_1 = -80i_2 + 60i_1$$

⇒ or

$$120 - 50i_1 + 30i_2 = -80i_2 + 60i_1$$

$$120 = -30i_1 + 30i_2$$

$$i_2 = i_1 + k$$

$$120 = 110i_1 - 110i_2$$

$$I_1 - I_2 = \frac{12}{11} \quad \text{also} \quad -\frac{I_1}{I_2} = \frac{1}{2} \text{ or } I_2 = -2I_1 \\ \text{or } I_1 = -\frac{I_2}{2}$$

$$\therefore -\frac{I_2 - I_1}{2} = \frac{12}{11}$$

$$-\frac{3I_2}{2} = \frac{12}{11} \Rightarrow I_2 = -0.7272 \text{ A}$$

Power supplied to 10Ω resistor is

$$P = (-0.7272)^2 \cdot 10$$

18

$$P = 5.29 \text{ W}$$

Answer



- Q.4 (c) Given is the list of 50 different numbers which are stored in consecutive memory location starting from 1000H. Write a program to search the given byte which is stored in the register 'C'. If the byte is found, then store the location of the byte in memory location 1100H and 1101H. If the byte is not found then store the value 00H at 1100H and 1101H and also draw the flow chart for the given program which is to be written for 8085 microprocessor.

[20 marks]

LXI H, 1000H

MVI B, 0032H ($32_{10} = 50$)

loop1: MOV A, M

CMP C

JZ loop2

INX H

DCR B

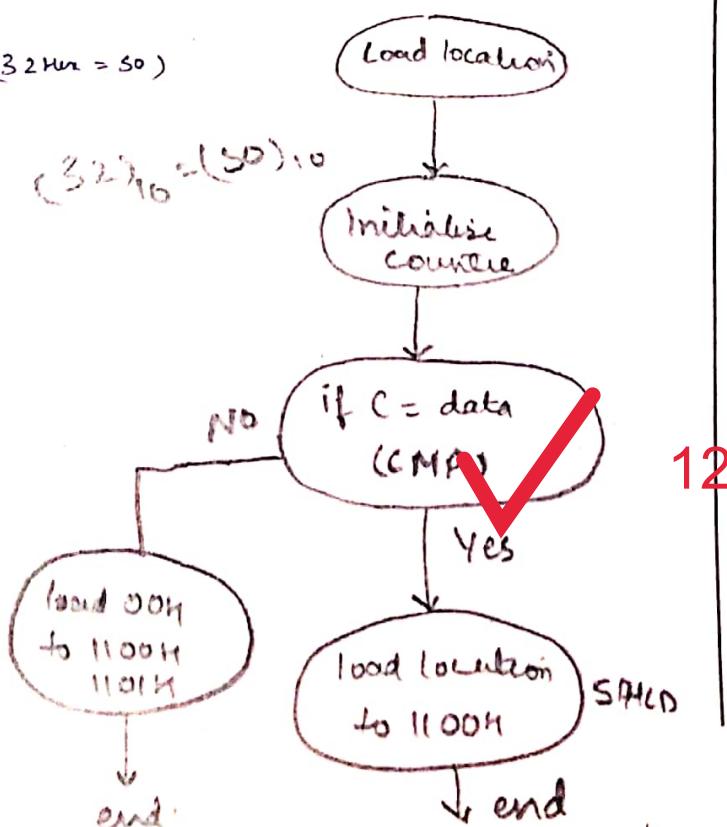
JNZ B, loop1

LXI H, 0000H

SHLD 1100H

loop2: SHLD 1100H

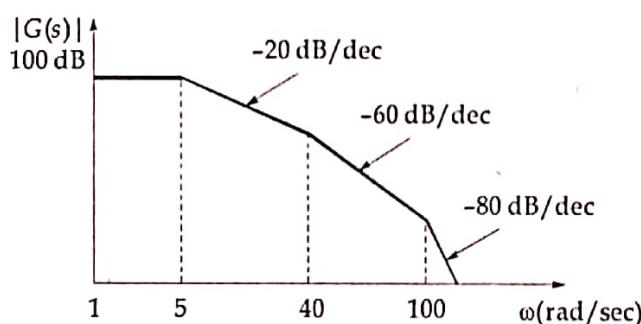
HLT



Section B : Digital Circuits-1 + Control Systems-1 [Part Syllabus]

Q.5 (a)

The magnitude plot of the open loop transfer function $G(s)$ of a certain system is shown below:



- Determine the open loop transfer function $G(s)$.
- Estimate the phase at each of the corner frequencies.

[12 marks]

(i) No initial slope so no zero or poles at zero, initial magnitude $\approx 100 \text{ dB}$

$$20 \log K = 100 \text{ dB}$$

Slope change at $5 \rightarrow$ one pole (-20 dB)
 $40 \rightarrow$ two poles (-60 dB)
 $100 \rightarrow$ one pole (-80 dB)



$$K = \text{amplifying 5} \quad K = \underline{10^5}$$

$$G(s) H(s) = \frac{K}{\left(\frac{s}{5} + 1\right) \left(\frac{s}{40} + 1\right)^2 \left(\frac{s}{100} + 1\right)}$$

$$G(s) H(s) = \frac{10^5}{(s+5) \cdot (s+40)^2 (s+100)}$$

$$G(s) = \frac{10^5 \cdot 8 \times 10^5}{(s+5) (s+40)^2 (s+100)}$$

Ans.

Angle

$$\angle G(j\omega) = -\tan^{-1} \frac{\omega}{5} - 2 \tan^{-1} \frac{\omega}{40} - 2 \tan^{-1} \frac{\omega}{100}$$

At $\omega = 5 \text{ rad/sec}$

$$\angle G(j\omega) = -62.11^\circ$$

-45° -
Ans

$$\text{at } \omega = 40 \quad \angle G(j\omega) = -82.87^\circ + -\frac{180^\circ}{90^\circ} - 21^\circ$$

$$\angle G(j\omega) = -194.67^\circ$$

Ans

at $\omega = 100 \text{ rad/sec}$

$$\angle G(j\omega) = -268.53^\circ$$

Ans

12

Q.5(b)

Consider the following characteristic equation of a closed loop system

$$s^4 + 20s^3 + 224s^2 + 1240s + 2400 + K = 0$$

- Find the condition for stability of the system.
- Determine the value of K which will cause sustained oscillations and also determine the frequency of such oscillations.

[12 marks]

Sol:

(i) formulate R-H table

| | | | | |
|-------|--------|--------|--------|--|
| s^4 | 1 | 224 | 2400+K | |
| s^3 | 20 | 1240 | 0 | |
| s^2 | 162 | 2400+K | 0 | |
| s^1 | A | 0 | 0 | |
| s^0 | 2400+K | 0 | 0 | |

$$A = \frac{162 \times 1240 - 20(2400+K)}{162}$$

For stable system

$$2400+K > 0$$

$$\text{and } A > 0 \text{ i.e. } 162 \times 1240 > 20(2400+K)$$

$$10044 > 2400+K$$

∴

$$-2400 < K < 7644$$

Aus-

$$7644 > K$$



(ii) When $A = 0$; System will have one column i.e. system will oscillate

$$\therefore K_{osc} = 7644$$

So Auxiliary Equation is

$$162s^2 + 2400+K = 0$$

$$162s^2 + 2400+7644 = 0$$

$$s^2 = \frac{-10044}{162}$$

$$S = \pm j \sqrt{62}$$

frequency of oscillation is

$$\boxed{\sqrt{62} \text{ or } 7.874}$$

\checkmark rad/sec

Ans'

12



Q.5 (c) Find the following conversions

- (i) Convert 423_{10} to hexadecimal.
- (ii) Convert 177_{10} to its 8-bit binary equivalent.
- (iii) Convert 378_{10} to 16-bit binary equivalent.
- (iv) Convert $B2F_{16}$ to decimal.
- (v) Convert 37_{10} to binary.

[12 marks]

$$(i) \quad (423)_{10} \rightarrow ()_{16}$$

$$\begin{array}{r} 16 \overline{) 423} \\ 16 \overline{) 26} \\ 1 \end{array} \quad \begin{array}{l} 7 \\ \downarrow A \\ 10 \end{array} \quad = \quad \underline{(1A7)}_{16} \quad \underline{\text{Ans}}$$

$$(ii) \quad (177)_{10} \rightarrow ()_2$$

$$\begin{array}{r} 2 \overline{) 177} \\ 2 \overline{) 88} \\ 2 \overline{) 44} \\ 2 \overline{) 22} \\ 2 \overline{) 11} \\ 2 \overline{) 5} \\ 2 \overline{) 2} \\ 1 \end{array} \quad \begin{array}{l} 1 \\ 0 \\ 0 \\ 1 \\ 1 \\ 0 \\ 0 \\ 1 \end{array} \quad = \quad \underline{(10110001)}_2$$

$$(iii) \cdot (378)_{10} \rightarrow (\quad)_2$$

$$\begin{array}{r} \underline{\underline{0\ 0\ 0\ 0\ ,\ 0\ 0\ 0\ 1}} \\ \underline{\underline{0\ 0\ 0\ 0\ ,\ 0\ 0\ 0\ 1}} \\ \hline 16 \text{ bit binary} \end{array} \quad \begin{array}{l} \underline{\underline{1\ 0\ 1\ 1\ ,\ 1\ 1\ 0\ 1\ 0}} \\ \hline \text{Ans.} \end{array}$$

$$\begin{array}{r} 378 \quad 0 \\ 189 \quad 1 \\ 94 \quad 0 \\ 47 \quad 1 \\ 23 \quad 1 \\ 11 \quad 1 \\ 5 \quad 1 \\ 2 \quad 0 \\ \hline 1 \end{array}$$

$$(iv) (B2F)_{16} \rightarrow (\quad)_{10}$$

$$\begin{aligned} & 11 \times 16^2 + 2 \times 16^1 + 15 \times 16^0 \\ & = 2816 + 32 + 15 = \underline{\underline{(2863)_{10}}} \quad \text{Ans.} \end{aligned}$$

$$(v) (37)_{10} \rightarrow (\quad)_2$$

$$= \underline{\underline{(1\ 0\ 0\ 1\ 0\ 1)}}_2 \quad \text{Ans.}$$



$$\begin{array}{r} 37 \quad 1 \\ 18 \quad 0 \\ 9 \quad 1 \\ 4 \quad 0 \\ 2 \quad 0 \\ \hline 1 \end{array}$$

- Q.5 (d) (i) Simplify the expression $Z = \overline{(A+C)} \cdot \overline{(B+D)}$ to one having only single variables inverted.
(ii) Using the K-map, simplify

$$Y = \overline{C}(\overline{ABD} + D) + A\overline{B}C + \overline{D}$$

Sol:

(i)

$$Z = \overline{(\overline{A} + C)} \cdot \overline{(B + \overline{D})}$$

[12 marks]

using de morgan theorem,

$$Z = (\overline{\overline{A} + C}) + (\overline{B + \overline{D}})$$

$$Z = \overline{\overline{A} \cdot \overline{C}} + \overline{\overline{B} \cdot \overline{\overline{D}}}$$

$$Z' = \boxed{Z' = A \cdot \overline{C} + \overline{B} \cdot D}$$



$$\underline{\underline{Z' = Z}} \quad \text{single variable un-inverted}$$

(ii)

$$Y = C(\overline{ABD} + D) + A\overline{B}C + \overline{D}$$

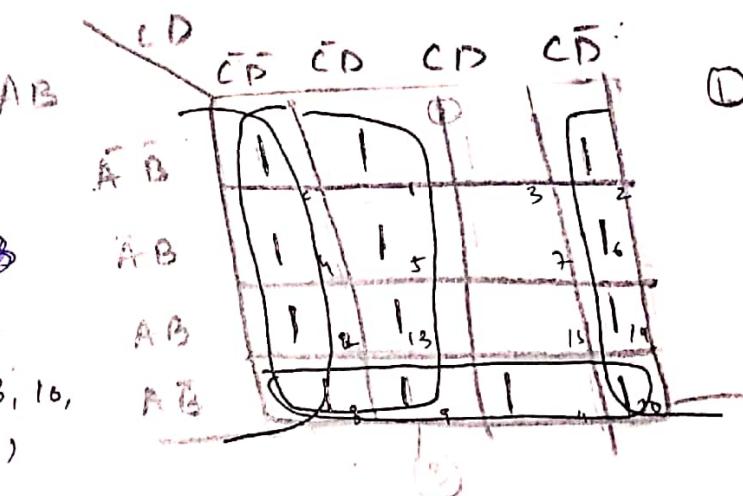
$$Y = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{C}D + A\overline{B}C + \overline{D}$$

$$\overline{ABC\overline{D}} = 0$$

$$\overline{CD} = 1, 3, 5, 9, 13$$

$$A\overline{B}C = 8, 9, 10, 11$$

$$\overline{D} = 0, 2, 4, 6, 8, 10, \\(12, 14)$$



Simplified expression

10

$$\boxed{Y = A'B + \overline{C} + \overline{D}} \quad \text{Ans.}$$

$$\boxed{Y = A\overline{B} + \overline{C} + \overline{D}} \quad \checkmark$$

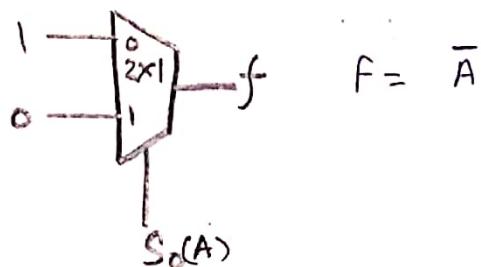


Q.5 (e) Implement all the basic logic gates using 2×1 MUX.

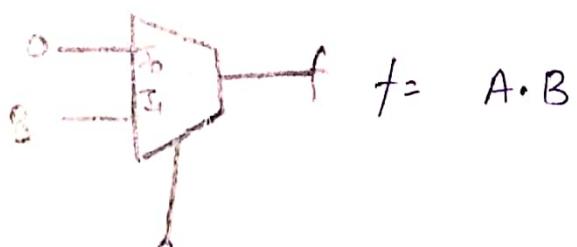
[12 marks]

Sol:

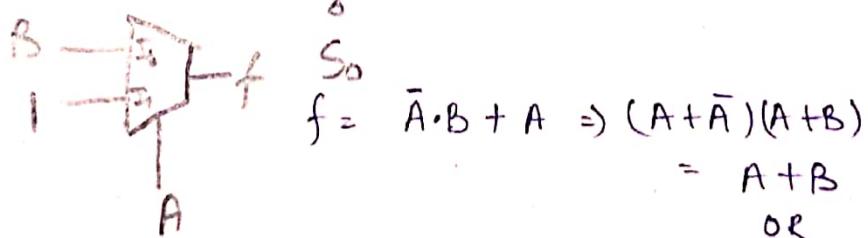
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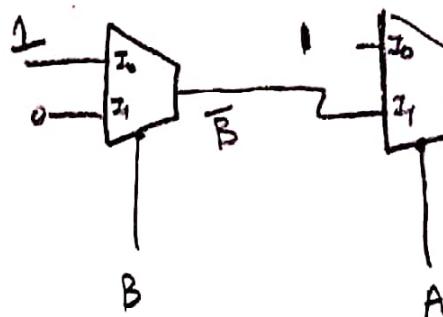


AND



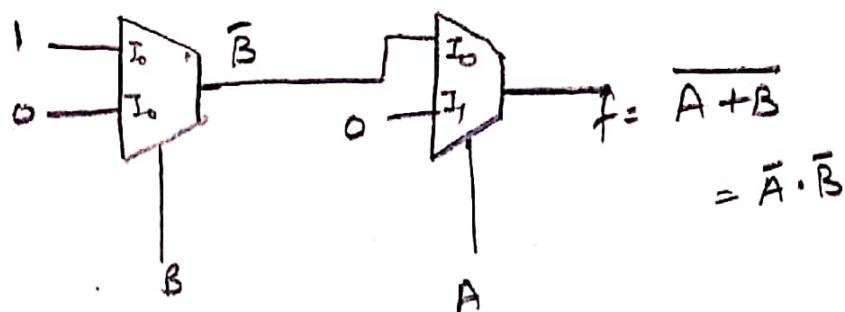
OR



NAND

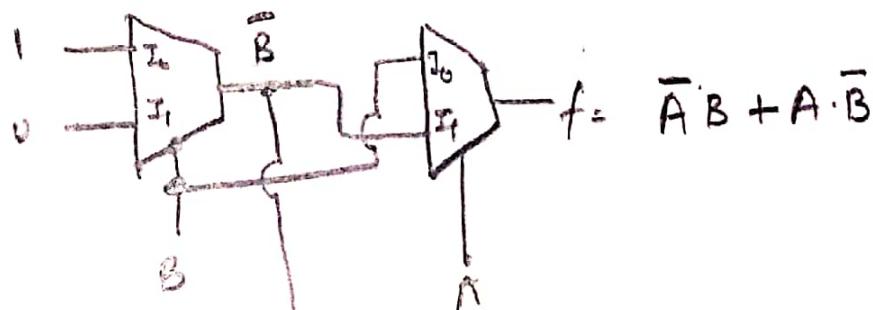
$$\overline{A \cdot B} = \overline{\overline{A} + \overline{B}}$$

$$f = \overline{\overline{A} + \overline{B}} = \overline{A \cdot B}$$

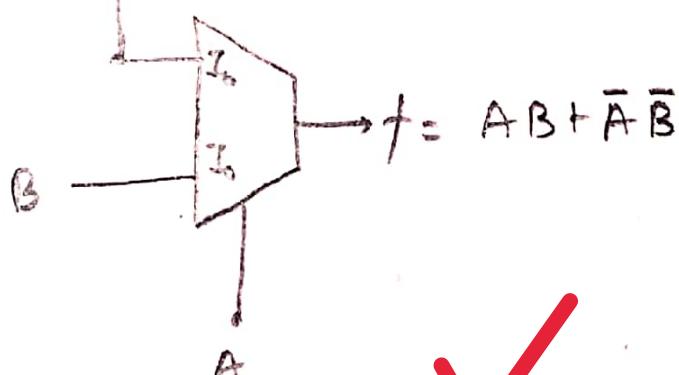
NOR

$$f = \overline{A + B}$$

$$= \overline{A} \cdot \overline{B}$$

X-OR

$$f = \overline{A} \cdot B + A \cdot \overline{B}$$

X-NOR

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- Q.6 (a) (i) The open-loop transfer function of a unity feedback system is $G(s)H(s) = \frac{s+2}{(s+1)(s-1)}$. Comment on the stability by drawing the Nyquist plot. [12 marks]

- (ii) A feedback control system has an open loop transfer function of

$$G(s)H(s) = \frac{Ke^{-s}}{s(s^2 + 2s + 1)}$$

Determine the maximum value of K for the closed loop stability by using R-H criteria.

[8 marks]

Sol:

(i)

$$G(s)H(s) = \frac{s+2}{(s+1)(s-1)}$$

$$G(j\omega)H(j\omega) = \frac{j\omega + 2}{(j\omega + 1)(j\omega - 1)}$$

$$|M| = \frac{\sqrt{\omega^2 + 4}}{\sqrt{\omega^2 + 1}\sqrt{\omega^2 + 1}}$$

$$\angle \phi = \tan^{-1} \frac{\omega}{2} - \tan^{-1} \frac{\omega}{1} + 180^\circ + \tan^{-1} \frac{\omega}{1}$$

$$\text{At } \omega = 0, M = 2, \phi = -180^\circ$$

$$\omega = \infty; M = 0; \phi = -90^\circ$$

$$\omega = 0^-, M = 2, \phi = 180^\circ$$

$$\omega = \infty^-, M = 0, \phi = 90^\circ$$

From figure

$$N = 1 \text{ (anticlockwise)} \quad \text{where}$$

$$N = P - Z$$

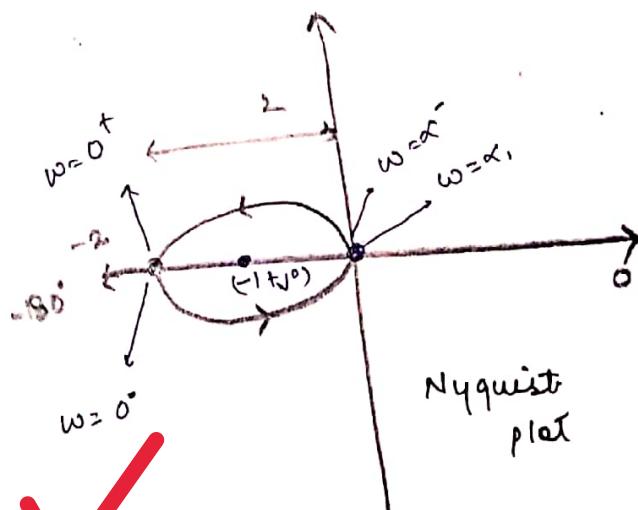
$$1 = 1 - 2$$

$$\boxed{Z = 0}$$

∴ closed loop system will be

Stable

Am-



(ii) for low frequency $e^{-s} = 1 - s$

$$\therefore G(s) H(s) = \frac{K(1-s)}{s^3 + 2s^2 + s}$$

Characteristic Equation: $1 + G(s) H(s) = 0$

$$s^3 + 2s^2 + s(1-K) + K = 0$$

$$\begin{array}{c|ccc} s^3 & 1 & 1-K & 0 \\ s^2 & 2 & K & 0 \\ s^1 & \frac{2-2K-K}{2} & 0 & 0 \\ s^0 & K & 0 & 0 \end{array}$$

For system to be stable

$$K > 0$$

$$\leftarrow \frac{2-2K-K}{2} > 0$$

$$2-3K > 0$$

$$2 > 3K$$

$$1.5 > K$$

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for stability

$$0 < K < 1.5$$

maximum value of $K = 1.5$

Ans-

- Q.6 (b) (i) Implement the logic circuit that has the expression $x = \overline{AB \cdot (\overline{C} + D)}$ using only NOR and NAND gates. [8 marks]

- (ii) Design an astable multivibrator using a practical MUX circuit. Calculate the minimum propagation delay that the MUX circuit should have so that the frequency of output wave is equal to 20 MHz. [12 marks]

(i)

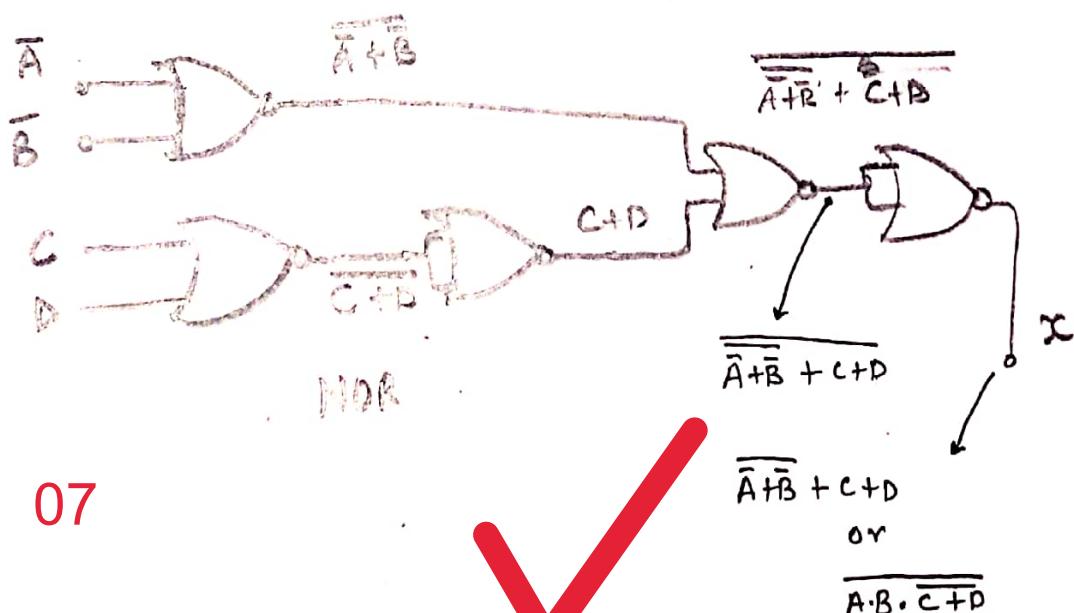
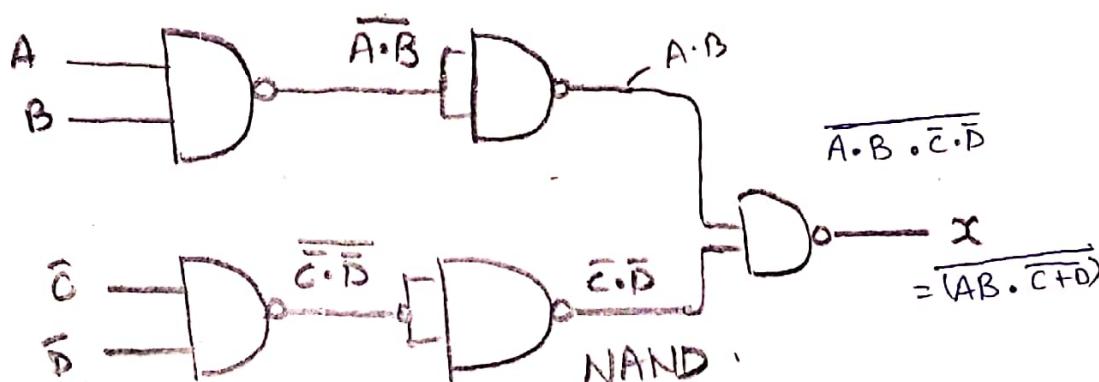
$$x = \overline{A \cdot B \cdot (\overline{C} + D)}$$

$$x = \overline{A \cdot B \cdot \overline{C} \cdot \overline{D}} \quad \text{NAND}$$

or

$$x = \overline{\overline{A} + \overline{B} \cdot (\overline{C} + D)}$$

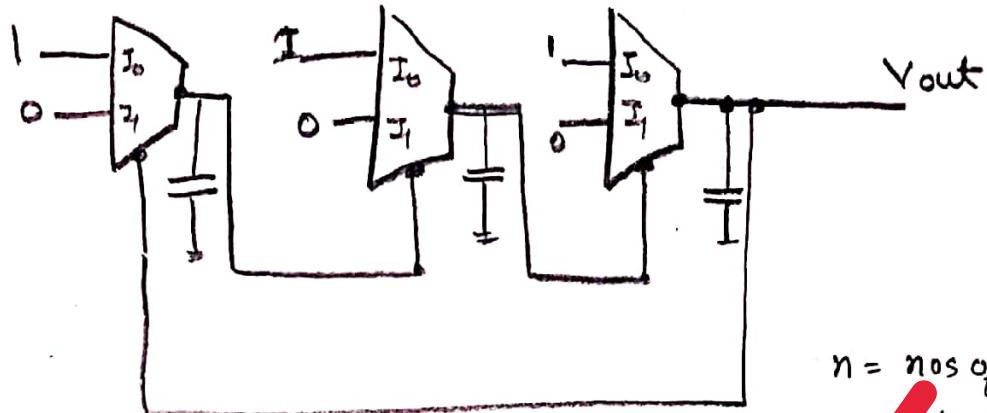
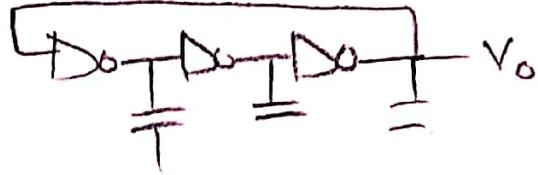
$$x = \overline{\overline{A} + \overline{B}} + (\overline{C} + D) \quad \text{NOR}$$



07



(ii)



Astable multivibrator

$$f = \frac{1}{2n \cdot t_p}$$

n = nos of multiplex used

$$f = 20 \text{ MHz}$$

$$\therefore 20 \times 10^6 = \frac{1}{2 \times 3 \times t_p}$$

$$t_p = \frac{1}{2 \times 3 \times 20 \times 10^6 \text{ Hz}}$$

$$t_p = 8.33 \text{ nsec}$$

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minimum propagation delay must be 8.33 nsec

Aus.

Q.6 (c)

Sketch the root-locus plot and determine the approximate damping ratio for a value of $K = 1.33$ for a control system having a forward transfer function with unity feedback,

$$G(s) = \frac{K(s+2)}{s^2 + 2s + 3}$$

[20 marks]

Sol:

$$G(s) = \frac{K(s+2)}{s^2 + 2s + 3}$$

$$\text{zero} = s = -2,$$

$$\text{pole} = s_1 = -1 + j\sqrt{2}$$

- No of asymptote $P-2 = 2-1 = 1$.
- Angle of asymptote, $\frac{\pm(2g+1)180}{P-2} = 180^\circ$,
- Centroid $-G = \frac{(-1-1)+2}{1} = 0$.
- break point

$$K = - \frac{s^2 + 2s + 3}{s+2}$$

$$-\frac{dK}{ds} = 0 \text{ i.e } \frac{(s+2)(2s+2) - (s^2 + 2s + 3) \cdot 1}{(s+2)^2} = 0$$

$$= 2s^2 + 2s + 4s + 4 - s^2 - 2s - 3$$

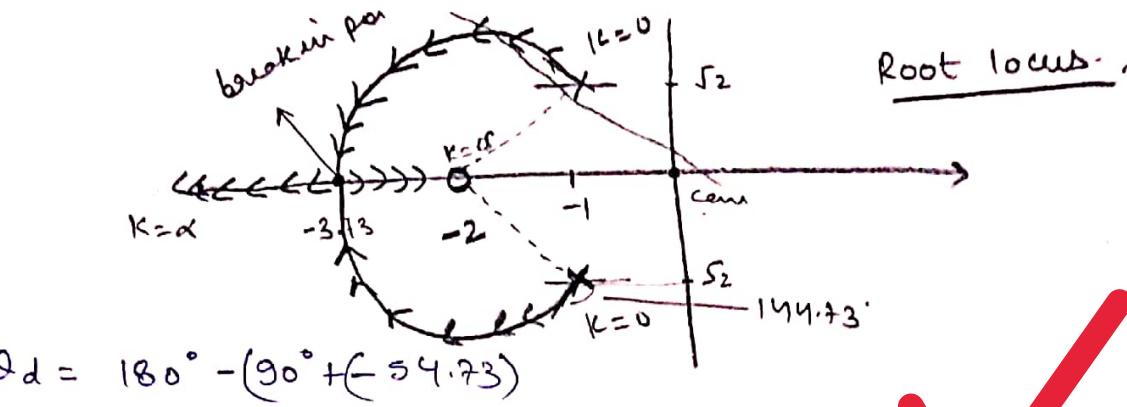
$$\Rightarrow s^2 + 4s + 1 = 0$$

$$s_1 s_2 = -3.73, -0.2679.$$

-3.73 is break away point

- Angle of departure

$$\begin{aligned} \angle s \Big|_{-1+j\sqrt{2}} &= \frac{(1+\sqrt{2}j)}{90^\circ, 0^\circ} \\ &= \tan^{-1} \frac{\sqrt{2}}{1} = -54.73^\circ \end{aligned}$$



- For $K=1.033$, we apply magnitude condition:

$$\frac{1.033 (s+2)}{s^2 + 2s + 3} = 1$$

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$$\text{or } s^2 + 0.67s + 0.34 = 0$$

$$s_1, s_2 = -0.335 \pm 0.477j$$

angle condition:

$$\angle G(s)N(s) = \frac{k(1.665 + 0.477j)}{(0.665 - 0.937j)(0.665 + 1.89j)}$$

$$s = -0.335 + 0.477j$$

X

$$\angle G(s)N(s) = 141.24^\circ \text{ or } 38.75^\circ$$

$$\xi = \cos \theta = \cos 38.75 \quad \text{X}$$

$\xi = 0.77$

Aus. X

Q.7 (a) In a thermal power plant, the three digital signals i.e. the steam temperature (T_s), drum level (L_D) and water flow (F_W) are used to control a particular system by a feedback signal (F) that comes from the field to the control room. A logic circuit is to be designed to generate a high feedback signal (F) whenever any one of the condition is satisfied.

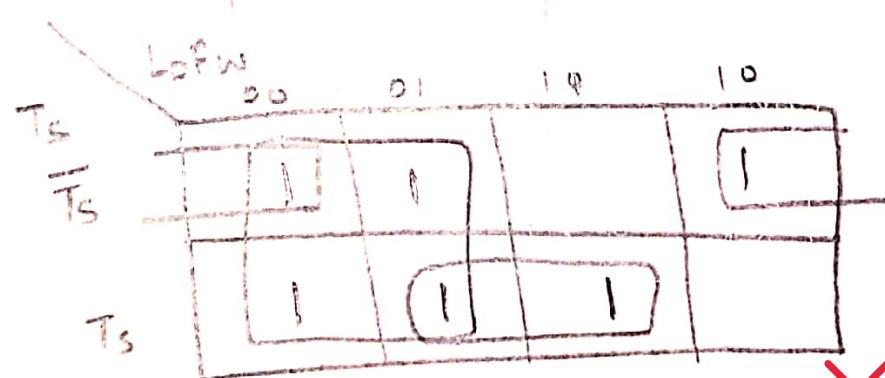
- C1 : All levels are at zero
- C2 : Level L_D and T_s set to zero
- C3 : Level F_W and T_s set to zero
- C4 : Only level L_D is set to zero
- C5 : All the levels are high

Find the minimum expression for the feedback signal F and construct a logic circuit using three 2×1 MUX only.

[20 marks]

Sol.

| | T_s | L_D | F_W | F | |
|---|-------|-------|-------|-----|----------|
| 0 | 0 | 0 | 0 | 1 | 0 → low |
| 1 | 0 | 0 | 1 | 1 | 1 - high |
| 2 | 0 | 1 | 0 | 1 | |
| 3 | 0 | 1 | 1 | 0 | |
| 4 | 1 | 0 | 0 | 1 | |
| 5 | 1 | 0 | 1 | 1 | |
| 6 | 1 | 1 | 0 | 0 | |
| 7 | 1 | 1 | 1 | 1 | |

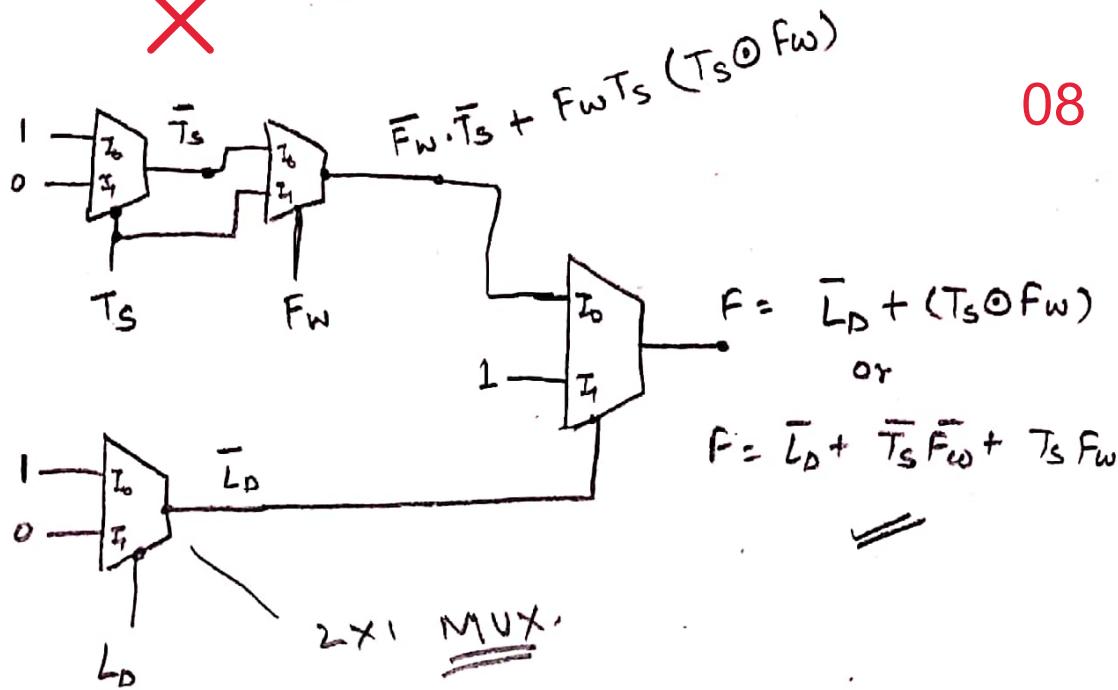


Minimum Expression

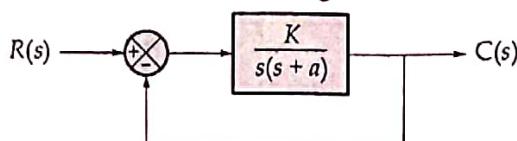
$$F = \bar{L}_D + T_s F_W + \bar{T}_s \bar{F}_W$$

Type

$$F = \bar{L}_D + T_S \odot F_W$$



- Q.7(b) Find K and a for the feedback system shown below so that $M_r = 1.25$ and $\omega_r = 12.65 \text{ rad/sec}$ will be satisfied. Also determine the settling time and bandwidth.



[20 marks]

Sol'

$$\frac{C(s)}{R(s)} = \frac{k}{s^2 + as + k}$$

on comparing with standard 2nd order equation

$$2\xi\omega_n = a$$

$$\omega_n^2 = k$$

Now $M_r = 1.25$

also $M_r = \frac{1}{2\xi\sqrt{1-\xi^2}}$

$$\frac{1}{2\xi\sqrt{1-\xi^2}} = 1.25 \quad \text{we get } \boxed{\xi = 0.447}$$

also

$$\omega_r = \omega_n \sqrt{1 - 2\xi^2}$$

$$12.65 = \omega_n \sqrt{1 - 2 \times 0.447^2}$$

$$\boxed{\omega_n = 16.33 \text{ rad/sec}}$$

∴ $k = \omega_n^2 = 16.33^2$

$$\boxed{k = 266.66 \approx 267} \quad \underline{\text{Ans.}}$$

$a = 2\xi\omega_n = 2 \times 0.447 \times 16.33$

$$\boxed{a = 14.61} \quad \underline{\text{Ans.}}$$

$$\bullet \text{ Setting time} = \frac{4}{\zeta \omega_n}$$

$$t_s = \frac{4}{\zeta \omega_n}$$

$$0.447 \times 16.33$$

$$t_s = 0.548 \text{ sec (2% criterion)}$$

$$t_s = 0.4109 \text{ sec (5% criterion)}$$

Ans.

$$\bullet \text{ Bandwidth} = \omega_n \sqrt{1 - 2\zeta^2 + \sqrt{2 - 4\zeta^2 + 4\zeta^4}}$$

$$B.W = 21.705 \text{ rad/sec}$$

Ans.



- Q.7(c) (i) Define various voltage and current parameters that are measured for the analysis of logic IC families.
- (NM), $I_{on}/I_{off} \rightarrow 10^4$
[8 marks]

- (ii) Simplify the following Boolean expression without using K-map and implement them using two level NAND gate only. (Assuming that are the multi-input NAND gate).

$$1. F = A\bar{B} + ABD + A\bar{B}\bar{D} + \bar{A}\bar{C}\bar{D} + \bar{A}B\bar{C}$$

$$2. F = BD + B\bar{C}\bar{D} + A\bar{B}\bar{C}\bar{D}$$

[12 marks]

- (i) Voltage parameters:

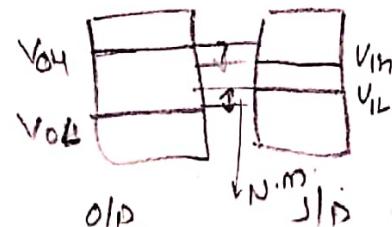
(i) V_{on} : it is minimum voltage level required at output of gate for logic 1.

(ii) V_{ol} : it is maximum voltage level required at output of gate for logic 0.

(iii) V_{ih} : it is min. voltage level required at input of gate for logic 1

(iv) V_{il} : it is maximum voltage level required at input of gate for logic 0.

- Current parameters:



I_{IH} and I_{IL} : Current flow into the input when logic is high and low (I_{IH})

I_{on} and I_{off} : It is current flow from output when logic is high(I_{on}) and low (I_{off}).

(ii)

$$F = A\bar{B} + \underline{ABD} + AB\bar{D} + \overline{A}\overline{C}\overline{D} + \overline{A}\overline{B}\overline{C}$$

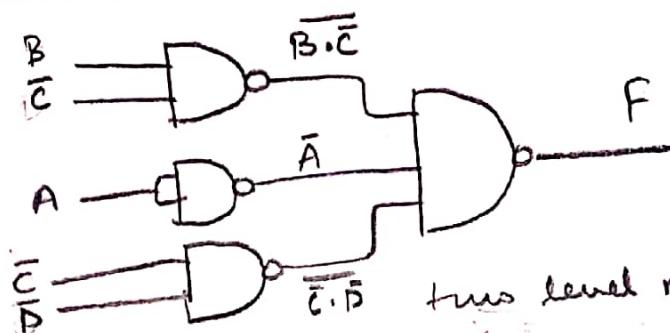
$$F = A\bar{B} + AB + \cancel{\overline{A}\overline{C}\overline{D}} (\overline{A}\overline{C}\overline{D} + \overline{A}\overline{B}\overline{C})$$

$$F = A(\bar{B} + B) + \overline{A}(\overline{C}\overline{D} + B\overline{C})$$

$$F = \underbrace{\overline{A}}_{2} + \overline{A} \underbrace{(\overline{C}\overline{D} + B\overline{C})}_{2}$$

$$F = (A + \overline{A})(A + \overline{C}\overline{D} + B\overline{C})$$

$$\boxed{F = A + \overline{C}\overline{D} + B\overline{C}}$$



(P.F.)

$$F = BD + BC\bar{D} + A\bar{B}\bar{C}D + \cancel{AB\bar{C}\bar{D}} \rightarrow 0$$

$$F = BD + BC\bar{D} + A(A\bar{B}\bar{C} + \bar{D})$$

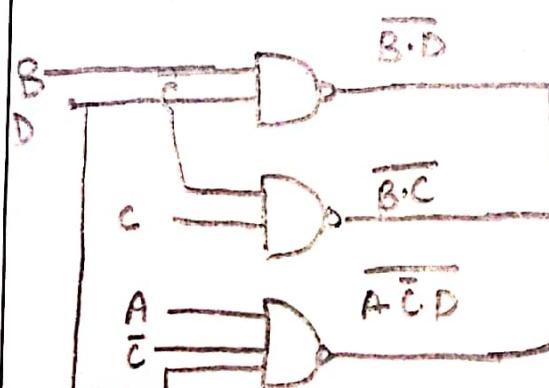
$$F = D(B + \overline{A}\overline{B}\overline{C}) + BC\bar{D}$$

$$F = D(B + A\bar{C}) + BC\bar{D}$$

$$F = BD + A\bar{C}D + BC\bar{D} \Rightarrow B(D + C\bar{D}) + A\bar{C}D$$

$$\boxed{F = BD + BC + A\bar{C}D}$$

$BD + BC$
 $A\bar{C}D$



$$F = BD + BC + A\bar{C}D$$

$$F = \overline{\overline{BD} \cdot \overline{BC} \cdot \overline{ACD}} \quad //$$

$$\boxed{F = BD + BC + A\bar{C}D}$$