



ESE 2026

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SET-C

Exam held on 08-02-2026

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Paper Analysis of ESE 2026
Preliminary Examination

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Substitute ' V_1 ' in eq. (6)

$$I_2 = Y_{21} \left(\frac{-Y_{12}V_2}{Y_{11}} \right) + Y_{22}V_2$$

$$\frac{I_2}{V_2} = \frac{Y_{11}Y_{22} - Y_{12}Y_{21}}{Y_{11}}$$

$$h_{22} = \frac{Y_{11}Y_{22} - Y_{12}Y_{21}}{Y_{11}}$$

Thus, statement 2 is also **incorrect**.

Now,

From h -parameters

$$h_{12} = \left. \frac{V_1}{V_2} \right|_{I_1 = 0} = 0$$

For Two-port network, $ABCD$ parameters is

$$V_1 = AV_2 - BI_2 \quad \dots(7)$$

$$I_1 = CV_2 - DI_2 \quad \dots(8)$$

Substitute $I_1 = 0$ in eq. (8)

$$0 = CV_2 - DI_2$$

$$I_2 = \frac{CV_2}{D}$$

Sub I_2 in eq. (1)

$$V_1 = AV_2 - B \left(\frac{C}{D} V_2 \right)$$

$$\frac{V_1}{V_2} = \frac{AD - BC}{D}$$

$$h_{12} = \frac{AD - BC}{D}$$

Statement 3 is correct.

Therefore, option (a) is correct.

End of Solution

Q.2 Consider the following statements regarding two-port network:

1. The overall Z -parameter matrix for series-connected two-port networks is simply the sum of Z -matrices of each individual network.
2. The overall Y -parameter matrix, if two networks A and B are connected in parallel, is simply the summation of Y -matrices of each individual two-port network.
3. The overall $ABCD$ parameter network matrix for the cascade-connected two-port network is the matrix product of $ABCD$ matrices of each individual two-port network.

Which of the above statements are correct?

- | | |
|------------------|------------------|
| (a) 1 and 2 only | (b) 1 and 3 only |
| (c) 2 and 3 only | (d) 1, 2 and 3 |

Ans. (d)

1. $[Z]_T = [Z]_A + [Z]_B \Rightarrow$ For series connection
2. $[Y]_T = [Y]_A + [Y]_B \Rightarrow$ For parallel connection
3. $[T]_T = [T]_A \times [T]_B \Rightarrow$ For cascaded connection.

Therefore option (d) is correct.

End of Solution

Q.3 Consider the following statements regarding small signal model of BJT:

1. The use of r_e -model for AC analysis of transistor does not provide exact analysis.
2. The use of h -parameter model as an equivalent circuit of transistor provides exact analysis.
3. In h -parameter model, h_{12} is referred to as short-circuit current gain.

Which of the above statements are **not** correct?

- | | |
|------------------|------------------|
| (a) 1 and 2 only | (b) 1 and 3 only |
| (c) 2 and 3 only | (d) 1, 2 and 3 |

Ans. (*)

- The use of r_e -model for AC analysis of transistor does not provide exact analysis. Statement 1 is correct.
- The use of h -parameter model as an equivalent circuit of transistor provides exact analysis. Statement 2 is also correct.
- In h -parameter model, h_{12} is referred to as open-circuit voltage gain. Therefore, statement 3 is incorrect.

End of Solution

Q.4 A BJT is operating at a base current of $7.6 \mu\text{A}$ and β_0 (AC common-emitter forward short-circuit current gain in hybrid- π model) of 104. Assume $V_T = 25 \text{ mV}$ The value of transconductance at room temperature ($T = 293\text{K}$) is

- | | |
|--------------------------|----------------------------|
| (a) 0.304 mA/V | (b) 1.826 mA/V |
| (c) 31.60 mA/V | (d) $790.40 \mu\text{A/V}$ |

Ans. (c)

We know that, transconductance $g_m = \frac{I_C}{V_T}$.

or,

$$I_C = \beta I_B = 104 \times 7.6 \mu\text{A}$$

$$I_C = 0.7904 \text{ mA}$$

$$g_m = \frac{I_C}{V_T} = \frac{0.7904}{25} = 31.6 \text{ mA/V}$$

Hence, option (c) is correct.

End of Solution

- Q.5** Consider the following statements regarding rectifier circuits:
1. The transformer needed for the bridge rectifier over the full-wave rectifier is lighter in weight.
 2. The Peak Inverse Voltage (PIV) of each diode used in the bridge rectifier is V_{\max} , whereas it is $2V_{\max}$ for the full-wave rectifier.
- Which of the above statements is/are correct?
- (a) 1 only (b) 2 only
(c) Both 1 and 2 (d) Neither 1 nor 2

Ans. (c)

- A full-wave rectifier (center-tapped) requires a center-tapped transformer.
- This increases:
 - ♦ Copper usage
 - ♦ Number of turns
 - ♦ Size and weight of the transformer
- A bridge rectifier does not require a center tap, hence:
 - ♦ Transformer is smaller and lighter
- Therefore, Statement 1 is true.
- In a bridge rectifier:
 - ♦ Each diode experiences a maximum reverse voltage = V_{\max} .
- In a center-tapped full-wave rectifier:
 - ♦ Each diode must withstand $2V_{\max}$ during the reverse-biased condition.
- Therefore, Statement 2 is true.

Both statements 1 and 2 are correct.

Therefore, option (c) is correct.

End of Solution

- Q.6** Consider the following statements regarding transistors:
1. FETs are less temperature stable compared to BJTs.
 2. FET is a current-controlled device whereas BJT is a voltage-controlled device.
 3. FET has zero offset voltage at zero drain current, hence it makes an excellent signal chopper.
- Which of the above statements are **not** correct?
- (a) 1 and 2 only (b) 1 and 3 only
(c) 2 and 3 only (d) 1, 2 and 3

Ans. (a)

- FETs are more temperature stable than BJTs
- Reason:
 - ♦ FETs are majority-carrier devices
 - ♦ BJTs are minority-carrier devices, hence more prone to thermal runaway.

Hence statement 1 is **not** correct.

- BJT is current-controlled device (base current controls collector current)
- FET is voltage-controlled device (gate-source voltage controls drain current)

Therefore, statement 2 is also **not** correct.

Ans. (c)

Given:

Number of bits $n = 6$

Step size (LSB) = 50 mV = 0.05

We need:

1. Full-scale output voltage
2. Percentage resolution

For a DAC:

$$\text{Step size} = \frac{\text{Full scale output}}{2^n - 1}$$

So,

$$\begin{aligned} \text{Full-Scale Output Voltage} &= (2^n - 1) \times \text{Step size} \\ &= (2^6 - 1) \times 0.05 = 3.15 \text{ V} \end{aligned}$$

Percentage Resolution

Resolution means smallest detectable change as a percentage of full scale.

$$\text{Percentage resolution} = \frac{1}{2^n - 1} \times 100 = \frac{1}{63} \times 100 = 1.587\%$$

Hence, option (c) is correct.

End of Solution

Q.11 Consider the following statements regarding A/D and D/A converters:

1. The settling time of a DAC is the time taken by its output to settle down to within ± 0.7 step size of its final value after the application of the digital input.
2. The step size of DAC is the same as the proportionality factor in the DAC I/O relationship.
3. The dual-slope ADC is also called the continuous-conversion type ADC.

Which of the above statements are **not** correct?

- | | |
|------------------|------------------|
| (a) 1 and 2 only | (b) 1 and 3 only |
| (c) 2 and 3 only | (d) 1, 2 and 3 |

Ans. (b)

- The settling time of a DAC is the time taken by its output to settle within ± 0.7 step size of its final value.

Therefore, statement 1 is incorrect.

- The step size of a DAC is the same as the proportionality factor in the DAC input-output relationship.

Thus statement (2) is correct.

- Dual-slope ADC is an integrating ADC.
- Continuous-conversion ADC refers to sigma-delta ADC

So this statement is incorrect.

Thus, option (b) is correct.

End of Solution

Q.12 The simplified form of the Boolean function.

$$F(A,B,C,D) = A\bar{B}C + B + BD + AB\bar{D} + \bar{A}C$$

- (a) $B + C$ (b) $A(B + C)$
(c) $B + D$ (d) B

Ans. (a)

$$\begin{aligned} \text{We have, } F(A, B, C, D) &= A\bar{B}C + B + BD + AB\bar{D} + \bar{A}C \\ &= A\bar{B}C + B(1 + D + A\bar{D}) + \bar{A}C \\ &= A\bar{B}C + B + \bar{A}C \\ &= (AC + B) + \bar{A}C = B + C \end{aligned}$$

Hence, option (a) is correct.

End of Solution

Q.13 The simplest possible POS form of $F(A, B, C, D, E)$
 $= \Sigma m(6, 9, 13, 18, 19, 25, 27, 29, 31) + d(2, 3, 11, 15, 17, 24, 28)$
 is

- (a) $(B + E)(\bar{B} + \bar{C}D)(\bar{A} + B + D\bar{E})$
 (b) $(\bar{B} + \bar{E})(B + C + \bar{D})(A + B + \bar{D} + E)$
 (c) $(D + E)(\bar{B} + E)(A + B + \bar{E})(\bar{A} + B + \bar{C})$
 (d) $(D + E)(\bar{B} + E)(A + B + \bar{E})(\bar{A} + B + \bar{C})(B + E)$

Ans. (c)

	D + E		A	
B + C	D + E	D + \bar{E}	\bar{D} + \bar{E}	\bar{D} + E
B + C	0	0	X	X
B + \bar{C}	0	0	0	1
\bar{B} + \bar{C}	0	1	X	0
\bar{B} + C	0	1	X	0

	D + E		\bar{A}	
B + C	D + E	D + \bar{E}	\bar{D} + \bar{E}	\bar{D} + E
B + C	0	X	1	1
B + \bar{C}	0	0	0	0
\bar{B} + \bar{C}	X	1	1	0
\bar{B} + C	X	1	1	0

$$F(A, B, C, D, E) = (D + E)(\bar{B} + E)(A + B + \bar{E})(\bar{A} + B + \bar{C})$$

Hence, option (c) is correct.

End of Solution

Q.14 Consider statements following the regarding Boolean functions:

- Each one of the product terms in the canonical SOP form is called a minterm.
 - Two squares in K-map are said to be adjacent to each other, if their min (max) terms differ in only one variable.
 - The binary number designations of the rows and columns of K-map are in Gray code.
- Which of the above statements are correct?



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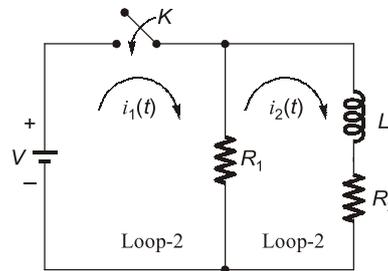
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- Q.25** What is the expression for $I_1(s)$ for the two-mesh network given below, when the switch is closed [assuming the initial condition $i(O^+)$ is zero through the inductance]?



- (a) $I_1(s) = \frac{V}{s} \left[\frac{R_1(R_2 + sL)}{R_1 + R_2 + sL} \right]$ (b) $I_1(s) = \frac{V}{s} \left[\frac{R_1 R_2 + sL}{R_1 + R_2 + sL} \right]$
 (c) $I_1(s) = \frac{V}{s} \left[\frac{R_1 + R_2 + sL}{R_1(R_2 + sL)} \right]$ (d) $I_1(s) = \frac{V}{s} \left[\frac{R_1 R_2 + sL}{R_1(R_2 + sL)} \right]$

Ans. (c)

The Laplace transform of

$$V \rightarrow V/s$$

$$R_1 \rightarrow R_1$$

$$R_2 \rightarrow R_2$$

$$L \rightarrow sL$$

The KVL equation in first loop

$$\frac{V}{s} = R_1(I_1(s) - I_2(s)) \quad \dots(1)$$

The KVL equation in second loop

$$0 = R_1(I_2(s) - I_1(s)) + sLI_2(s) + R_2I_2(s)$$

From eq. (2)

$$I_2(s) = \frac{R_1 I_1(s)}{R_1 + R_2 + sL}$$

Substitute $I_2(s)$ in equation (1).

$$\frac{V}{s} = R_1 I_1(s) - \frac{R_1 \cdot R_1 I_1(s)}{R_1 + R_2 + sL}$$

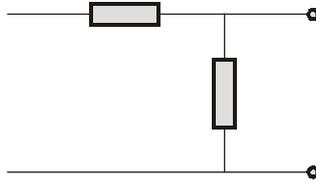
On solving we get,

$$I_1(s) = \frac{V}{s} \left[\frac{(R_1 + R_2 + sL)}{R_1(R_2 + sL)} \right]$$

Hence, option (c) is correct.

End of Solution

- If one shunt arm of a π network is zero. Thus, π network reduces to an 'L' network as shown below:



Thus statement 3 is correct.
Hence, option (d) is correct.

End of Solution

- Q.28** Hybrid parameter h_{21} is called
- | | |
|--------------------------|--------------------------|
| (a) input impedance | (b) forward current gain |
| (c) reverse voltage gain | (d) output admittance |

Ans. (b)

The h-parameter of two-port network is given as

$$V_1 = h_{11}I_1 + h_{12}V_2 \quad \dots(1)$$

$$I_2 = h_{21}I_1 + h_{22}V_2 \quad \dots(2)$$

From eq. (2)

$$h_{21} = \left. \frac{I_2}{I_1} \right|_{V_2=0} = \text{Forward current gain}$$

Hence, option (b) is correct.

End of Solution

- Q.29** The two-port network will be reciprocal if
- | | |
|------------------------|------------------------|
| (a) $h_{12} = h_{21}$ | (b) $Y_{12} = -Y_{21}$ |
| (c) $Z_{12} = -Z_{21}$ | (d) $AD = 1 + BC$ |

Ans. (d)

The reciprocity of a two-port network is given by

$$h_{12} = -h_{21}$$

$$Y_{12} = Y_{21}$$

$$Z_{12} = Z_{21}$$

$$AD - BC = 1$$

(or) $AD = 1 + BC$

Hence, option (d) is correct.

End of Solution

- Q.30** Consider the following statements regarding two-port network:

1. A network is termed to be reciprocal, if the ratio of the response variable to the excitation variable remains identical even if the positions of the response and excitation in the network are interchanged.



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- Q.46** The Curie temperature of nickel (Ni) metal is
(a) 298 °C (b) 358°C
(c) 770°C (d) 1120°C

Ans. (b)

Curie temperature is the temperature above which a ferromagnetic material loses its ferromagnetism and becomes paramagnetic.

Material	Curie Temperature
Iron (Fe)	≈770°C
Nickel (Ni)	≈358°C
Cobalt (Co)	≈1120°C

End of Solution

- Q.47** Consider the following statements regarding electromagnetic induction:
1. Whenever magnetic flux linked with a closed coil changes, an induced EMF is set up in the coil and the induced EMF lasts as long as the change in magnetic flux continues.
 2. The magnitude of the induced EMF is proportional to the rate of change of magnetic lines of force.
 3. Lenz's law states that the direction of the induced current is such that it opposes the very cause producing this current.

Which of the above statements are correct?

- (a) 1 and 2 only (b) 1 and 3 only
(c) 2 and 3 only (d) 1, 2 and 3

Ans. (d)

As,

- Statement 1 defines Faraday's first law of electromagnetic induction.
- Statement 2 defines Faraday's second law of electromagnetic induction.
- Statement 3 defines Lenz's law.

Hence, all the statements are correct.

Thus, option (d) is correct.

End of Solution

- Q.48** A current of 10 A when flowing through a coil of 2000 turns establishes a flux of 0.6 milliweber. The inductance of the coil is
(a) 0.12 H (b) 0.72 H
(c) 1.08 H (d) 1.32 H

Ans. (a)

We know that,

$$L = \frac{N\phi}{i} = \frac{2000(0.6 \times 10^{-3})}{10} = 0.12 \text{ H}$$

End of Solution



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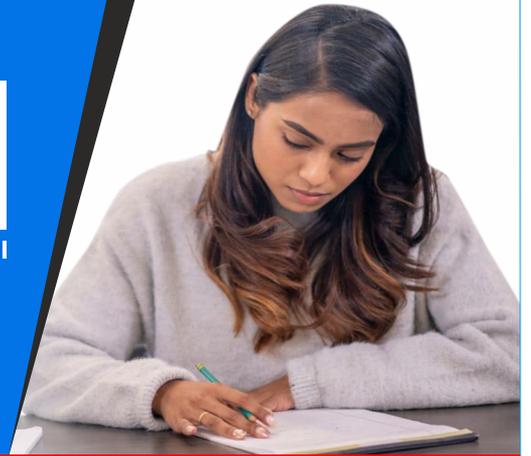


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- Q.71** If the input voltages of an op-amp are $V_{i1} = 150 \mu\text{V}$, $V_{i2} = 140 \mu\text{V}$, the differential is 4000 CMRR is 100, then the output voltage is
- (a) 26.4 mV (b) 40.0 mV
(c) 45.8 mV (d) 68.2 mV

Ans. (c)

$$V_{\text{out}} = A_{DM}V_d + A_{CM}V_{CM} \quad \dots(1)$$

$$\begin{aligned} V_D &= V_{i1} - V_{i2} \\ &= 150 \mu\text{V} - 140 \mu\text{V} \\ &= 10 \mu\text{V} \end{aligned}$$

$$V_{CM} = \frac{V_{i1} + V_{i2}}{2} = \frac{150\mu\text{V} + 140\mu\text{V}}{2}$$

$$V_{CM} = 145 \mu\text{V}$$

$$A_{CM} = \frac{A_{DM}}{CMRR} = \frac{4000}{100} = 40$$

Putting all the values in eqn (i)

$$\begin{aligned} V_{\text{out}} &= (4000 \times 10 \times 10^{-6}) + (40 \times 145 \times 10^{-6}) \\ &= 45.8 \text{ mV} \end{aligned}$$

End of Solution

- Q.72** An inverting amplifier using op-amp is having $R_F = 150 \text{ k}\Omega$ and $R_1 = 2 \text{ k}\Omega$. The offset voltage for the op-amp specification listing $I_{IO} = 100 \text{ nA}$ is
- (a) 0.2 mV (b) 7.5 mV
(c) 7.6 mV (d) 15 mV

Ans. (d)

The output offset voltage due to input offset current is calculated as

$$\begin{aligned} V_0 &= I_{IO} \times R_F \\ &= 100 \text{ nA} \times 150 \text{ k}\Omega \\ &= 15 \text{ mV} \end{aligned}$$

End of Solution

- Q.73** Among two cubes, the first one has a length of $L \text{ m}$, while the second one has a length of $2L \text{ m}$. What is the ratio of the conductivities of the materials of the cubes so that the resistance between any two faces of one cube is same as that of the other cube?
- (a) 1/2 (b) 1
(c) 3/2 (d) 2

Ans. (d)

Resistance R between opposite faces of a cube is given by

$$R = \rho \cdot \frac{l}{A} \text{ where } \rho = \frac{1}{\sigma}$$

- Resistance for the first cube:

$$\text{side} = L, \text{ so } l = L, A = L^2$$

Q.78 Consider a multimode step-index optical fiber that has a core radius of 25 μm , a core index of 1.48 and an index difference $\Delta = 0.01$. What is the V -number at an operating wavelength of 840 nm?

- (a) 39 (b) 76
(c) 42 (d) 16.89

Ans. (b)

Given, Core radius (r) = 25 μm ; $n_1 = 1.48$; $\Delta = 0.01$; $\lambda = 840$ nm

Numerical aperture (NA) = $n_1\sqrt{2\Delta} = 1.48\sqrt{2 \times 0.01} = 0.209$

$$V = \frac{2\pi a}{\lambda} \times NA = \frac{2\pi \times 25 \times 10^{-6}}{840 \times 10^{-9}} \times 0.209$$

Rounding to the nearest whole number, the V -number is 39. The correct option is (a).

End of Solution

Q.79 Match the following Lists:

List-I

- P. Perigee
Q. Eccentricity
R. Semi-major axis
S. Apogee

List-II

- It is the point on the orbit that is nearest to the center of the Earth.
- It is the point on the satellite orbit that is at the farthest distance from the center of the Earth.
- It is the ratio of the distance between the center of the ellipse and the center of the Earth to the semi-major axis of the ellipse.
- It is a geometrical parameter of an elliptical orbit.

Select the correct answer using the code given below.

	P	Q	R	S
(a)	2	3	4	1
(b)	1	3	4	2
(c)	2	4	3	1
(d)	3	2	4	1

Ans. (b)

Perigee (nearest), Eccentricity (ratio), semi-major axis (geometry) and Apogee (farthest).

End of Solution

Q.80 An Earth station antenna having a maximum gain of 60 dB at the operational frequency is fed from a power amplifier generating 10 kW. If the feed system has a loss of 2 dB, what is the Earth station EIRP?

- (a) 98 dB (b) 110 dB
(c) 48 dB (d) 40 dB

Ans. (a)

Attenuation is defined by the log ratio of input power to output power per unit length

$$\text{Attenuation } (\alpha) = 10 \log_{10} \left(\frac{P_i}{P_o} \right)$$

End of Solution

Q.90 Match the following Lists:

List-I

- P. IP layer
- Q. TCP/UDP layer
- R. TCP/IP
- S. Physical layer

List-II

1. Transmission of bits
2. Network layer of the OSI reference model
3. Transport layer of the OSI reference model
4. Application layer of the OSI reference model

Select the correct answer using the code given below.

	P	Q	R	S
(a)	2	3	4	1
(b)	1	3	4	2
(c)	2	4	3	1
(d)	3	2	4	1

Ans. (a)

P. **IP Layer:** This corresponds to the network layer of the OSI reference model.

Q. **TCP/UDP layer:** These are protocols that operate at the transport layer of the OSI reference model.

R. **TCP/IP:** In this list, it is paired with the application layer of the OSI reference model.

S. **Physical layer:** This layer is responsible for the actual transmission of bits over a physical medium.

End of Solution

Q.91 A low-loss transmission line of 50 ohms to a load of characteristic impedance is connected 100 ohms. The voltage reflection coefficient and the standing wave ratio are, respectively

- (a) 1/2 and 3
- (b) 3 and 1/2
- (c) 1/3 and 2
- (d) 1/3 and 3

Ans. (c)

Given data, $Z_o = 50 \Omega$; $Z_L = 100 \Omega$

$$(i) \quad \Gamma = \frac{Z_L - Z_o}{Z_L + Z_o} = \frac{100 - 50}{100 + 50} = \frac{50}{150} = \frac{1}{3}$$

$$(ii) \quad S = \frac{1 + |\Gamma|}{1 - |\Gamma|} = \frac{1 + 1/3}{1 - 1/3} = 2$$

End of Solution



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Q.100 The execution of ORI instruction requires

- (a) 2 M-cycles and 7 T-states (b) 2 M-cycles and 10 T-states
(c) 3 M-cycles and 7 T-states (d) 3 M-cycles and 10 T-states

Ans. (a)

In the 8085 microprocessor, the ORI (OR immediate) instruction is a 2-byte instruction (opcode + immediate data) that requires 2 machine cycles (opcode fetch + memory read) and a total of 7 T-states (4 opcode fetch + 3 for memory read of immediate data).

End of Solution

Q.101 Which one of the following instructions in 8085 microprocessor does not affect any flag?

- (a) RLC (b) PUSH
(c) ORI (d) INR

Ans. (b)

PUSH instruction in 8085 microprocessor is not affect the flags. Since, it is a data transfer instruction.

End of Solution

Q.102 Consider the following regarding RISC processor: statements

1. The number of instructions is less than 400.
2. The number of addressing modes is more than 3.
3. Memory is accessed only by Load and Store instructions.

Which of the above statements are **not** correct?

- (a) 1 and 2 only (b) 1 and 3 only
(c) 2 and 3 only (d) 1, 2 and 3

Ans. (a)

In RISC processor, memory is accessed only by load and store instructions which is true statement and (a) is correct option where ξ^2 are false.

End of Solution

Q.103 Consider the following statements regarding MC68HC11 microcontroller :

1. It has 40 I/O lines with multiple functions.
2. It has two operating modes.
3. It has 8k bytes of ROM and 512 bytes of EEPROM.

Which of the above statements are correct?

- (a) 1 and 2 only (b) 1 and 3 only
(c) 2 and 3 only (d) 1, 2 and 3

Ans. (d)

For the given MC68HC11 microcontroller, all statements are correct.

End of Solution

Q.104 The Z-transform of $x(n) = -a^n(-n - 1)$ is

- (a) $-\frac{1}{1-az^{-1}}$; ROC is $|z| < |a|$ (b) $-\frac{1}{1-az^{-1}}$; ROC is $|z| > |a|$
 (c) $-\frac{z^{-1}}{1-az^{-1}}$; ROC is $|z| < |a|$ (d) $-\frac{1}{1-(az)^{-1}}$; ROC is $|z| < |a|$

Ans. (a)

$$-a^n u[-n-1] \xrightarrow{\text{Z.T.}} \frac{1}{1-az^{-1}}; |z| < |a|$$

End of Solution

Q.105 The inverse Z-transform of $X(z) = \log(1 + az^{-1})$ with ROC $|z| > |a|$ is

- (a) $x(n) = \begin{cases} (-1)^n \frac{a^n}{n}, & n \geq 1 \\ 0, & n \leq 0 \end{cases}$ (b) $x(n) = \begin{cases} (-1)^n \frac{a^{n+1}}{n}, & n \geq 0 \\ 0, & n \leq -1 \end{cases}$
 (c) $x(n) = \begin{cases} (-1)^{n+1} \frac{a^{n+1}}{n}, & n \geq 0 \\ 0, & n \leq -1 \end{cases}$ (d) $x(n) = \begin{cases} (-1)^{n+1} \frac{a^n}{n}, & n \geq 1 \\ 0, & n \leq 0 \end{cases}$

Ans. (d)

Given,

$$\begin{aligned} X(z) &= \log(1 + az^{-1}) \\ \frac{dX(z)}{dz} &= \frac{1}{1+az^{-1}} (-az^{-2}) \\ -z \frac{dX(z)}{dz} &= -z \left[\frac{1}{1+az^{-1}} \right] (-az^{-2}) \\ &= \frac{az^{-1}}{1+az^{-1}}; |z| > |a| \end{aligned}$$

We know that, $nx(n) \longleftrightarrow -z \frac{dX(z)}{dz} = \frac{az^{-1}}{1+az^{-1}}; |z| > |a|$... (i)

Let, $(-a)^n u[n] \longleftrightarrow \frac{1}{1+az^{-1}}; |z| > |a|$

$$(-a)^{n-1} u[n-1] \longleftrightarrow \frac{z^{-1}}{1+az^{-1}}; |z| > |a|$$

$$a \cdot (-a)^{n-1} u[n-1] \longleftrightarrow \frac{az^{-1}}{1+az^{-1}}; |z| > |a|$$

$$a \cdot a^{n-1} (-1)^{n-1} u[n-1] \longleftrightarrow \frac{az^{-1}}{1+az^{-1}}; |z| > |a|$$

$\therefore a^n (-1)^2 (-1)^{n-1} u[n-1] \longleftrightarrow \frac{az^{-1}}{1+az^{-1}}; |z| > |a|$

$\therefore a^n (-1)^{n+1} u[n-1] \longleftrightarrow \frac{az^{-1}}{1+az^{-1}}; |z| > |a|$

Ans. (a)

End of Solution

Q.109 Which type of procedure is employed in which some computers give the responsibility for solving data conflicts problem to the compiler that translate the high-level programming language into a machine language program?

- (a) Hardware interlock (b) Operand forwarding
(c) Branch target buffer (d) Delayed load

Ans. (d)

Hardware techniques: Hardware interlock operand forwarding branch target buffer.
Software technique: Delayed load.

End of Solution

Q.110 In an asynchronous data transfer, a unit receiving the data item responds with another control signal to acknowledge receipt of agreement the data. This type of agreement between two independent units is referred to as

- (a) timing diagram (b) strobe
(c) handshaking (d) activity diagram

Ans. (c)

Handshaking signals are exchanged between transmitter and receiver for error free transfer of data for proper coordination. Example : acknowledgment signals.

End of Solution

Q.111 Which of the following rules are correct, when a transmitted character is detected by the receiver from knowledge of the transmissions?

1. When a character is not being sent, the line is kept in the 1-state.
2. The initiation EL character of transmission is detected from the start bit, which is always 0.
3. The character bits always follow the start bit.
4. After the last bit of the character is transmitted, a stop bit is detected when the line returns to the 1-state for at least one bit time.

Select the correct answer using the code given below.

- (a) 1 and 2 only (b) 2 and 3 only
(c) 1, 3 and 4 only (d) 1, 2, 3 and 4

Ans. (d)

All the given statements are correct.

End of Solution

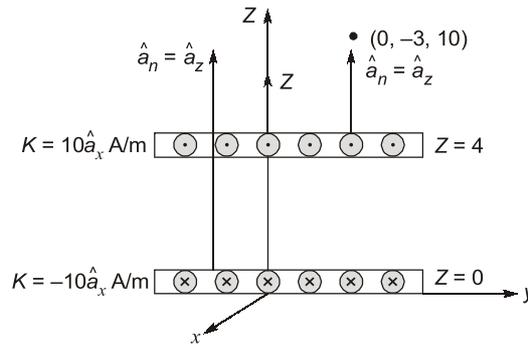
Q.112 In the serial transmission of a terminal whose transfer rate is 10 characters per second and each transmitted character consists of a start bit, eight information bits and two stop bits, for a total of 11 bits, the approximate baud rate is

- (a) 110 baud (b) 11 baud
(c) 210 baud (d) 10 baud

Q.119 The planes $z=0$ and $z=4$ carry currents $K = -10\hat{a}_x$ A/m and $K = 10\hat{a}_x$ A/m is respectively. The value of H at $(0, -3, 10)$

- (a) 0 A/m (b) $5\hat{a}_z$ A/m
(c) $10\hat{a}_z$ A/m (d) $10\hat{a}_y$ A/m

Ans. (a)



Mathematically, $\vec{H} = \frac{1}{2} (\vec{K} \times \hat{a}_n)$

$\Rightarrow \vec{H} = \frac{1}{2} [-10\hat{a}_x \times \hat{a}_z] + \frac{1}{2} [10\hat{a}_x \times \hat{a}_z]$

$\Rightarrow \vec{H} = -5[\hat{a}_x \times \hat{a}_z] + 5[\hat{a}_x \times \hat{a}_z] = 0$ A/m

End of Solution

Q.120 Consider the following statements regarding transmission lines:

1. In single-stub matching, the combination of stub and line presents a conductance which is equal to twice of the characteristic conductance of the line.
2. Single-stub matching is useful for all frequencies because the position of the stub can be varied with the variation in frequency.
3. Single-stub matching system is a narrowband system.

Which of the above statements are **not** correct?

- (a) 1 and 2 only (b) 1 and 3 only
(c) 2 and 3 only (d) 1, 2 and 3

Ans. (a)

Statement 2 is wrong.

End of Solution

Q.121 Consider the following statements regarding Mason's gain formula $T = \sum_{k=1}^k \frac{P_k \Delta_k}{\Delta}$:

1. P_k is the forward path transmittance of kth path from a specified input node to an output node.
2. Δ is the graph determinant which involves closed-loop transmittances and mutual interactions between non-touching loops.

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Ans. (d)

End of Solution

Q.125 Consider the following statements regarding principles of control systems:

1. In control system, controlling variable is the excitation applied to a control system from an external source. It is also a motivating input signal to the system, which is independent of the output of the system.
2. One of the important features of feedback is reduced sensitivity of the ratio of the output to input to variations in system characteristics.
3. In the system, the actuating signal is the difference between the reference input and feedback signal.

Which of the above statements are correct?

- | | |
|------------------|------------------|
| (a) 1 and 2 only | (b) 1 and 3 only |
| (c) 2 and 3 only | (d) 1, 2 and 3 |

Ans. (d)

End of Solution

Q.126 The steady-state errors for type 0 and type 1 systems for the parabolic input are, respectively

- | | |
|---------------|---------------------------|
| (a) 0 and 0 | (b) 0 and $1/K$ |
| (c) $1/K$ and | (d) ∞ and ∞ |

Ans. (d)

End of Solution

Q.127 Consider following the statements regarding feedback compensation:

1. The design procedure for a cascade compensator is more direct than those for a feedback compensator.
2. A faster response can be achieved by the use of parallel compensation.
3. The degree of accuracy and stability of a control system can be improved by the use of a series compensator.

Which of the above statements are correct?

- | | |
|------------------|------------------|
| (a) 1 and 2 only | (b) 1 and 3 only |
| (c) 2 and 3 only | (d) 1, 2 and 3 |

Ans. (d)

End of Solution

Q.128 The stability limit of the servomechanism having open-loop transfer function

$$G(s)H(s) = \frac{K_a(2 + sT_1)}{s^2(1 + sT_2)}$$

- | | |
|------------------|-----------------------|
| (a) $T_1 > T_2$ | (b) $T_1 > (3/2) T_2$ |
| (c) $T_1 > 2T_2$ | (d) $T_2 > (1/2) T_1$ |

Ans. (c)

$$\text{Given, } s^3 T_2 + s^2 + K_a T_1 + 2K_a = 0$$

$$\text{By comparison with } as^3 + bs^2 + cs + d = 0$$

$$bc = K_a T_1$$

$$ad = 2K_a T_2$$

$$\therefore bc > ad \Rightarrow K_a T_1 > 2K_a T_2$$

$$\therefore T_1 > 2T_2$$

End of Solution

Q.129 How many number of roots are there on the right half of the s-plane for the system whose characteristic equation is given below?

$$s^6 + s^5 - 2s^4 - 3s^3 - 7s^2 - 4s - 4 = 0$$

(a) 0

(b) 1

(c) 2

(d) 3

Ans. (b)

Given characteristics equation,

$$s^6 + s^5 - 2s^4 - 3s^3 - 7s^2 - 4s - 4 = 0$$

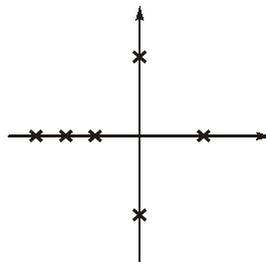
$$\begin{array}{l|llll} s^6 & 1 & -2 & -7 & -4 \\ s^5 & 1 & -3 & -4 & \\ s^4 & 1 & -3 & -4 & \\ s^3 & \phi_4 & \phi_{-6} & & \\ s^2 & -1.5 & -4 & & \\ s^1 & -\frac{50}{3} & & & \\ s^0 & -4 & & & \end{array}$$

The auxiliary equation, $s^4 - 23s^2 - 4$

$$\frac{d}{ds} [s^4 - 3s^2 - 4]$$

$$4s^3 - 6s$$

The placement of roots



Since there is one sign change in the below row of zeros, hence one pole must be on right side which is symmetrical.

End of Solution

Q.133 For the simple arithmetic expression $(A + B) * [C * (D + E) + F]$, the expression can be written in reverse Polish notation as

- (a) $AB + DE * C + F + *$ (b) $A * B + D * E + C + F *$
 (c) $AB + DE + C * F + *$ (d) $A + B * D + E + C * F$

Ans. (c)

Reverse Polish Notation (means post fix notation) is a way of writing expressions without brackets.

Rule: Operator comes after its operands $(A + B) * (C * (D + E) + F)$

- We convert it from inside to outside.
- First bracket is $(A + B)$ i.e., $AB+$
- Second bracket is $(D + E)$ i.e., $DE+$
- Next bracket is $C * (D + E)$ i.e., $DE + C *$
- Next bracket is $C * (D + E) + f$ i.e., $DE + C * F +$
- Multiply with $(A + B)$, i.e., $AB + DE + C * F + *$

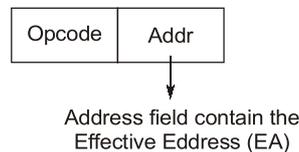
End of Solution

Q.134 In which one of the following addressing modes, the effective address is equal to the address part of the instruction?

- (a) Implied Mode (b) Direct Address Mode
 (c) Immediate Mode (d) Register Indirect Mode

Ans. (b)

Direct AM instruction design is



End of Solution

Q.135 In an 8-bit ALU, let $A = 11110000$ and $B = 00010100$. After performing $A - B$, the compare instruction updates the status bits as

- (a) $C = 1, S = 1, V = 0, Z = 0$ (b) $C = 0, S = 0, V = 1, Z = 1$
 (c) $C = 1, S = 1, V = 1, Z = 1$ (d) $C = 0, S = 0, V = 1, Z = 0$

Ans. (*)

$$A - B = A + (2\text{'s complement of } B)$$

Here, $CY = 1$ but for subtraction operation

$$CY = \overline{CY} = 0$$

$$S = 1, Z = 0, V = 0 \quad (\because C_7 \oplus C_8 = 1 \oplus 1 = 0)$$

No option is matching.

End of Solution

