

ESE 2025

Main Exam Detailed Solutions

Electrical Engineering

PAPER-II

EXAM DATE: 10-08-2025 | 02:00 PM to 05:00 PM

MADE EASY has taken due care in making solutions. If you find any discrepency/error/typo or want to contest the solution given by us, kindly send your suggested answer(s) with detailed explanation(s) at:

info@madeeasy.in

Corporate Office: 44-A/1, Kalu Sarai, Near Hauz Khas Metro Station, New Delhi-110016

Delhi | Hyderabad | Bhopal | Jaipur | Pune | Kolkata





ANALYSIS

Paper-II **Electrical Engineering ESE 2025 Main Examination** SI. Marks **Subjects** Analog and Digital Electronics 1. 92 **Power Systems** 2. 84 3. Systems & Signal Processing 72 4. **Control Systems** 64 **Electrical Machines** 84 5. **Power Electronics** 6. 84 Total 480

Scroll down for detailed solutions



Advance Ranker Batch for ESE & GATE 2026



Commencement Dates:

CE 9 Aug 2025

ME 10 Aug 2025

CS 13 Aug 2025

EE | EC 11 Aug 2025

Teaching Hours:

GATE: 300-350 Hrs

ESE + GATE : 400-450 Hrs

Course Validity:

Till 28 Feb, 2026

Mode: Live-Online

- Live-online classes by experienced faculty.
- Specially designed for repeaters and serious aspirants.
- Focus on enhancing problem-solving skills, speed, and accuracy.
- ✓ Includes 2000+ advanced-level practice questions in PDF format.
- Dedicated online test series for GATE and ESE Prelims.
- Teaching hours: **300–350** for GATE and **400–450** for ESE + GATE.
- **Timings 6 PM to 9 PM,** suitable for college going students & working professionals.
- **Solution** Regular live **Zoom sessions** for doubt resolution and academic guidance.
- Course is offered for Civil, Mechanical, Electrical, Electronics and Computer Science.
- Course validity till 28th February, 2026 for full syllabus coverage and revision.



Low Cost **EMI Facility** Available **Admissions Open**

Download the App





Android

iOS

© 9021300500



SECTION: A

- Q.1 (a) Give the circuit diagram of a negative peak clamper circuit using op-amp, and
 - Considering V_{ref} = +2 V, sketch the output waveform for an input signal $v_i = 2 \sin(1000t)$.
 - (ii) Provide conditions to achieve precision clamping and explain how will you protect op-amp against excessive discharge currents.
 - (iii) State how will you modify your circuit to achieve positive peak clamping.

[12 marks : 2025]

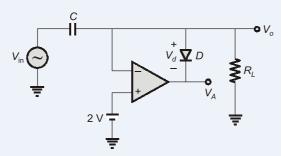
Solution:

Given negative peak clamper using op-amp.

Consider

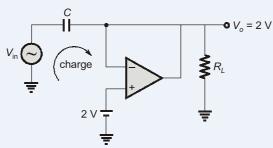
$$V_{REF} = 2 V$$

$$V_{in} = 2 \sin(1000t)$$



Initially capacitor is uncharged.

$$V_{\rm in} > 2$$
 V, $V_{\rm d}$ (-ve) $V_{\rm A} = -V_{\rm s}$ at 0 is ON

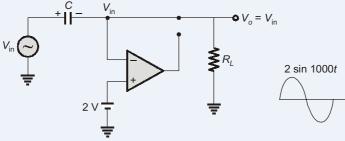


But in the given question

$$V_{\rm in} = 2 \sin(1000t)$$

That means,





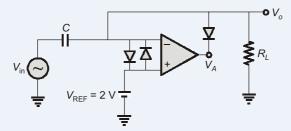
Electrical Engineering

PAPER-II

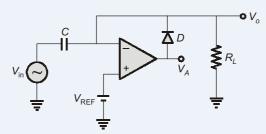
(ii) To achieve precision clamping

$$V_{\rm in} > V_{\rm RFF}$$

To protect op-amp against excessive discharge amount, we have to take two diodes in between inverting and non-inverting terminals.



(iii) To modify negative clamper to positive clamper



End of Solution

Q1 (b) A 7.5 kW, 440 V, 3-phase, star-connected, 50 Hz, 4-pole squirrel cage induction motor develops full load torque at a slip of 3% when operated at rated voltage and frequency. The leakage reactances of stator and rotor windings are five times the respective stator and rotor resistances. The ratio of stator to rotor winding is 3:5. Determine the percentage increase in stator reactance to limit starting current to 2.5 times the full load current. Assume R_1 and R_2 are equal and of requisite amount; and motor has negligible magnetising reactance and core losses.

[12 marks : 2025]

Solution:

Given Data: 7.5 kW, 440 V, 4 pole, 50 Hz

Full load slip = 0.03

$$R_1 = R_2$$

$$X_1 = 5R_1; \quad X_2 = 5R_2$$

Stator to rotor turns ratio = 3:5

But given $R_1 = R_2$ which implies stator to rotor turns ratio is 1.

Question is also not framed for ratio 3:5. After solving for a ratio 3:5, it is considered as ratio 1, as per question and given hints.

.. For this question solution, stator to rotor turns ratio is 1.

Given:

$$R_1 = R_2$$

$$R_1 = R_2'$$

Electrical Engineering

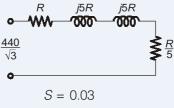
PAPER-II

Let

$$X_1 = X'_1$$

 $R_1 = R_2 = R$
 $X_1 = 5R_1 = 5R$
 $X_2' = 5R_2 = 5R$

Equivalent Circuit:



At full load,

By substituting

$$S = 0.03$$

Finding Z_f and I_f .

$$Z_f = \sqrt{\left(R + \frac{R}{0.03}\right)^2 + (5R + 5R)^2} = 35.76R$$

$$V = 440/\sqrt{3} = 7.1$$

 $I_f = \frac{V}{Z_f} = \frac{440/\sqrt{3}}{35.76R} = \frac{7.1}{R}$

Similarly, find starting current without adding reactance.

By substituting

$$S = 1$$

$$Z_{st} = \sqrt{(R+R)^2 + (5R+5R)^2} = 10.198R$$

$$I_{st} = \frac{V}{Z_{st}} = \frac{440/\sqrt{3}}{10.198R} = \frac{24.91}{R}$$

Ratio of starting current to full load current (without adding any reactance)

$$\frac{I_{st}}{I_f} = \frac{24.91/R}{7.1/R} = 3.5$$

This ratio must be equal to 2.5 by additing additional reactance.

Starting impedance with added reactance:

$$Z'_{st} = \sqrt{(R+R)^2 + (X_1 + X'_2 + X_{add})}$$

$$= \sqrt{(R+R)^2 + (10R + X_{add})^2}$$

$$I'_{st} = \frac{V}{Z'_{st}}$$

$$I_f = \frac{V}{Z_f}$$

$$\frac{I'_{st}}{I_f} = \frac{Z_f}{Z'_{st}} = 2.5$$

Electrical Engineering

PAPER-II

$$\frac{35.76R}{\sqrt{4R^2 + (10R + X_{add})^2}} = 2.5$$

Dividing both sides by R and squaring on both sides

$$\frac{X_{\text{add}}}{R} \cong 4.158 \text{ or } X_{\text{add}} = 4.158R$$

Original stator resistance,

$$X_{\star} = 5R$$

% increase =
$$\frac{X_{\text{add}}}{X_1} \times 100 = \frac{4.158R}{5R} \times 100 = 83.16\%$$

 \therefore Stator reactance must be increased by 83.16% to limit starting current to 2.5 times I_{t}

End of Solution

Q1 (c) In a short-circuit test on a 3-pole, 110 kV circuit breaker, power factor of the fault was 0.4, the recovery voltage was 0-95 times full line value. The breaking current was symmetrical. The frequency of oscillation of restriking voltage was 15,000 cycles/sec. Estimate the average rate of rise of restriking voltage. The neutral is grounded and fault involves earth.

[12 marks: 2025]

Solution:

RRRV Avg =
$$\frac{\text{Maximum value of restriking voltage}}{\text{Time taken to reach that value}}$$

Time taken,
$$f = \frac{\pi}{\omega_0} = \frac{\pi}{2\pi d_0} = \frac{1}{2f_0} = \frac{1}{2 \times 15 \times 10^3} = \frac{1}{3 \times 10^4}$$

Maximum value restriking voltage = $2V_{ar}$

$$V_{ar} = K_1 K_2 K_3 V_{ar}$$

$$V_m = \sqrt{2} \times \frac{1100}{\sqrt{3}}$$

$$\cos \phi = 0.4$$

$$\phi = \cos^{-1}(0.4) = 66.02$$

$$K_1 = \sin \phi = \sin 66.02 = 0.9165$$

$$K_2 = 0.95$$

 $K_3 = 1.0$ as fault is grounded.

$$V_{ar} = (0.9165)(0.95)(1.0)\sqrt{2} \times \frac{110}{\sqrt{3}} = 78.2 \text{ kV}$$

$$V_{\rm rms} = 2V_{ar} = 2 \times 78.2 = 156.4 \text{ kV}$$

$$(RRRV)_{Avg} = \frac{156.4 \times 10^3}{\frac{1}{3 \times 10^4}} = 4.7 \text{ kV/µsec}$$

Electrical Engineering

PAPER-II

Q.1 (d) The standstill impedances of the inner and outer cages of a double cage 3-\$\phi\$ induction motor rotor are given as $Z_{ic} = (0.01 + j0.5)\Omega$ and $Z_{oc} = (0.05 + j0.1)\Omega$ respectively. Assuming the stator impedance to be negligible, determine the approximate ratio of the torques produced by the outer cage (T_{oc}) to the torque produced by the inner cage (T_{ic}) at a slip of s = 0.05? Also determine the net torque developed as a function of T_{oc} and comment on performance as compared to single cage motor.

[12 marks : 2025]

Solution:

 $Z_i = 0.01 + j0.5 \Omega$ Given: Inner cage Outer cage

 $Z_o = 0.05 + j0.1 \Omega$

 $T = \frac{3 \times 60}{2\pi N_s} \cdot \frac{SE_2^2 R_2}{R_2^2 + (SX_2)^2} \text{ N-m}$ Torque,

Torque developed by outer cage to inner cage at start s = 1

$$\frac{T_{oc}}{T_{ic}} = \frac{\frac{3 \times 60}{2\pi N_s} \times \frac{E_2^2 \times 0.05}{0.05^2 + (0.1)^2}}{\frac{3 \times 60}{2\pi N_s} \times \frac{E_2^2 \times 0.01}{0.01^2 + 0.5^2}} = \frac{\frac{0.05}{0.0125}}{\frac{0.01}{0.2501}} = \frac{4}{0.04} = 100$$

Outer cage develops starting torque 100 times inner cage.

Torque ratio of outer cage to inner at s = 0.05.

$$\frac{T_{oc}}{T_{ic}} = \frac{\frac{0.05 \times 0.05}{0.05^2 + (0.05 \times 0.1)^2}}{\frac{0.005 \times 0.01}{(0.01)^2 + (0.05 \times 0.5)^2}} = \frac{\frac{0.0025}{0.002525}}{\frac{0.0005}{0.000725}} = \frac{0.99}{0.689}$$

$$= 1.4355$$

At s = 0.05,

or

$$T_{oc} = 1.435 T_{ic}$$

During running, torque developed by outer cage,

$$T_{oc} \propto 0.99$$

Torque developed by inner cage,

$$T_{ic} \propto 0.689$$

Total/Net torque = $T_{oc} + T_{ic} = 0.99 + 0.689 = 1.679$

$$\frac{T_{\text{net}}}{T_{\text{oc}}} = \frac{1.679}{0.99} = 1.695$$

 $T_{\rm net} = 1.695 T_{\rm oc}$

$$\frac{T_{\text{net}}}{T_{ic}} = \frac{1.679}{0.689}$$

$$T_{\rm net} = 2.437 T_{ic}$$

Comments: At start s = 1, outer cage (high resistance) produce large torque, 100 times inner cage.

Electrical Engineering

PAPER-II

.. Double cage rotor provides very high starting torque. Best suitable for high inertia or higher starting torque loads/hard to start loads.

At running condition, inner cage (low R and X) at low slip becomes relatively more effective and produce good torque in low slip region.

Compared with single cage rotor, double cage rotor gives higher starting torque and better torque at running at low slip.

Double cage has better and improved starting torque without sacrificing running efficiency.

End of Solution

Q.1 (e) A 10 MVA, 13.8 kV turbo-generator having $X_d'' = X_2 = 15\%$ and $X_0 = 5\%$ is about to be connected to power system. The generator has current limiting reactor of $0.7\,\Omega$ in the neutral. Before the generator is connected to the system, its voltage is adjusted to 13.2 kV. When a double line to ground fault develops at terminal 'b' and 'c' find the initial symmetrical r.m.s. currents in the ground and in line 'b'.

[12 marks : 2025]

Solution:

Let the base MVA be 10 and base kV be 13.8

Base current,
$$I_{B} = \frac{\text{MVA} \times 1000}{\sqrt{3} \times 13.8} = \frac{10000}{\sqrt{3} \times 13.8} = 418.37 \, \text{A}$$
 Now,
$$Z_{n} = j0.7 \, \Omega$$

$$Z_{n(p.u.)} = j0.7 \times \frac{10}{(13.8)^{2}} = j0.03675 \, \text{p.u.}$$

$$Z_{0} = Z_{g0} + 3Z_{n} = j0.05 + 3 \times j0.03675 = j0.16 \, \text{p.u.}$$
 Also,
$$I_{a1} = \frac{E_{a}}{Z_{1} + \frac{Z_{2}Z_{0}}{Z_{2} + Z_{0}}}$$

$$E_{a} = \frac{13.2}{13.8} \, \text{p.u.} = 0.9565 \, \text{p.u.}$$

$$I_{a1} = \frac{0.9565}{j0.15 + \frac{j0.15 \times j0.16}{j0.15 + j0.16}} = -j4.206 \, \text{p.u.}$$
 Now,
$$V_{a0} = V_{a2} = V_{a1} = E_{a} - I_{a1}Z_{1} = 0.9565 - (-j4.206) \times j0.15 = 0.3256 \, \text{p.u.}$$

$$I_{a2} = \frac{-V_{a2}}{Z_{2}} = \frac{-0.3256}{j0.16} = j2.171 \, \text{p.u.}$$

$$I_{a0} = \frac{-V_{a0}}{Z_{0}} = \frac{-0.3256}{j0.16} = j2.035 \, \text{p.u.}$$

Initial symmetrical rms current in line b,

$$I_b = a^2 I_{a1} + a I_{a2} + I_{a0}$$



Live-Online

General Studies & **Engineering Aptitude** for ESE 2026 Prelims (Paper-I)

- Course duration approx. 3 Months.
- 200 Hrs of comprehensive classes.
- Teaching pedagogy similar to the classroom course.
- Study material will be provided.
- Streams: CE, ME, EE, E&T



Total 8 Subjects are covered

(Engineering Maths and Reasoning Aptitude will not be covered)

- Current Affairs
- General Principles of Design, Drawing & Safety
- Standards and Quality Practices in Production, Construction, Maintenance and Services
- Basics of Energy and Environment
- Basics of Project Management
- Basics of Material Science and Engineering
- Information and Communication Technologies
- **Solution** Ethics and values in Engineering Profession

Batches commenced from

25 Aug 2025



Electrical Engineering

PAPER-II

=
$$-j4.206(-0.5 - j0.866) + j2.171(-0.5 + j0.866) + j2.035$$

= $-5.52 + j3.05250 = 6.3 \angle 151.06$ p.u.

Initial symmetrical rms current in ground wire

$$= 3I_{a0} = 3 \times j2.035 \times 418.37$$

= 2554.15∠90° A

End of Solution

Q2 (a) A 15 km long 3-phase overhead line delivers 5 MW at 11 kV at a power factor of 0.8 lagging. Line loss is 12% of the power delivered. Line inductance is 1.1 mH per km per phase.

Calculate:

- (i) Sending end voltage and voltage regulation.
- (ii) Power factor of the load to make voltage regulation zero.

[15 + 5 marks : 2025]

Solution:

$$I = 15 \text{ km}, P_R = 5 \text{ MW}, V_R = 11 \text{ kV}, \cos V_R = 0.8 \text{ lag}$$

$$L = 1.1 \text{ mH/km}, P_1 = 12\% \text{ of load}$$

$$P_L = \frac{12}{100} \times 5 = 0.6 \text{ MW}$$

$$I_R = \frac{5 \times 10^3}{\sqrt{3} \times 1180.8} = 328 \text{ A}$$

$$P_I = 3I^2R$$

$$R = \frac{0.6 \times 10^6}{3(328)^2} = 1.86 \,\Omega$$

$$X_t = 2\pi \times 650 \times 1.1 \times 10^{-3} \times 15 = 5.18 \Omega$$

$$Z = 1.86 + 855.18 = 5.5 \angle 70.2^{\circ}$$

$$V_{\rm Plph} = \frac{11000}{\sqrt{3}} = 6351 \text{ Volt}$$

$$V_S = V_R + ZI_R = 6351\angle 0^\circ + 5.5\angle 70.2^\circ \times 328\angle -36.86^\circ$$

= 7920\angle 7.2^\circ Volt

(i)
$$V_s(l-l) = \sqrt{3} \times 7920 = 13.7 \text{ kV}$$

$$%R_{\text{eq}} = \frac{V_S - V_R}{V_R} \times 100 = \frac{13.7 - 11}{10} \times 100 = 24.54\%$$

(ii) At zero regulation the condition is $\theta + \phi = 90^{\circ}$

$$\phi = 90 - \theta = 90 - 70.2 = 19.8^{\circ}$$

$$P.F. = \cos 19.8 = 0.94 \text{ lead}$$

Q.2 (b) A 2200/220 V, single phase transformer has maximum possible voltage regulation of 6% and it occurs at a power factor of 0.3 lag. Find the load voltage at full load at a power factor of 0.8 lead.

[20 marks : 2025]

Solution:

The p.u. voltage regulation of transformer is given as,

$$V.R._{(pu)} = Z_{pu} \cos(\theta_{eq} - \phi) pu$$

Maximum voltage regulation occurs at $\phi = \theta_{eq}$

$$\phi = \theta_{eq} = \cos^{-1}(0.3) = 72.54^{\circ}$$

Maximum voltage regulation,

$$V.R._{(max)} = Z_{pu} = 0.06 pu$$

Load voltage at full load and 0.8 pf leading is given as

$$V_L = V_1 - IZ$$

$$V_{I} = 1\angle0^{\circ} - (1\angle36.86^{\circ}) \times (0.06\angle72.54^{\circ})$$

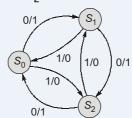
$$V_{I} = 1.0214 \angle -3.17^{\circ} \text{ pu}$$

$$V_{L(actual)} = 1.0214 \times 220 = 224.73 \text{ Volts}$$

Hence, load voltage at full load and 0.8 pf leading is 224.73 Volts.

End of Solution

For the state diagram shown below, design the circuit using D-flip flops. Q.2 (c) (i) Assume S_0 : 00, S_1 : 10 and S_2 : 01.



Realize the signal circuits with minimum number of NAND gates (More than two input NAND gates are allowed).

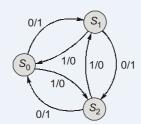
[10 marks : 2025]

Solution:

 $S_0 : 00$

 $S_1 : 10$

 $S_2 : 01$



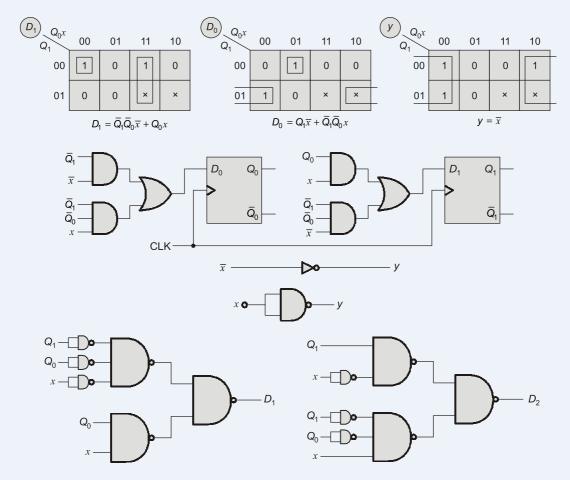
Electrical Engineering

PAPER-II

D-flip flop

State transition table:

Present state		Input	Next state		Output	Flip flop Inputs	
Q ₁	Q_0	x	Q_1^+	Q_0^+	У	D_1	D_0
0	0	0	1	0	1	1	0
0	0	1	0	1	0	0	1
0	1	0	0	0	1	0	0
0	1	1	1	0	0	1	0
1	0	0	0	1	1	0	1
1	0	1	0	0	0	0	0
1	1	0	×	×	×	×	×
1	1	1	×	×	×	×	×



Electrical Engineering

PAPER-II

- Q.2 (c) (ii) An angle modulated signal is given as $X(t) = 20 \cos(12000)t$ for $|t| \le 1$. If the carrier wave frequency $\omega_c = 10000 \text{ rad/sec}$, determine
 - (A) Modulation index m(t), if X(t) were a PM (phase modulated) signal with $K_P = 500 \text{ over } |t| \le 1.$
 - (B) Modulation index m(t), if X(t) were a PM (phase modulated) signal with $K_f = 500 \text{ over } |t| \le 1.$

[10 marks : 2025]

Solution:

Given angle modulated signal

$$X(t) = 20\cos(12000)t; |t| \le 1$$

(A) By considerin given as PM signal

$$SPM(t) = A_c \cos\left[\omega_c t + K_p m(t)\right]$$
 Given,
$$\omega_c = 10000 \, \text{rad/sec}$$

$$K_p = 500 \, \text{rad/volt}$$

$$10000t + 500 \, m(t) = 12000t$$

$$m(t) = 4t \, ; \, |t| \le 1$$

$$S_{FM}(t) = A_c \cos\left[\omega_c t + K_f \int m(t) dt\right]$$

(B)
$$S_{FM}(t) = A_c \cos \left[\omega_c t + K_f \right] m(t) dt$$
Given
$$K_f = 500 \text{ rad/sec volt}$$

$$10000 + K_f \int m(t) dt = 12000t$$

$$K_f \int m(t)dt = 2000t$$

 $K_f m(t) = 2000 \rightarrow 500 \ m(t) = 2000$
 $m(t) = 4 \ ; \ |t| \le 1$

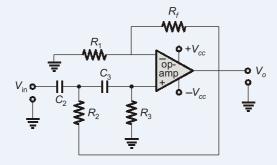
End of Solution

Q3 (a) Give the circuit diagram of a second-order highpass Butterworth filter circuit using op-amp. Evaluate the component values such that the filter has a lower cutoff frequency of 5 kHz and a passband gain $A_f = 2$. Also, give the expression for voltage gain magnitude and sketch its frequency response.

[20 marks : 2025]

Solution:

The second order high pass Butterworth filter circuit using op-amp



Electrical Engineering

PAPER-II

The voltage gain magnitude equation for the second order high pass filter is

$$\left| \frac{V_o}{V_{\text{in}}} \right| = \frac{A_F}{\sqrt{1 + \left(\frac{f_L}{f}\right)^4}}$$

where,

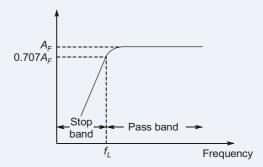
f = Input frequency in Hz

$$f_L$$
 = Lower cut-off frequency in Hz = $\frac{1}{2\pi\sqrt{R_2R_3C_2C_3}}$

For
$$R_2 = R_3 = R$$
 and $C_2 = C_3 = C$,

$$f_L = \frac{1}{2\pi RC}$$

The frequency response of this filter



For cut-off frequency,

$$f_0 = 5 \text{ kHz}$$

Choose $R = 10 \text{ k}\Omega$,

$$f_o = \frac{1}{2\pi RC}$$

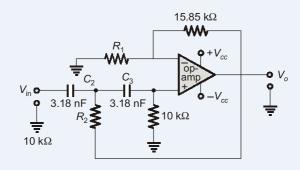
$$C = \frac{1}{2\pi \times 10 \times 10^3 \times 5 \times 10^3} = 3.18 \text{ nF}$$

Given:

$$\frac{R_f}{R_1} = 2$$

Hence,

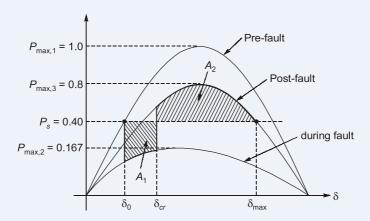
$$R_f = 15.85 \text{ k}\Omega$$



Q3 (b) A 50 Hz alternator is supplying 40% of the power that it is capable of delivering through a transmission line to an infinite bus. A fault occurs that increases the reactances between the generator and the infinite bus to 600% of the value before the fault. When the fault is isolated, the maximum power that can be delivered is 80% of the original maximum value. Find the critical clearing angle.

[20 marks : 2025]

Solution:



Prefault Fault:

$$P_{\text{max1}} = 1.0 \text{ pu}, P_{s} = 0.40 \text{ pu}$$

$$\delta_{\text{o}} = \sin^{-1} \left(\frac{P_{s}}{P_{\text{max1}}} \right) = \sin^{-1} \left(\frac{0.4}{1} \right) = 23.57^{\circ} \text{ or } 0.411 \text{ rad}$$

Post Fault:

$$P_{\text{max}3} = 0.8 \text{ pu}, P_s = 0.40 \text{ pu}$$

$$\delta_{\text{max}} = 180^{\circ} - \sin^{-1} \left(\frac{0.4}{0.8} \right) = 150^{\circ} \text{ or } 2.618 \text{ rad}$$

For system to operate in stable region,

Area,
$$A_1 > \text{Area}$$
, A_2

Acceleration area > Deceleration area

At critical condition,

$$\begin{aligned} \text{Area, } A_1 &= \text{Area, } A_2 \\ \int\limits_{\delta_c}^{\delta_{cr}} (P_s - P_{\text{max}\,2} \sin \delta) d\delta &= \int\limits_{\delta_{cr}}^{\delta_{\text{max}}} (P_{\text{max}\,3} \sin \delta - P_s) d\delta \\ P_s(\delta_{cr} - \delta_o) + P_{\text{max}\,2} (\cos \delta_{cr} - \cos \delta_o) &= P_{\text{max}\,3} (\cos \delta_{cr} - \cos \delta_{\text{max}}) - P_s(\delta_{\text{max}} - \delta_{cr}) \\ (P_{\text{max}\,3} - P_{\text{max}\,2}) \cos \delta_{cr} &= P_s(\delta_{\text{max}} - \delta_o) - P_{\text{max}\,2} \cos \delta_o + P_{\text{max}\,3} \cos \delta_{\text{max}} \\ \cos \delta_{cr} &= \frac{P_s(\delta_{\text{max}} - \delta_o) - P_{\text{max}\,2} \cos \delta_o + P_{\text{max}\,3} \cos \delta_{\text{max}}}{P_{\text{max}\,3} - P_{\text{max}\,2}} \end{aligned}$$

Now, on putting the respective values



Live-Online Course for GENERAL STUDIES for **State Engineering and SSC Exams**

Full Fledged Course for General Studies

Subject Covered: History, General Science, Polity, Environment, Geography, General Knowledge, Economy & Current Affairs

Duration: 3 Months **Validity:** Till 31st Dec 2025

Batch commencing from

1st Sept, 2025 (8 AM - 11: 30 AM) | 25st Sept, 2025 (6 PM - 9: 30 PM)

Key Features

- 250 Hrs of quality teaching by renowned teachers of MADE EASY.
- Printed study material will be dispatched to your address.
- Comprehensive coverage as per latest syllabus and trends of various competitive exams.



Electrical Engineering

PAPER-II

$$\cos\delta_{cr} = \frac{0.4(2.618 - 0.411) - 0.167\cos(23.57^{\circ}) + 0.8\cos(150^{\circ})}{0.8 - 0.169}$$

$$\cos \delta_{cr} = 0.05831$$

Thus, critical clearing angle is

$$\delta_{cr} = \cos^{-1}(0.05831) = 86.65^{\circ}$$

End of Solution

- Q3 (c) A 240 V DC series motor takes 40 A when giving its rated output at 1500 rpm. Its resistance is 0.3 Ω. Calculate the value of resistance that must be added to obtain the rated torque:
 - During starting and
 - (ii) at 1000 rpm.

[20 marks : 2025]

Solution:

Additional resistance value to obtain rated torque at starting:

At start N = 0, $E_b = 0$.

To obtain rated torque,

$$I_a = 40 \text{ A}$$

$$I_a = \frac{V - E_b}{R_a + R_{\text{ext}}}$$

$$40 = \frac{240 - 0}{0.3 + R_{\text{ext}}}$$

$$R_{\rm ext} = 6 - 0.3 = 5.7 \ \Omega$$

 \therefore If 5.7 Ω additional resistance is added in series with motor at start, motor start with rated torque.

(ii)
$$E_b$$
 at 1500 rpm \Rightarrow

$$E_b = 240 - 40(0.3) = 228 \text{ V}$$

$$E_b \propto \phi N$$

For series motor,

$$E_b \propto I_a N$$

$$\phi \propto I_a$$

$$E_b = KI_aN \implies K = \frac{E_b}{I_aN}$$

Constant

$$K = \frac{228}{40 \times 1500} = 0.0038$$

At N = 1000 rpm and $I_a = 40$ A

$$E_b = KI_a N = 0.0038 \times 40 \times 1000 = 152 \text{ V}$$

 R_{ext} for new $E_b = 152 \text{ V}$

$$E_b = V - I_a(R_a + R_{\text{ext}})$$

152 = 240 - 40(0.3 + R_{ext})

$$R_{\rm ext} = 1.9 \ \Omega$$

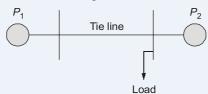
 \therefore By adding a resistance of 1.9 Ω in series with motor, rated torque is developed at 1000 rpm by taking $I_a = 40 \text{ A}$.

Q.4 (a) The figure shows a two-bus system. If a power of 125 MW is transferred from Plant 1 to the load, a power loss of 15.625 MW occurs. Find the generation schedule and load demand if the cost of received power is ₹24 per MWh. The incremental production costs are:

$$\frac{dF_1}{dP_2} = 0.025P_1 + 15$$
;

$$\frac{dF_1}{dP_1} = 0.025P_1 + 15$$
; $\frac{dF_2}{dP_2} = 0.05P_2 + 20$

Assume the penalty factor of the 2^{nd} generator = 1.



[20 marks : 2025]

Solution:

Power loss equation is given as

$$P_L = B_{11}P_1^2 + B_{22}P_2^2 + 2B_{12}P_1P_2$$

: load is concentrated on bus-2 of plant-2

$$B_{12} = 0 \text{ and } B_{22} = 0$$

Therefore,
$$P_L = B_{11}P_1^2$$

Now, as per data

 \Rightarrow

Also,

$$P_L = 15.625 = B_{11} \times (125)^2$$

$$\Rightarrow B_{11} = 1 \times 10^{-3}$$

Now, penalty factor for plant (1)

$$L_{1} = \frac{1}{1 - \frac{\partial P_{L}}{\partial P_{1}}} = \frac{1}{1 - 2B_{11}P_{1}}$$

Also, peanlty factor for plant (2)

$$L_2 = 1.0$$

For economic power generation

$$IC_{1}L_{1} = IC_{2}L_{2} = \lambda$$

$$\frac{0.025P_{1} + 15}{1 - 2 \times 1 \times 10^{-3}P_{1}} = 24$$

$$0.025P_{1} + 15 = 24 - 48 \times 10^{-3}P_{1}$$

$$P_{1}(0.025 + 0.048) = 9$$

$$P_{1} = 123.30 \text{ MW}$$

$$IC_{2}L_{2} = \lambda$$

$$0.05P_{2} + 20 = 24$$

$$P_{2} = 80 \text{ MW}$$

Electrical Engineering

PAPER-II

Now, generation schedule for

 $P_1 = 123.30 \,\text{MW}$ Plant-1,

 $P_2 = 80 \, \text{MW}$ Plant-2.

 $P_D = P_1 + P_2 - P_L$ Load demand,

 $P_D = 123.30 + 80 - 1 \times 10^{-3} \times (123.30)^2$

 $P_D = 188.1 \,\text{MW}$

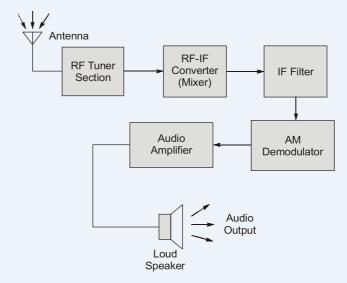
End of Solution

Q4 (b) (i) Give the block diagram of an AM Receiver and an FM Receiver. Also, explain each block.

[10 marks : 2025]

Solution:

AM Receiver: The AM super heterodyne receiver takes the amplitude modulated wave as an input and produces the original audio signal as an output. Selectivity is the ability of selecting a particular signal, while rejecting the others. Sensitivity is the capacity of detecting RF signal and demodulating it, while at the lowest power level. Radio amateurs are the initial radio receivers. However, they have drawbacks such as poor sensitivity and selectivity. To overcome these drawbacks, super heterodyne receiver was invented. The block diagram of AM receiver is shown in the following figure.



RF Tuner Section: The amplitude modulated wave received by the antenna is first passed to the tuner circuit through a transformer. The tuner circuit is nothing but a LC circuit, which is also called as resonant or tank circuit. It selects the frequency, desired by the AM receiver. It also tunes the local oscillator and the RF filter at the same time.

RF Mixer: The signal from the tuner output is sent to the RF-IF converter, which acts as a mixer. It has a local oscillator, which produces a constant frequency. The mixing process is done here, having the received signal as one input and the local oscillator frequency as the other input. The resultant output is a mixture of two frequencies $[(f_1 + f_2), (f_1 - f_2)]$ produced by the mixer, which is called as intermediate frequencies (IF).

Electrical Engineering

PAPER-II

Intermediate Frequency (IF).

The production of IF helps in the demodulation of any station signal having any carrier frequency. Hence, all signals are translated to a fixed carrier frequency for adequate selectivity.

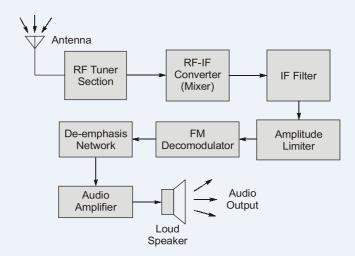
IF Filter: Intermediate frequency filter is a band pass filter, which passes the desired frequency. It eliminates all other unwanted frequency components present in it. This is the advantage of IF filter, which allows only IF frequency.

AM Demodulator: The received AM wave is now demodulated using AM demodulator. This demodulator uses the envelope detection process to receive the modulating signal.

Audio Amplifier:

This is the power amplifier stage, which is used to amplify the detected audio signal. The processed signal is strengthened to be effective. This signal is passed on to the loudspeaker to get the original sound signal.

FM Receiver: The block diagram of FM receiver is shown in the following figure.



This block diagram of FM receiver is similar to the block diagram of AM receiver. The two blocks Amplitude limiter and De-emphasis network are included before and after FM demodulator. The operation of the remaining blocks is the same as that of AM receiver.

We know that in FM modulation, the amplitude of FM wave remains constant. However, if some noise is added with FM wave in the channel, due to that the amplitude of FM wave may vary. Thus, with the help of amplitude limiter we can maintain the amplitude of FM wave as constant by removing the unwanted peaks of the noise signal.

In FM transmitter, we have seen the pre-emphasis network (High pass filter), which is present before FM modulator. This is used to improve the SNR of high frequency audio signal. The reverse process of pre-emphasis is known as de-emphasis. Thus, in this FM receiver, the de-emphasis network (Low pass filter) is included after FM demodulator. This signal is passed to the audio amplifier to increase the power level. Finally, we get the original sound sigle from the loudspeaker.



Electrical Engineering

PAPER-II

Q.4 (b) (ii) What is the largest value of output voltage from an 8-bit DAC that produces 2.0 V for a digital input of 01110010?

[10 marks: 2025]

Solution:

Convert to decimal:

$$01110010_2 = 64 + 32 + 16 + 2 = 114$$

So, for digital code 114, DAC output = 2.0 V

For an n-bit DAC (n = 8 here), output voltage is :

$$V_{\text{out}} = \frac{D}{2^{n} - 1} \times V_{FS}$$

$$V_{\text{out}} = \frac{D}{255} \times V_{FS}$$

$$2.0 = \frac{114}{255} \times V_{FS}$$

$$V_{FS} = \frac{2.0 \times 255}{114} = 4.47 \text{ V}$$

Largest possible digital input = 255(1111 1111)₂

$$V_{\text{max}} = \frac{255}{255} \times V_{FS} = V_{FS} \approx 4.47 \text{ V}$$

End of Solution

- Q4 (c) A 3-phase, 6-pole, 500 kVA, 6600 V, 50 Hz star-connected synchronous motor having synchronous impedance of $j80 \Omega$ per phase operates at unity power factor at rated conditions.
 - Determine the mechanical torque driving capability for this motor at rated conditions, neglecting all mechanical losses.
 - (ii) At this rated torque, what are the required deviations from rated armature current and excitation (in terms of $E \angle \delta$) to produce a maximum torque of 1.26 times the maximum rated torque for a leading power factor operation of the motor?
 - (iii) Determine the value of the leading power factor for motor operation as stated in (ii) above.

[20 marks : 2025]

Solution:

 $T = \frac{60}{2\pi N_s} P \text{ N-m}$ (i) Find Torque:

500 kVA motor operating at U.P.F., rated conditions means P_{out} = 500 kW.

$$N_{\rm S} = \frac{120f}{P} = \frac{120 \times 50}{6} = 1000 \text{ rpm}$$

Electrical Engineering

PAPER-II

$$T = \frac{60}{2\pi(1000)} \times 500 \times 10^3$$

T = 4777.07 N-m (Rated torque)

(ii) Deviations required in terms of $E \angle \delta$ to produce maximum torque of 1.26. Rated torque for leading P.f operation of motor.

Given : $R_a = 0$, Neglect mech. loss

$$P_{i/p} = P_{o/p}$$

$$T \propto P \propto E$$

For a given V and X_s ,

$$P = \frac{EV}{X_S} \sin \delta$$

Maximum P or maximum T occurs at $\sin \delta = 1$

$$\delta = 90^{\circ}$$

$$V = \frac{6600}{\sqrt{3}} = 3810.512 \text{ V}$$

Rated current,

:.

$$I_a = \frac{500 \times 10^3}{\sqrt{3} \times 6600} = 43.7387 \text{ A}$$

At rated unity P.f. operation

$$E_b \angle -\delta = V \angle 0 - I_a \angle \phi \ (Z_s \angle \theta)$$

= 3810.512 - 43.73\(\neg 0^\circ\) (j80)
= 5173.36\(\neg -42.56^\circ\)

Mentioned maximum torque occurs at $\delta = 90^{\circ}$, $\sin \delta = 1$

$$T \propto P \propto E$$

$$T_{\text{max}} = 1.26T_{\text{rated}}$$

$$E_{b,\text{new}} = 1.26E_{b,\text{rated}} = 1.26 \times 5173.36$$

= 6518.43 V

For the same real power output, but for new $E_b = 6518.43$, same V and X_S ,

$$\delta_{\text{new}} = -32.47^{\circ}$$

As per mentioned case deviation in $E_b \angle \delta$ New $E_b = 6518.43$ Old $E_b = 5173.36$ New $\delta = 32.47^\circ$ Old $\delta = 42.56^\circ$

New
$$F_{c} = 6518.43$$

Old
$$E_{\rm c} = 5173.36$$

New
$$\delta = 32.47^{\circ}$$

Old
$$8 - 42569$$

(iii) Value of leading p.f. as stated in (ii).

For new E_b and δ_i findign new current V = 3810.512

$$I_{a2} \angle \phi_2 = \frac{V \angle 0^\circ - E_{b2} \angle - \delta_2}{jX_s}$$

By solving $I_{a2} = 48.568$ A and $\cos \phi = 0.9$ leading.



GATE 2026 ONLINE TEST SERIES

Streams: CE, ME, EE, EC, CS, IN, PI, CH

Tests are live

Quality Questions

Thoroughly researched, quality questions as per standard & orientation of GATE consisting MCQs, NATs & MSQs

GATE Interface

Test series interface is exactly similar to actual GATE

Anywhere, **Anytime**

Facility to appear in test anywhere & anytime (24 x 7)

Video Solution Get video solutions by senior faculties for proper understanding of concepts

Ask an **Expert** Ask your doubt to our experts, Get answer of your queries on chat window

Step by Step **Solutions**

Detailed, step by step and well illustrated solutions, For user's better understanding

Smart Report

Comprehensive and detailed analysis of test-wise performance. Evaluate yourself and get All India Rank

Virtual **Calculator Embedded**

Make yourself conversant in use of embedded virtual calculator

Available on android, iOS (Desktop & Laptop)



1584 + Newly Designed Questions



Scan to enroll

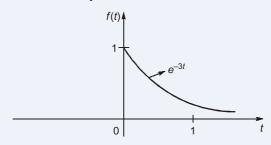
Q Queries: 9021300500

□ queryots@madeeasy.in

Enroll now www.madeeasy.in

SECTION: B

Q.5 (a) An exponential function $f(t) = e^{-3t} u(t)$ as shown in the following figure is delayed by 1 sec. Sketch and describe mathematically the delayed function. Also, repeat the same if f(t) is advanced by 1 second.



[12 marks: 2025]

Solution:

Given function is,

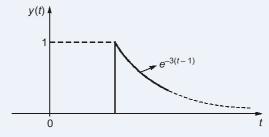
$$f(t) = e^{-3t} u(t)$$

Let the delayed function of f(t) by '1' sec is y(t) then

$$y(t) = f(t-1)$$

$$= e^{-3(t-1)} \cdot u(t-1)$$

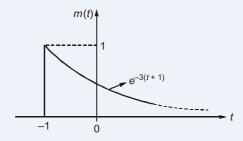
$$= \begin{cases} 0, & t < 1 \\ e^{-3(t-1)}, & t > 1 \end{cases}$$



Let the advanced function of f(t) by '1' sec is m(t) then

$$m(t) = f(t + 1) = e^{-3(t-1)} \cdot u(t-1)$$

$$= \begin{cases} 0, & t < -1 \\ e^{-3(t+1)}, & t > -1 \end{cases}$$



Electrical Engineering

PAPER-II

Q.5 (b) A step-down DC-DC converter is feeding an RLE load with a freewheeling diode across the load. Assuming a ripple-free load current, derive the expression for the maximum duty cycle in terms of supply voltage V_S and back emf of the load Efor which the RMS current through the freewheeling diode has a maximum value.

[12 marks: 2025]

Solution:

For chopper with RLE load, average load current I_0 is

$$I_0 = \frac{V_0 - E}{B} = \frac{\alpha V_s - E}{B}$$

Freewheeling-diode current flows during the period $T_{\rm off}$. Therefore, rms value of freewheelingdiode current, when I_0 is ripple free, is given by

$$\begin{split} I_{fdr} &= \sqrt{\frac{T_{\text{off}}}{T}} \cdot I_0 = \left[\frac{T - T_{\text{on}}}{T} \right]^{1/2} \left[\frac{\alpha V_s - E}{R} \right] \\ &= \frac{1}{R} (\alpha V_s - E) \left(\sqrt{1 - \alpha} \right) \qquad ...(i) \\ &= \frac{1}{R} \left[\alpha \sqrt{1 - \alpha} \cdot V_s - \sqrt{1 - \alpha} \cdot E \right] \\ I_{fdr} &= \frac{1}{R} \left[\sqrt{\alpha^2 - \alpha^3} \cdot V_s - \sqrt{1 - \alpha} \cdot E \right] \end{split}$$

This current I_{fdr} will have maximum value when

$$\frac{dI_{fdr}}{d\alpha} = \frac{1}{R} \left[\frac{1}{2} \frac{(2\alpha - 3\alpha^2)V_s}{\sqrt{\alpha^2 - \alpha^3}} + \frac{1}{2} \frac{E}{\sqrt{1 - \alpha}} \right] = 0$$
or
$$\frac{(2\alpha - 3\alpha^2)V_s}{\sqrt{\alpha^2 - \alpha^3}} = -\frac{E}{\sqrt{1 - \alpha}}$$
or
$$\frac{(2\alpha - 3\alpha^2)\sqrt{1 - \alpha}}{\alpha\sqrt{1 - \alpha}} = -\frac{E}{V_s}$$
or
$$3\alpha - 2 = \frac{E}{V_s} \quad \text{or} \quad \alpha = \frac{1}{3} \left(2 + \frac{E}{V_s} \right)$$

Substituting this value of a in Eq. (i), we get maximum value of rms current rating $I_{fd,rm}$ of freewheeling diode as under:

$$I_{fd.mm} = \frac{1}{R} \left[\frac{1}{3} \left(2 + \frac{E}{V_s} \right) V_s - E \right] \left[1 - \frac{1}{3} \left(2 + \frac{E}{V_s} \right) \right]^{1/2}$$

$$= \frac{1}{R} \left[\frac{1}{3} \left(\frac{2V_s + E}{V_s} \right) V_s - E \right] \left[1 - \frac{2V_s + E}{3V_s} \right]^{1/2}$$

$$= \frac{1}{R} \left[\frac{2V_s + E - 3E}{3} \right] \left[\frac{3V_s - 2V_s - E}{3V_s} \right]^{1/2}$$

Electrical Engineering

PAPER-II

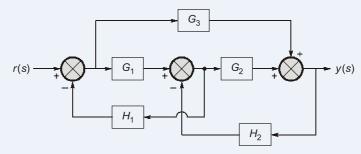
$$= \frac{1}{R} \left[\frac{2V_s - 2E}{3} \right] \left[\frac{V_s - E}{3V_s} \right]^{1/2} = \frac{1}{R} \cdot \frac{2}{3} [V_s - E]^{3/2} \cdot \frac{1}{\sqrt{3V_s}}$$

$$= \frac{1}{R} \cdot \frac{2}{3} \frac{V_s \cdot \sqrt{V_s}}{\sqrt{3V_s}} \left[1 - \frac{E}{V_s} \right]^{3/2} = \frac{2}{3\sqrt{3}} \cdot \frac{V_s}{R} \left[1 - \frac{E}{V_s} \right]^{3/2}$$

$$= 0.3849 \frac{V_s}{R} \left[1 - \frac{E}{V_s} \right]^{3/2}$$

End of Solution

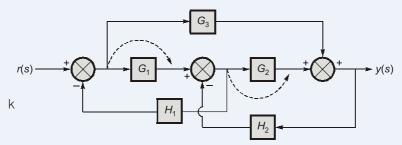
Q.5 (c) The block diagram of a system is as shown below. Find the overall transfer function of the system using block diagram reduction technique.



[12 marks: 2025]

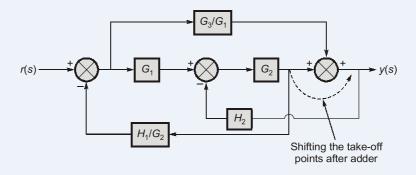
Solution:

Step 1:

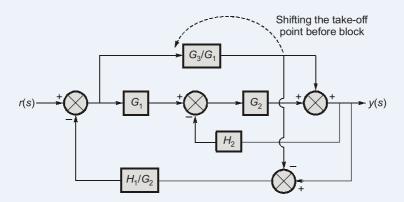


Shifting the take-off points after block

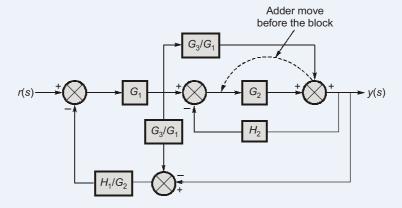
Step 2:



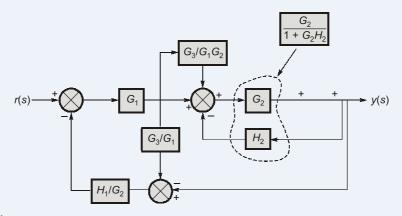
Step 3:



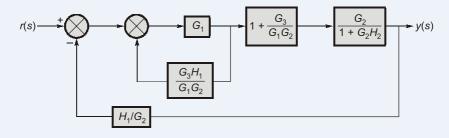
Step 4:



Step 5:



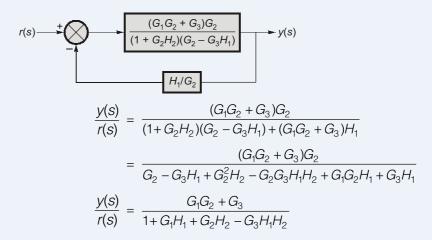
Step 6:



Electrical Engineering

PAPER-II

Step 7:



End of Solution

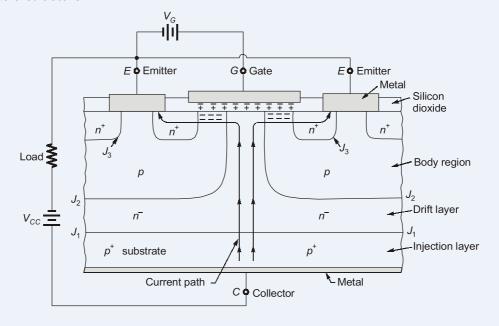
Q.5 (d) (i) Draw the silicon cross-section view of an IGBT and identify the distinguishing feature from MOSFET with reference to conductivity modulation. Also, state its impact on IGBT operation and performance.

[8 marks : 2025]

Solution:

IGBT has been developed by combining into it the best qualities of both BJT and PMOSFET. Thus an IGBT possesses high input impedance like a PMOSFET and has low on-state power loss as in a BJT. Further, IGBT is free from second breakdown problem present in BJT. All these merits have made IGBT very popular amongst power-electronics engineers. IGBT is also known as metal oxide insulated gate transistor (MOSIGT), conductively-modulated field effect transistor (COMPET) or gain-modulated FET (GEMFET). It was also initially called insulated gate transistor (IGT).

Basic structure:



ESE 2025 Main Examination Electrical Engineering PAPER-II

Figure above illustrates the basic structure of an IGBT. It is constructed virtually in the same manner as a power MOSFET. There is, however, a major difference in the substrate. The n^+ layer substrate at the drain in a PMOSFET is now substituted in the IGBT by a p^+ layer substrate called collector C. Like a power MOSFET, an IGBT has also thousands of basic structure cells connected appropriately on a single chip of silicon.

In IGBT, p^+ substrate is called injection layer because it injects holes into m^- layer. The m^- layer is called drift region. As in other semiconductor devices, thickness of m^- layer determines the voltage blocking capability of IGBT. The p layer is called body of IGBT. The p^+ layer in between p^+ and p regions serves to accommodate the depletion layer of pm^- junction, i.e. junction J_2 .

Comparison of BJT, IGBT, and MOSFET as lower elements							
	MOSFET	IGBT					
Drive type	Voltage drive	Voltage drive					
Power for driving	High	Low					
On state voltage	Tends to increase in proportion to breakdown voltage	Low					
Switching speed	High	Moderate					
Temperature stability	Good	Good					
Difficulty of achieving high breakdown voltage	High	Easy with conductivity modulation					

1. Type of Device:

- IGBT: Combines features of both MOSFET and bipolar transistor. It has a voltagecontrolled gate like a MOSFET and a bipolar-like current-carrying capability.
- MOSFET: A voltage-controlled transistor that relies on the voltage applied to the gate to control the flow of current between the source and drain terminals.

2. Voltage Rating:

- IGBT: Generally suitable for higher voltage applications (hundreds to thousands of volts).
- MOSFET: Typically used in lower to medium voltage applications (tens to hundreds of volts).

3. Current Handling:

- IGBT: Well-suited for high current applications. It combines the voltage control of a MOSFET with the current-carrying capability of a bipolar transistor.
- MOSFET: Generally used for lower to moderate current applications.

4. Switching Speed:

- IGBT: Slower switching speed compared to MOSFETs. Suitable for applications where switching speed is not the primary concern.
- MOSFET: Faster switching speed, making them suitable for applications that require high-frequency operation.



ESE 2026 PRELIM EXAM

Online Test Series



An early start gives you an extra edge!!

Test series is live.



Scan to enrol

Key Features:



Newly designed quality questions as per standard of ESE



Due care taken for accuracy



Error free comprehensive solutions.



Comprehensive and detailed analysis report of test performance



Including tests of Paper-I (General Studies & Engineering Aptitude) and Paper-II (Technical syllabus)



All India Ranking



Available on android, iOS (Desktop & Laptop)



Streams Offered: CE, ME, EE, E&T

Electrical Engineering PAPER-II

5. Efficiency:

- IGBT: Lower conduction losses compared to a MOSFET at high voltages and currents. Suitable for high-power applications like motor drives and power inverters.
- MOSFET: More efficient at low voltages and currents. Often used in applications where efficiency and fast switching are critical, such as power supplies and certain amplifiers.

6. Applications:

- IGBT: Commonly used in high-power applications such as motor drives, power inverters, and induction heating systems."
- MOSFET: Widely used in applications where fast switching and efficiency at lower power levels are crucial, such as voltage regulators and electronic switching circuits.

7. Gate Drive Requirements:

- IGBT: Requires a positive voltage on the gate relative to the emitter for turn-on, but the turn-off can be controlled by reducing the gate voltage.
- MOSFET: Requires a positive voltage on the gate relative to the source for both turnon and turn-off.

End of Solution

Q.5 (d) (ii) Draw 2-transistor and simplified equivalent circuits with proper labels and their significance.

[4 marks : 2025]

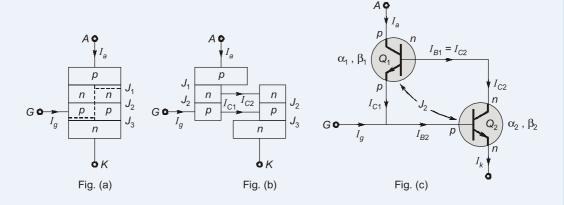
Solution:

The principle of thyristor operation can be explained with the use of its two-transistor model (or two-transistor analogy). Fig. (a) shows schematic diagram of a thyristor. From this figure, two-transistor model is obtained by bisecting the two middle layers, along the dotted line, in two separate halves as shown in Fig. (b). In this figure, junctions $J_1 - J_2$ and $J_2 - J_3$ can be considered to constitute pnp and npn transistors separately. The circuit representation of the two-transistor model of a thyristor is shown in Fig. (c).

In the off-state of a transistor, collector current I_C is related to emitter current I_F as

$$I_C = \alpha I_E + I_{CBO}$$

where α is the common-base current gain and I_{CBO} is the common-base leakage current of collector-base junction of a transistor.



Electrical Engineering

PAPER-II

For transistor Q_1 in figure (c), emitter current I_F = anode current I_A and I_C = collector current I_{C1} . Therefore, for Q_1 ,

$$I_{C1} = \alpha_1 I_a + I_{CBO1}$$

 α_1 = common base current gain of Q_1 where, ...(i)

and I_{CBO1} = common base leakage current of Q_1

Similarly, for transistor Q_2 , the collector current I_{C2} is given by

$$I_{C2} = \alpha_2 I_k + I_{CBO2} \qquad \dots (ii)$$

where

 α_2 = common base current gain of Q_2

 $I_{\rm CBO2}$ = common base leakage current of Q_2

 I_k = emitter current of Q_2 and

The sum of two collector currents given by equation (i) and (ii), is equal to the external circuit current I_a entering at anode terminal A.

$$I_a = I_{C1} + I_{C2}$$
 or
$$I_a = \alpha I_1 + I_{\text{CBO1}} + \alpha I_2 + I_{\text{CBO2}} \qquad ...(iii)$$

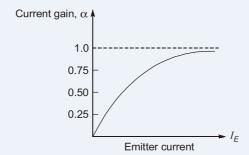
when gate current is applied, then $I_k = I_a + I_a$

Substituting this value of I_k in equation (iii), gives

$$I_{\alpha} = \alpha_1 I_a + I_{\text{CBO1}} + \alpha_2 (I_a + I_g) + I_{\text{CBO2}}$$

$$I_{\alpha} = \frac{\alpha_2 I_g + I_{CBO1} + I_{CBO2}}{1 - (\alpha_1 + \alpha_2)} \qquad \dots (iv)$$





For a silicon transistor, current gain α is very low at low emitter current. With an increase in emitter current, α builds up rapidly as shown in figure. With gate current $I_a = 0$ and with thyristor forward biased, $(\alpha_1 + \alpha_2)$ is very low as per figure. Under these conditions, equation (iv) shows that forward leakage current somewhat more than $(I_{CBO1} + I_{CBO2})$ flows. If, by some means, the emitter current of two component transistors can be increased so that $\alpha_1 + \alpha_2$ approaches unity, then as per equation (iv), I_a would tend to become infinity thereby turning-on the device. Actually, external load limits the anode current to a safe value after the thyristor, in fact, are the methods of making $\alpha_1 + \alpha_2$ to approach unity.

Electrical Engineering

PAPER-II

Q.5 (e) The overall transfer function of a unity feedback system is given by:

$$G_{CL}(S) = \frac{Ks + b}{s^2 + as + b}.$$

- Calculate the open-loop transfer function of the system and its type.
- (ii) If the overall system with K = 0, admits a unity normalized bandwidth and a settling time of 4 seconds for a 2% tolerance band, compute position, velocity, and acceleration error constants. Assume unity DC gain.
- (iii) Compute the sensitivity and complementary sensitivity function value at $\omega = 1 \text{ rad/sec. Consider } K = 1.$
- (iv) Discuss the effect of having a non-zero value of K on the behavior of the system in comparison to that with K = 0.

[12 marks: 2025]

Solution:

Given: Closed loop system with u.f.s.

$$G_{CL}(s) = \frac{L(s)}{1 + L(s)} = \frac{ks + b}{s^2 + as + b}$$

(i) We know that

$$G_{CL}(s) = \frac{G(s)}{1 + G(s)H(s)}$$

For
$$H(s) = 0$$
:

$$G_{CL}(s) = \frac{G(s)}{1 + G(s) - G(s)} = \frac{ks + b}{s^2 + as + b - ks - b} = \frac{ks + b}{s(s + a - k)}$$

Hence, open loop system is

$$G_{CL}(s) = \frac{ks+b}{s(s+a-k)}$$

Number of poles at the origin = 1. Hence given is type-1 system.

(ii) For k = 0:

$$G_{OL}(s) = \frac{b}{s(s+a)}$$

$$T_s = \frac{4}{\xi \omega_n} = 4 \text{ sec}$$

$$\xi \omega_n = 1 \text{ and } \omega_n = 1 \text{ rad/sec}$$

$$\xi = 1 \Rightarrow G_{OL}(s) = \frac{1}{s(s+2)}$$

$$a = 2\xi \omega_n = 2, \quad b = \omega_n^2 = 1$$

$$k_p = \lim_{s \to 0} G_{OL}(s) = \lim_{s \to 0} \frac{1}{s(s+2)} = \infty$$

$$k_p = \lim_{s \to 0} G_{OL}(s) = \lim_{s \to 0} \frac{1}{s(s+2)} = \infty$$

$$k_p = \lim_{s \to 0} G_{OL}(s) = \lim_{s \to 0} \frac{1}{s(s+2)} = 0.$$

 \Rightarrow

Electrical Engineering

PAPER-II

$$k_a = \lim_{s \to 0} s^2 G_{OL}(s) = \lim_{s \to 0} s^2 \cdot \frac{1}{s(s+2)} = 0$$

(iii) For k = 1:

$$\omega_n = 1 \text{ rad/sec}$$

$$G_{OL}(s) = \frac{s+1}{s(s+1)} = \frac{1}{s}$$

Sensitivity,

$$S = \frac{1}{1 + G_{OI}(s)} = \frac{s}{s+1}$$

Put $s = i\omega$:

$$S = \frac{j\omega}{1 + j\omega}$$

$$|S| = \frac{1}{\sqrt{1^2 + 1^2}} = \frac{1}{\sqrt{2}} \simeq 0.707$$

Complementary sensitivity,

$$S_c = \frac{G_{OL}(s)}{1 + G_{OL}(s)}$$

$$=\frac{\frac{1}{s}}{1+\frac{1}{s}}=\frac{1}{s+1}$$

Put $s = j\omega$:

$$S_c = \frac{1}{1+j\omega}\bigg|_{\omega=1}$$

$$|S_c| = \frac{1}{\sqrt{1+1^2}} \simeq 0.707$$

- (iv) The characteristics equation $s^2 + as + b$ do not change.
 - Open Loop Poles/Zeros Change with k: Charging k moves the open-loop poles at s = k - a and moves the zero at $s = \frac{-b}{k}$ (when $k \neq 0$). This affects frequency response, margins and sensitivity.

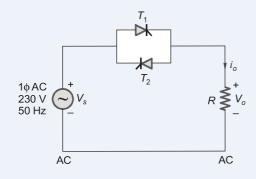
- Q.6 (a) A single-phase AC controller operating on phase control is supplied from a 230 V, 50 Hz AC supply. If the controller is feeding a purely resistive load of 10 Ω at a firing angle of 45°, then determine:
 - The RMS output voltage $V_{0, \text{rmp(phase)}}$ of the phase-controlled AC controller.
 - (ii) The equivalent duty cycle (K) of an integral cycle AC controller that would produce the same RMS output voltage.

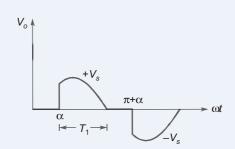
- (iii) If the integral cycle controller operates with a total of 100 cycles for one complete operation, determine the number of 'ON' cycles and 'OFF' cycles for the same as in (ii).
- (iv) The input power factor of the integral duty cycle AC controller operating at the equivalent duty cycle.
- (v) The RMS Thyristor current $I_{T, rms}$ for the integral cycle controller operating at this equivalent duty cycle.

Derive the formula used for integral cycle AC controllers as used in the above parts.

[20 marks : 2025]

Solution:





$$V_{\text{or}} = \left\{ \frac{1}{\pi} \int_{\alpha}^{\pi} V_m^2 \sin^2 \omega t d(\omega t) \right\}^{1/2}$$

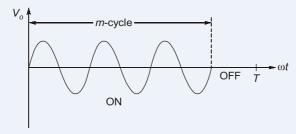
$$V_{\text{or}} = \frac{V_m}{\sqrt{2\pi}} \left\{ (\pi - \alpha) + \frac{1}{2} \sin 2\alpha \right\}^{1/2}$$

$$= \frac{230\sqrt{2}}{\sqrt{2\pi}} \left\{ \left(\pi - \frac{\pi}{4} \right) + \frac{1}{2} \sin \frac{\pi}{2} \right\}^{1/2}$$

(i)
$$V_{or} = 219.304 \text{ V}$$

$$I_{or} = \frac{V_{or}}{R} = \frac{219.304}{10} = 21.93 \text{ A}$$

(ii) Integral Cycle Control Tech:



For m cycles \rightarrow ON

$$\therefore$$
 $V_{o} = V_{s}$

Electrical Engineering

PAPER-II

For n cycles \rightarrow OFF

$$V_0 = 0$$

$$V_{or} = V_{Sr} \left(\frac{m}{m+n}\right)^{1/2} = \sqrt{K} \cdot V_{Sr}$$

where K is the duty cycle of AC voltage constant.

$$V_{or} = \sqrt{K} \cdot V_{sr}$$

m + n = 100 cycles

$$219.304 = \sqrt{K} \cdot 230$$

$$K \text{ (duty check)} = \left(\frac{219.304}{230}\right)^2 = 0.909$$

(iii)
$$m + n = 100$$
 cycles

$$K = 0.909$$

$$\frac{m}{m+n} = 0.909$$

$$m = 0.909 \times 100$$

$$m = 90.9$$
 cycles

Here, load is connected to supply.

For *m*-integral cycles and disconnected.

For *n*-integral cycles.

∴
$$m \simeq 91$$
 (integer)

$$m + n = 100$$
 (ON cycles – 9; OFF cycles – 9)

$$91 + n = 100$$

$$n = 9$$

(iv)
$$PF = \frac{V_{or}}{V_{or}} \rightarrow R \text{ load}$$

$$=\sqrt{K}=\sqrt{\frac{m}{m+n}}=\sqrt{\frac{91}{100}}=0.9539$$

or
$$PF = \frac{V_{or}}{V_{or}} = \frac{219.304}{230} = 0.953$$

$$(I_{7})_{\text{rms}} = \left\{ \frac{1}{2\pi(m+n)} \left[n \int_{0}^{\pi} \left(\frac{V_{m} \sin \omega t}{R} \right)^{2} \cdot d(\omega t) \right] \right\}^{1/2}$$

$$= \left\{ \frac{1}{2\pi \frac{m+n}{n}} \int_{0}^{\pi} (I_m \sin \omega t)^2 d(\omega t) \right\}^{1/2}$$

$$= \sqrt{\frac{n}{m+n}} \left\{ \frac{1}{2\pi} \int_{0}^{\pi} I_{m}^{2} \sin^{2} \omega t \cdot d(\omega t) \right\}^{1/2} = \sqrt{K} \cdot \frac{I_{m}}{2}$$

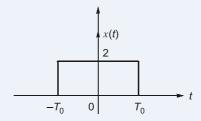
$$I_m = \frac{V_m}{R} = \frac{230\sqrt{2}}{10} = 23\sqrt{2} \text{ Amp}$$

$$= \sqrt{0.909} \times \frac{23\sqrt{2}}{2}$$

$$(I_7)_{\text{rms}} = 15.5 \text{ A}$$

End of Solution

- Derive the even and odd decomposition of a general signal x(t) by applying **Q.6** (b) (i) the definitions of even and odd signals.
 - (ii) Find the Fourier Transform of x(t), which is given by the following rectangular pulse, as shown in the figure.



[10 + 10 marks : 2025]

Solution:

(i) Let $x_e(t)$ is representing even part of x(t) and $x_0(t)$ is representing odd part of x(t), Now, we can represent signal x(t) in terms of $x_e(t)$ and $x_0(t)$ in the following form

$$x(t) = x_{e}(t) + x_{0}(t)$$
 ...(i)

Put t = -t in equation (i),

$$x(-t) = x_{\theta}(-t) + x_{0}(-t)$$

= $x_{\theta}(t) - x_{0}(t)$...(ii)

From equation (i) + (ii),

$$x(t) + x(-t) = 2x_{\mathcal{Q}}(t)$$

$$x_{\varrho}(t) = \frac{x(t) + x(-t)}{2}$$

From equation (i) – (ii),

$$x(t) - x(-t) = 2x_0(t)$$

$$x_0(t) = \frac{x(t) - x(-t)}{2}$$

Thus we can decomposed x(t) in ferms of $x_e(t)$ and $x_0(t)$ such as

$$x(t) = x_e(t) + x_0(t)$$

where

$$x_e(t)$$
 = even part of $x(t) = \frac{x(t) + x(-t)}{2}$

and

$$x_0(t) = \text{odd part of } x(t) = \frac{x(t) - x(-t)}{2}$$





NATIONAL SCHOLARSHIP TEST (NST)

Test Date: 2 Nov, 2025

Last date to Register: 28 Oct, 2025



Test Pattern

Time Duration : 60 Minutes

▼ Total Questions : 60 MCQs

✓ Weightage Per Question: 2 Marks

✓ Negative Marking: 0.66 Marks

Test Syllabus :

Technical Subjects: 40 Questions
Reasoning & Aptitude : 10 Questions
Engineering Mathematics : 10 Questions

Test Fee: Rs. 50/-

Offline Batches at Delhi

from First week of Dec 2025 Streams: CE, ME/PI, EE, EC/IN, CS

Live-Online Batches

from Last week of Nov, 2025 Streams: CE, ME/PI, EE, EC/IN, CS



Scan to register



NST is valid for Foundation Courses for

ESE 2027 GATE 2027

- Offline Mode
- Live-Online

Features of Foundation Courses:

- ✓ Classes by experienced & renowned faculties.
- ✓ Facility for doubt removal.
- Systematic subject sequence & timely completion.
- ✓ Concept practice through workbook solving.
- ✓ Comprehensive & updated books (Optional).
- Exam oriented learning ecosystem.
- **✓** Efficient teaching with comprehensive coverage.
- ✓ Proper notes making & study concentration.
- Regular performance assessment through class tests.

For more details, visit our website: www.madeeasy.in

Corporate Office: 44 - A/1, Kalu Sarai, Near Hauz Khas Metro Station, New Delhi - 110016

9021300500

mst@madeeasy.in

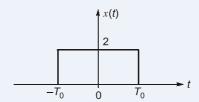


MADE EASY Centres: Delhi | Bhopal | Hyderabad | Jaipur | Pune

Electrical Engineering

PAPER-II

(ii) Given that,



The Fourier transform of x(t) is given by

$$X(\omega) = \int_{-\infty}^{\infty} x(t)e^{-j\omega t}dt = \int_{-T_0}^{T_0} 2 \cdot e^{-j\omega t}dt$$

$$= 2 \cdot \left[\frac{e^{-j\omega t}}{-j\omega}\right]_{-T_0}^{T_0}$$

$$= \frac{2}{-j\omega_0} \left[e^{-j\omega T_0} - e^{j\omega T_0}\right] = \frac{2}{-j\omega_0} \left[-2j\sin\omega T_0\right]$$

$$= \frac{4}{\omega_0} \sin\omega T_0 = \frac{4}{T_0} Sa(\omega T_0)$$

$$= \frac{4}{\omega_0} \left[\frac{\sin\omega T_0}{\omega T_0}\right] \omega T_0$$

$$= 4T_0 Sa(\omega T_0)$$

$$X(\omega) = 4T_0 Sa(\omega T_0)$$

Where $Sa(\theta)$ is sampling function defined by

$$Sa(\theta) = \frac{\sin \theta}{\theta}$$

End of Solution

Q.6 (c) A unity feedback system has an open-loop transfer function:

$$G(s) = \frac{K \cdot e^{-0.5s}}{(s^2 + \alpha s + \beta)}$$

G(s) has a DC gain of K/16, and has a decay rate of 2 nepers per second. Using the first-order Pade approximation for the delay, sketch the root locus plot of G(s) and find the range of K for which the unity feedback system remains stable.

[20 marks : 2025]

Solution:

 $G(s) = \frac{Ke^{-0.5s}}{(s^2 + \alpha s + \beta)}$ Given:

Using the first order pole approximation for the delay

Electrical Engineering

PAPER-II

$$e^{-T_d s} \simeq \frac{1 - \frac{T_d s}{2}}{1 + \frac{T_d s}{2}}$$
 where $T_d = 0.5$

We get

$$e^{-0.5s} \simeq \frac{1 - 0.25s}{1 + 0.25s}$$

So, approximated transfer function becomes

$$G(s) \simeq \frac{K(1-0.25s)}{(s^2 + \alpha s + \beta)(1+0.25s)}$$

DC gain is $\frac{K}{16}$.

So,

$$G(0) = \frac{K \cdot e^0}{0^2 + \alpha \cdot 0 + \beta} = \frac{K}{\beta} = \frac{K}{16} \implies \beta = 16$$

Delay rate of 2 neper per second,

$$\xi \omega_n = 2$$

$$\alpha = 2\xi \omega_n = 4$$

Therefore,

$$G(s) = \frac{K(1-0.25s)}{(s^2+4s+16)(1+0.25s)} \simeq \frac{-K(s-4)}{(s+4)(s^2+4s+16)}$$

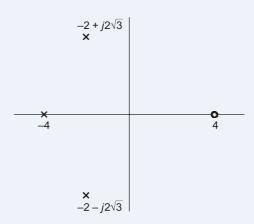
Poles location,

$$s_p = -4, -2 \pm j2$$

Zero's location,

$$s_{z} = 4$$

Pole-Zero Plot:

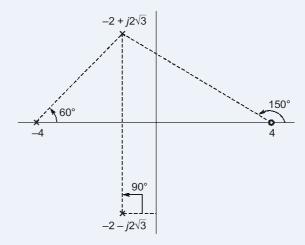


: Two poles are complex, there will exists angle of departure.

Angle of departure for pole $-2 + j2\sqrt{3}$.

Electrical Engineering

PAPER-II



$$\theta_{d(-2+j2\sqrt{3})} = 180^{\circ} + \Sigma \phi_{z} - \Sigma \phi_{p}$$

 $\theta_{d} = 180^{\circ} + 150^{\circ} - 90^{\circ} - 60^{\circ} = 180^{\circ}$

Intersection of Root Locus with Imaginary Axis:

$$1 + G(s) = 0$$

$$(s+4)(s^2+4s+16)-k(s-4)=0$$

$$s^3 + 8s^2 + (32 - k)s + (64 + 4k) = 0$$

Routh Array:

For stability:

$$64 + 4k > 0 \implies k > -16$$

 $24 - 1.5k > 0 \implies k < 16$

But k is positive. So, stable range is 0 < k < 16. Root locus will intersect imaginary axis for k = 16

Break Points:

$$1 + G(s) = 0$$

$$(s+4)(s^2+4s+16) - k(s-4) = 0$$

$$k = \frac{(s^3+8s^2+32s+64)}{s-4}$$

$$\frac{dk}{ds} = \left[\frac{(s-4)(3s^2+16s+32) - (s^3+8s^2+32s+64)}{(s-4)^2}\right] = 0$$

$$2s^3 - 4s^2 - 64s - 192 = 0$$



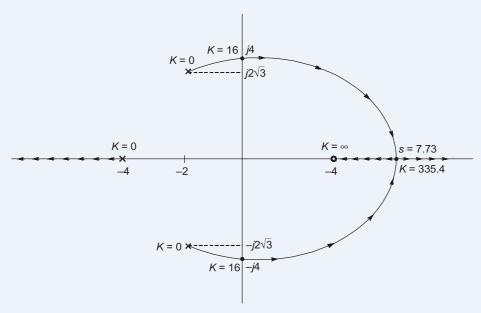
Electrical Engineering

PAPER-II

$$s = -2.86 \pm j2.04, 7.73$$

Valid break in point is 7.73.

Root Locus Diagram:



End of Solution

Q.7 (a) A second order system
$$G_1(s)$$
 as shown in figure,
$$r(s) \xrightarrow{\qquad \qquad } G_1(s) \xrightarrow{\qquad \qquad } y(s)$$

has no zeros and has unity DC gain.

The unit step response of $G_1(s)$ has a decay rate of 2.5 nepers/sec and an undamped natural frequency of $\sqrt{6}$ rad/sec.

- (i) Compute the observable canonical state-space representation of $G_1(s)$ and obtain its state transition matrix using the Cayley-Hamilton approach.
- (ii) Now, another identical $G_1(s)$ is placed in cascade with the earlier $G_1(s)$, as shown below.

$$r(s)$$
 $G_1(s)$ $G_1(s)$ $Y(s)$

Obtain the state-space representation of the overall cascaded system using the previously computed observable canonical representation of $G_1(s)$.

[20 marks : 2025]

Solution:

(i) The standard second-order transfer functions:

$$G(s) = \frac{\omega_n^2}{s^2 + 2\xi\omega_n s + \omega_n^2}$$

Electrical Engineering

PAPER-II

$$\xi \omega_{n} = 2.5$$

$$\omega_0 = \sqrt{6} \text{ rad/sec}$$

$$\xi = \frac{2.5}{\sqrt{6}}$$

Thus, C.E.
$$s^2 + 2\xi\omega_n s + \omega_n^2 = s^2 + 5s + 6$$

∵ DC gain is 1.

Therefore,

$$G_1(s) = \frac{6}{s^2 + 5s + 6}$$

For the transfer function,

$$G(s) = \frac{b_0}{s^2 + a_1 s + a_0}$$

Observable canonical form is:

$$A = \begin{bmatrix} 0 & -a_0 \\ 1 & -a_1 \end{bmatrix}; \quad B = \begin{bmatrix} b_0 \\ 0 \end{bmatrix}, \quad C = \begin{bmatrix} 0 & 1 \end{bmatrix}; \quad D = 0$$

Hence.

$$A = \begin{bmatrix} 0 & -6 \\ 1 & -5 \end{bmatrix}; \quad B = \begin{bmatrix} 6 \\ 0 \end{bmatrix}; \quad C = \begin{bmatrix} 0 & 1 \end{bmatrix}, \quad D = 0$$

State transition matrix using Cayley-Hamilton approach.

For A, its characteristics polynomial is

$$|sI - A| = s^2 + 5s + 6$$

So.

$$A^2 + 5A + 6I = 0$$

The state transition matlrix is:

$$\phi(t) = e^{At}$$

By C-H approach, since A^2 can be expressed in terms of A and I, we assume :

$$e^{At} = \alpha(t)I + \beta(t) A$$

To determine $\alpha(t)$, $\beta(t)$, we will use eigen values of A.

C.E.:
$$s^2 + 5s + 6 = 0$$

$$(s+2)(s+3) = 0$$

$$\lambda_1 = -2, \quad \lambda_2 = -3$$

So,

$$e^{At} = \frac{(A - \lambda_2 I)e^{\lambda_1 t} - (A - \lambda_1 I)e^{\lambda_2 t}}{\lambda_1 - \lambda_2}$$

Substitute the values,

$$e^{At} = \frac{(A+3I)e^{-2t} - (A+2I)e^{-3t}}{1}$$

Thus,

$$\phi(t) = (A + 3I)e^{-2t} - (A + 2I)e^{-3t}$$

$$\phi(t) = \begin{bmatrix} 3 & -6 \\ 1 & -2 \end{bmatrix} e^{-2t} - \begin{bmatrix} 2 & -6 \\ 1 & -3 \end{bmatrix} e^{-3t}$$



Electrical Engineering

PAPER-II

$$\phi(t) = \begin{bmatrix} e^{-2t} - e^{-3t} & 6(e^{-3t} - e^{-2t}) \\ e^{-2t} - e^{-3t} & 3e^{-3t} - 2e^{-2t} \end{bmatrix}$$
(State transition matrix)

(ii) Cascade of two $G_1(s)$ system:

The cascaded transfer function:

$$G_{\text{overall}}(s) = [G_1(s)]^2 = \left(\frac{6}{s^2 + 5s + 6}\right)^2$$

State Space Construction: If the first system is (A, B, C, D) is cascaded with an identical copy, then

$$\dot{x}_1 = Ax_1 + Br$$
 $u = Cx_1$
 $\dot{x}_2 = Ax_2 + Bu = Ax_2 + B(Cx_1) = BCx_1 + Ax_2$
 $y = Cx_2$

Overall combined system states:

$$X = \begin{bmatrix} x_1 & x_2 \end{bmatrix}$$

$$\dot{X} = \begin{bmatrix} A & 0 \\ BC & A \end{bmatrix} X + \begin{bmatrix} B \\ 0 \end{bmatrix} r$$

$$y = \begin{bmatrix} 0 & C \end{bmatrix} X$$

So,

On substituting the values,

$$A = \begin{bmatrix} 0 & -6 \\ 1 & -5 \end{bmatrix}, \quad B = \begin{bmatrix} 6 \\ 0 \end{bmatrix}, \quad C = \begin{bmatrix} 0 & 1 \end{bmatrix}$$
$$BC = \begin{bmatrix} 6 \\ 0 \end{bmatrix} \begin{bmatrix} 0 & 1 \end{bmatrix} = \begin{bmatrix} 0 & 6 \\ 0 & 0 \end{bmatrix}$$

Thus, overall system becomes

$$A_{\text{overall}} = \begin{bmatrix} 0 & -6 & 0 & 0 \\ 1 & -5 & 0 & 0 \\ 0 & 6 & 0 & -6 \\ 0 & 0 & 1 & -5 \end{bmatrix}$$

$$B_{\text{overall}} = \begin{bmatrix} 6 \\ 0 \\ 0 \\ 0 \end{bmatrix}$$

$$C_{\text{overall}} = \begin{bmatrix} 0 & 0 & 0 & 1 \end{bmatrix}$$

End of Solution



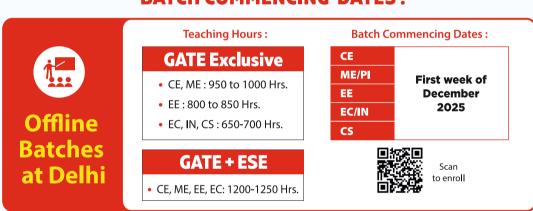
Announcing Foundation Courses for

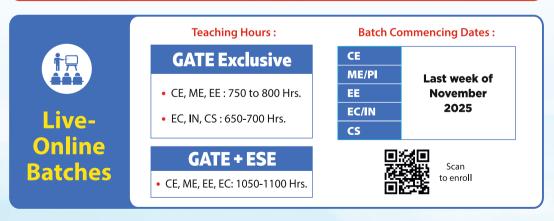
ESE & GATE: 2027

The foundation batches are taught comprehensively which cover the requirements of "all technical-syllabus based examinations".

- Classes by experienced & renowned faculties.
- Comprehensive & updated study material.
- Exam oriented learning ecosystem.
- Concept practice through workbook solving.
- Efficient teaching with comprehensive coverage.
- Similar teaching pedagogy in offline & online classes.
- Systematic subject sequence and timely completion.
- Regular performance assessment through class tests.

BATCH COMMENCING DATES:





More batches to be announced soon. | Courses with SES (State Engineering Services) are also available. | Low Cost EMI Facility Available | Admissions Open

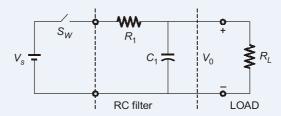
Delhi Centre: 44-A/1, Kalu Sarai, Near Hauz Khas Metro Station, New Delhi-110016 • Ph: 9021300500

MADE EASY Centres: Delhi | Bhopal | Hyderabad | Jaipur | Pune www.madeeasy.in

Electrical Engineering

PAPER-II

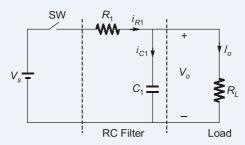
Q.7 (b) A buck converter with an RC filter is shown in the figure below with a load resistance R₁.



The switch S_W is operated with DT_S time ON and $(1 - D)T_S$ time OFF, cyclically with a time period of T_S . Draw the relevant waveforms and derive the expression for output voltage V_0 as a function of duty ratio 'D'. Assume the switching frequency to be high.

[20 marks : 2025]

Solution:



Assuming high switching frequency as per given data, we can neglect the ripple in output voltage V_{o} .

output voltage
$$V_o$$
.

I. $0 \le t \le DT_s$ $T_{ON} = D.T_s$ i_{C1} $V_o = V_o$ I_{C2} I_{C3} I_{C4} I_{C4} I_{C5} $I_{$

$$i_{C1} = i_{R1} - I_o$$

$$i_{C1} = \frac{V_s - V_o}{R_1} - \frac{V_o}{R_1}$$

II. $DT_S \le t \le T_s$: $SW \rightarrow OFF$: $i_{R1} = 0$

Capacitor will discharge and supply constant load curve :

$$i_{C1} = -\frac{V_o}{R_L}$$

$$i_{C1} = -\frac{V_o}{R_L}$$
After reaching steady state,
$$(i_C)_{\text{Avg}} = 0$$

$$\text{Area} = 0$$

$$\frac{V_s - V_o}{R_1} - \frac{V_o}{R_L} \qquad (1 - D)T_s$$

$$\text{SW} \qquad T_{ON} \qquad T_s$$

$$\left(\frac{V_{S} - V_{O}}{R_{1}} - \frac{V_{O}}{R_{I}}\right) D.T_{S} - \frac{V_{O}}{R_{I}} (1 - D)T_{S} = 0$$

Electrical Engineering

PAPER-II

$$\frac{V_o}{R_1}D + \frac{V_o}{R_L}D + \frac{V_o}{R_L} - \frac{V_o}{R_L}D = D \cdot \frac{V_s}{R_1}$$

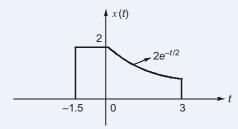
$$V_o \left[\frac{D}{R_1} + \frac{1}{R_L}\right] = \frac{D \cdot V_s}{R_1}$$

$$V_o = \frac{DR_L}{R_1 + DR_L} \cdot V_s$$

$$\frac{V_o}{V_s} = \frac{DR_L}{R_1 + DR_L} = \frac{D\left(\frac{R_L}{R_1}\right)}{1 + D\left(\frac{R_L}{R_1}\right)}$$

End of Solution

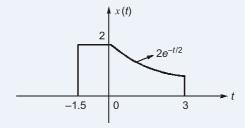
- Q.7 (c) Consider a signal x(t) shown in the following figure. Sketch and describe mathematically the signal x(t).
 - If time-compressed by a factor of 5. (i)
 - Repeat the problem for the same signal time-expanded by a factor of 3.



[10 + 10 marks : 2025]

Solution:

Given signal x(t) is



We can write x(t) in the following form

$$x(t) = \begin{cases} 2, & -1.5 < t < 0 \\ 2e^{-t/2}, & 0 < t < 3 \\ 0, & \text{otherwise} \end{cases} \dots (i)$$



Electrical Engineering

PAPER-II

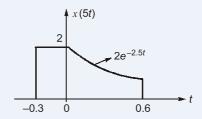
(i) Put t = 5t in equation (i),

$$x(5t) = \begin{cases} 2, & -1.5 < 5t < 0 \\ 2e^{-5t/2}, & 0 < 5t < 3 \\ 0, & \text{otherwise} \end{cases}$$

 \Rightarrow

$$x(5t) = \begin{cases} 2, & -0.3 < t < 0 \\ 2e^{-2.5t}, & 0 < t < 0.6 \\ 0, & \text{otherwise} \end{cases}$$

The waveform of time compressed form of x(t) by '5' i.e. x(5t) is shown below

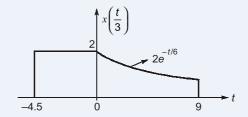


(ii) The time expanded form of x(t) by '3' i.e. $x\left(\frac{t}{3}\right)$ can be obtained by putting $t=\frac{t}{3}$ in equation (i),

$$x\left(\frac{t}{3}\right) = \begin{cases} 2, & -1.5 < \frac{t}{3} < 0 \\ 2e^{-\left(\frac{t}{3}\right)\frac{1}{2}}, & 0 < \frac{t}{3} < 3 \\ 0, & \text{otherwise} \end{cases}$$

$$= \begin{cases} 2, & -4.5 < t < 0 \\ 2e^{-t/6}, & -4.5 < t < 9 \\ 0, & \text{otherwise} \end{cases}$$

The waveform of $x\left(\frac{t}{3}\right)$ is shown below



End of Solution

Electrical Engineering

PAPER-II

Q.8 (a) Find the solution of the following second-order differential equation using the Laplace Transform Method: The differential equation is

$$\frac{d^{2}y(t)}{dt^{2}} + 7\frac{dy(t)}{dt} + 12y(t) = \frac{dx(t)}{dt} + x(t)$$

with initial conditions $y(0^-) = 2$ and $\dot{y}(0^-) = 1$ and $x(t) = e^{-5t} u(t)$ and $x(0^-) = 0$.

[20 marks: 2025]

Solution:

Given 2nd order differential equation

$$\frac{d^2y(t)}{dt^2} + 7\frac{dy}{dt} + 12y(t) = \frac{dx(t)}{dt} + x(t)$$
 ...(1)

and given $y(0^{-}) = 2$, $\dot{y}(0^{-}) = 1$

$$x(t) = e^{-5t}u(t)$$
 and $x(0^{-}) = 0$

On taking laplace transform of eqn. (1)

$$[s^{2}Y(s) - sy(0^{-}) - \dot{y}(0^{-})] + 7[s^{1}Y(s) - y(0^{-})] + 12Y(s) = [SX(s) - x(0^{-})] + X(s)$$

On putting initial conditions

$$[s^2Y(s) - 2s - 1] + 7[sY(s) - 2] + 12Y(s) = [sX(s) - 0] + X(s)$$

$$Y(s)[s^2 + 7s + 12] - 2s - 1 - 14 = X(s)[s + 1]$$

$$Y(s)[s^2 + 7s + 12] - 2s - 15 = X(s)[s + 1]$$

$$Y(s) = \frac{(s+1)X(s)}{(s^2+7s+12)} + \frac{2s+15}{(s^2+7s+12)} \qquad \dots (2)$$

Given:

$$x(t) = e^{-5t}u(t)$$

So,

$$X(s) = \frac{1}{(s+5)}$$
 ...(3)

Now, from eqn. (2)

$$Y(s) = \frac{(s+1)}{(s+5)(s^2+7s+12)} + \frac{2s+15}{(s^2+7s+12)}$$

$$= \frac{(s+1)+(2s+15)(s+5)}{(s+5)(s^2+7s+12)}$$

$$= \frac{(s+1)+2s^2+10s+15s+75}{(s+5)(s+3)(s+4)}$$

$$Y(s) = \frac{2s^2+26s+76}{(s+3)(s+4)(s+5)} \qquad ...(4)$$

On doing partial fraction

$$\frac{2s^2 + 26s + 76}{(s+3)(s+4)(s+5)} = \frac{A}{(s+3)} + \frac{B}{(s+4)} + \frac{C}{(s+5)}$$
$$(2s^2 + 26s + 76) = A(s+4)(s+5) + B(s+3)(s+5) + C(s+3)(s+4) \dots (5)$$

On putting s = -3 in eqn. (5)

$$2(-3)^2 + 26(-3) + 76 = A(-3 + 4)(-3 + 5) + 0 + 0$$

 $A = 8$

On putting s = -4 in eqn. (5)

$$2(-4)^2 + 26(-4) + 76 = 0 + B(-4 + 3)(-4 + 5)$$

$$B = -4$$

On putting s = -5 in eqn. (5)

$$2(-5)^2 + 26(-5) + 76 = 0 + 0 + C(-5 + 3)(-5 + 4)$$

$$C = -2$$

From eqn. (4) and (5)

$$Y(s) = \frac{8}{(s+3)} - \frac{4}{(s+4)} - \frac{2}{(s+5)}$$

On taking inverse laplace transform

$$y(t) = 8e^{-3t}u(t) - 4e^{-4t}u(t) - 2e^{-5t}u(t)$$

$$y(t) = (8e^{-3t} - 4e^{-4t} - 2e^{-5t})u(t)$$

End of Solution

Q.8 (b) The open-loop transfer function of a unity feedback system is given as:

$$G(s) = \frac{10}{(s-1)(s+5)}$$

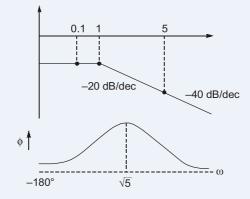
Sketch the Bode plot for the system and calculate Gain and Phase margins.

[20 marks: 2025]

Solution:

$$GH = \frac{10}{(s-1)(s+5)} = \frac{2}{(s-1)(\frac{1}{5}s-1)}$$

$$GH = \frac{10}{(s-1)(s+5)}$$





Foundation Courses for

2027

GATE



Tablet Course

- Pre-loaded full fledged recorded course
- · Android OS based 10.5 inch Samsung tablet
- · Internet access does not required
- Classes by senior faculties
- · Validity: 2 Years
- · Learn at your own pace
- Tablet is reusable for normal purpose after validity expires



Recorded Course

- Recorded Course
- Full fledged holistic preparation
- Classes by senior faculties
- Lectures can be watched anytime/ anywhere
- Courses are accessible on PC & Mac desktops/laptops/android/ iOS mobile devices.
- · Learn at your own pace
- Validity: 1 year
- Internet connection required

Teaching Hours

- **♥ GATE Exclusive** CE, ME, EE: 800 to 900 Hrs.
 - EC, IN, CS, CH: 650-700 Hrs.
- **♥ GATE + ESE** CE, ME, EE, EC : 1100 to 1200 Hrs.
- - EC, IN, CS, CH: 950-1050 Hrs.

Note: State Engineering Services Examination. • The course is offered with a validity options of 1 year and 2 years.

Admissions Open for **ESE 2026 & GATE 2026**

Admissions Open from **1 Jan 2026** for **ESE 2027 & GATE 2027** For Online Courses, Download: "MADE EASY Prime" App now



Android





Low Cost EMI Facility Available

Delhi Centre: 44-A/1, Kalu Sarai, Near Hauz Khas Metro Station, New Delhi-110016 • Ph: 9021300500

Electrical Engineering

PAPER-II

$$W: 0 \rightarrow \infty$$

 $M: 2 \rightarrow 0$

$$\phi: -180^{\circ} \rightarrow -180^{\circ}$$

(i) At
$$\omega_{pc} \Rightarrow$$

$$\omega_{pc} = 0$$
 $M_{\text{at }\omega_{pc}} = 2$

$$MM = \frac{1}{M} = 0.5 = -6 \text{ dB}$$

(ii) At
$$\omega_{pc} \Rightarrow$$

$$M = 1$$

 $\phi = -180$

$$\omega_{gc} = 1.618$$

$$PM = 180 + [-180^{\circ} + \tan^{-1}(\omega_{gc}) - \tan^{-1}(\omega_{gc}/5)]$$

$$(at \omega = \omega_{gc})$$

End of Solution

Q.8 (c) Design a UJT triggering circuit for a 220 V, 50 Hz AC source-fed single-phase half-controlled rectifier using BT 151 - 500 R SCR and 2N2646 UJT having the following parameters:

 $2N2646 \text{ UJT} : \eta = 0.65,$

$$R_{BB} = 7 \text{ k}\Omega,$$
 $I_P = 5 \text{ }\mu\text{A},$ $V_V = 3 \text{ }V,$ $I_V = 4 \text{ mA}$

$$I_{P} = 5 \, \mu A$$

$$V_{V} = 3 \text{ V}$$

$$I_V = 4 \text{ mA}$$

BT 151 - 500 R SCR:

$$V_{GT} = 0.8 \text{ V (typical)}, 1.5 \text{ V (max)}$$

$$I_{GT}$$
 = 5 mA (typical), 15 mA

$$VDRM = 500 V$$

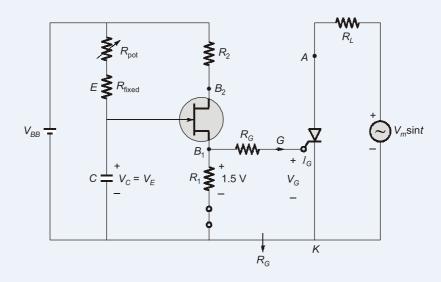
Assume the triggering circuit is fed from a 24 V DC supply.

Take V_{BB} = 20 V for design and a pulse width of the triggering pulse of 30 μ s. Draw relevant circuits and show the component values with power ratings.

[20 marks : 2025]

Solution:

UJT working as relaxation oscillator



$$R_G = \frac{1.5 \,\mathrm{V}}{I_{GT}} = \frac{1.5}{15.10}$$

$$R_h = 100 \,\Omega$$

Peak point voltage,

$$V_D = \eta V_{BB} + V_D$$

 $n \rightarrow$ Intrinsic stand off ratio

When UJT \rightarrow OFF

Capacitor charger,

$$V_C = V_{BB} \left(1 - e^{-t/RC} \right)$$

Where $R = R_{pot} + R_{find}$

$$UJT$$
: $\eta = 0.65$,

$$V_D = 0.7 \text{ V},$$

$$V_{V} = 3 \text{ V}$$

SCR:

Gate trigger current (For worst case) = 15 mA

$$V_{BB} = 20 \text{ V DC}$$

Half cycle at 50 Hz =
$$\frac{1}{2} \left(\frac{1}{T} \right)$$
 = 10 ms

Gate pulse width required,

$$t_p = 30 \, \mu s$$

$$t_p = 30 \, \mu s$$

$$V_P = \eta V_{BB} + V_D$$

$$= (0.65 \times 20) + (0.7)$$

$$V_P = 13.7 \text{ V}$$

:. UJT fires once capacitor voltage reaches (13.7 V)

When UJT - OFF

:.

Capacitor charges

$$V_C = V_{BB} \left(1 - e^{-t/RC} \right)$$
 When
$$V_C = V_P, \quad \text{UJT} \to \text{ON}$$

$$V_{BB} \left(1 - e^{-t/RC} \right) = V_P$$

$$V_{BB} - V_{BB} \cdot e^{-t/RC} = V_P$$

$$V_{BB} \cdot e^{-t/RC} = V_{BB} - V_P$$

$$e^{-t/RC} = \frac{V_{BB} - V_P}{V_{BB}}$$

$$e^{t/RC} = \frac{V_{BB}}{V_{BB}} - V_P$$

$$t = RC \cdot \ln \left(\frac{V_{BB}}{V_{BB}} - V_P \right) \qquad \dots (i)$$

.. Firing depends on product of RC

When UJT - ON,

Capacitor discharger gate pulse across R_1 until value voltage V_{V} .

$$\Delta V = V_P - V_V = 13.7 - 3 = 10.7 \text{ V}$$

$$\Delta Q = C \cdot \Delta V_C$$

$$I_{GT} \cdot t_p = C \cdot \Delta V_C \qquad (I_{GT} \ge 15 \text{ mA})$$

$$C = \frac{I_{GT} \cdot t_p}{\Delta V} = \frac{(15.10^{-3}) \times (30.10^{-6})}{10.7}$$

$$C = 0.042 \, \mu\text{F}$$

$$C > 0.042 \, \mu\text{F} \qquad (\because I_{GT} \ge 15 \, \text{mA})$$
Let
$$C = 0.047$$

$$I_{GT} = \frac{C \cdot \Delta V_C}{t_p} = \frac{(0.047 \, \mu\text{F}) \times (10.7)}{30.10^{-6}}$$

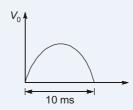
 $(I_{GT} = 16.8 \text{ mA}) > 15 \text{ mA}$ From equation (i),

$$t = RC \cdot \ln\left(\frac{V_{BB}}{V_{BB} - V_P}\right) = RC \cdot \ln\left(\frac{20}{20 - 13.7}\right)$$
$$t = 1.154 \, \text{RC}$$



Electrical Engineering

PAPER-II



For max firing angle ($\alpha = \pi$),

$$t = 10 \, \text{ms}$$

:.

$$t_{\text{max}} = 10 \text{ ms} = 1.154 R_{\text{max}} \cdot C$$

$$R_{\text{max}} = \frac{10 \text{ ms}}{1.154 \times RC} = \frac{10 \text{ ms}}{1.54 \times 0.047 \mu\text{F}} = 184 \text{ k}\Omega$$

: Let us use,

$$R_{\rm pot} = 200 \text{ k}\Omega$$

$$R_{\text{min}} = R_{\text{fixed}} = 10 \text{ k}\Omega$$

$$t = 1.154 \, \text{RC}$$

$$E_{\min} = 1.154 \times R_{\min} \cdot C$$

 $= 1.154 \times 10.10^3 \times (0.047 \times 10^{-6})$

 $= 0.542 \, \text{ms}$

$$\alpha_{\min} = wt_{\min}^{\circ} = 2\pi \cdot 50 \times (0.542 \times 10^{-3}) \times \frac{180}{\pi} = 9.75^{\circ}$$

$$\alpha_{\text{max}} = 180^{\circ}$$

 \therefore Range of firing angle (9.75° $\leq \alpha \leq$ 180°)

End of Solution