MADE EASY

Leading Institute for ESE, GATE & PSUs

ESE 2025 : Mains Test Series

UPSC ENGINEERING SERVICES EXAMINATION

Electronics & Telecommunication Engineering

Test-7 : Full Syllabus Test (Paper-I)

Name : ....

Roll No :

Test Centres				Student's Signature
Delhi ☑	Bhopal ☐	Jaipur ☐	Pune ☐	
Kolkata ☐	Hyderabad ☐			

Instructions for Candidates

1. Do furnish the appropriate details in the answer sheet (viz. Name & Roll No).
2. There are Eight questions divided in TWO sections.
3. Candidate has to attempt FIVE questions in all in English only.
4. Question no. 1 and 5 are compulsory and out of the remaining THREE are to be attempted choosing at least ONE question from each section.
5. Use only black/blue pen.
6. The space limit for every part of the question is specified in this Question Cum Answer Booklet. Candidate should write the answer in the space provided.
7. Any page or portion of the page left blank in the Question Cum Answer Booklet must be clearly struck off.
8. There are few rough work sheets at the end of this booklet. Strike off these pages after completion of the examination.

FOR OFFICE USE

Question No.	Marks Obtained
Section-A	
Q.1	46
Q.2	
Q.3	
Q.4	54
Section-B	
Q.5	54
Q.6	24
Q.7	18
Q.8	/
Total Marks Obtained	196

Signature of Evaluator   Cross Checked by

Ch. ....

- Good attempt

IMPORTANT INSTRUCTIONS

CANDIDATES SHOULD READ THE UNDERMENTIONED INSTRUCTIONS CAREFULLY. VIOLATION OF ANY OF THE INSTRUCTIONS MAY LEAD TO PENALTY.

DONT'S

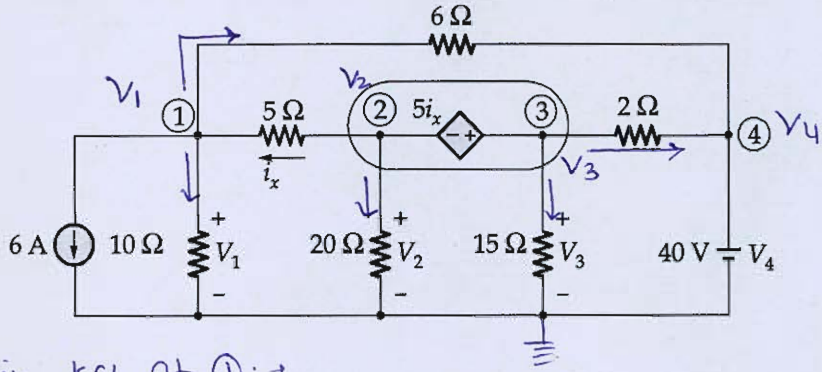
1. Do not write your name or registration number anywhere inside this Question-cum-Answer Booklet (QCAB).
2. Do not write anything other than the actual answers to the questions anywhere inside your QCAB.
3. Do not tear off any leaves from your QCAB, if you find any page missing do not fail to notify the supervisor/invigilator.
4. Do not leave behind your QCAB on your table unattended, it should be handed over to the invigilator after conclusion of the exam.

DO'S

1. Read the Instructions on the cover page and strictly follow them.
2. Write your registration number and other particulars, in the space provided on the cover of QCAB.
3. Write legibly and neatly.
4. For rough notes or calculation, the last two blank pages of this booklet should be used. The rough notes should be crossed through afterwards.
5. If you wish to cancel any work, draw your pen through it or write "Cancelled" across it, otherwise it may be evaluated.
6. Handover your QCAB personally to the invigilator before leaving the examination hall.

Section A

1 (a) Find the node voltages V_1, V_2, V_3 and V_4 in the circuit given below:



[12 marks]

Applying KCL at ①:→

$$\frac{V_1 - V_4}{6} + 6 + \frac{V_1}{10} = i_x \quad -①$$

Apply KCL at supernode ② & ③ :→

$$i_x + \frac{V_2}{20} + \frac{V_3}{15} + \frac{V_3 - V_4}{2} = 0 \quad -②$$

Also, $V_3 - V_2 = 5i_x \quad -③$

2

~~Apply KCL at node ④:→~~ $V_4 = 40V$

$$\frac{V_2 - V_1}{5} = i_x \quad -④$$

From ① & ④ :-

$$\frac{V_1 - 40}{6} + 6 + \frac{V_1}{10} = \frac{V_2 - V_1}{5}$$

From ②, ③ & ④

$$\frac{V_2 - V_1}{5} + \frac{V_2}{20} + \frac{V_2 + 5\left(\frac{V_2 - V_1}{5}\right)}{15} + \frac{V_2 + 5\left(\frac{V_2 - V_1}{5}\right) - 40}{5} = 0$$

Q.1 (b) Implement a Full subtractor with two 4 × 1 multiplexers.

[12 marks]

x	y	z	Diff = Z	Borrow = B
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

Difference $Z = x \oplus y \oplus z$

$= x \oplus (y\bar{z} + \bar{y}z)$

$= x\,\overline{(y\bar{z} + \bar{y}z)} + \bar{x}\,(y\bar{z} + \bar{y}z)$

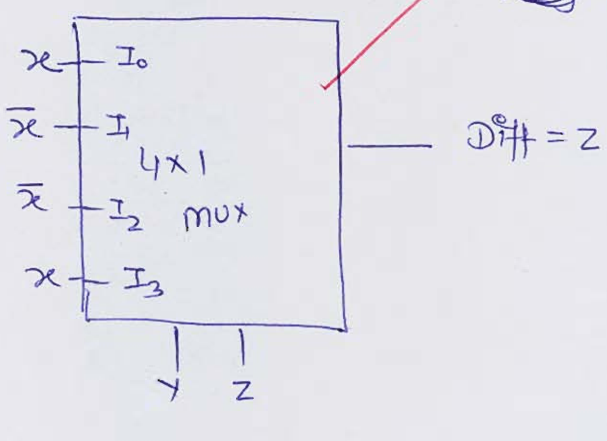
$= xyz + x\bar{y}\bar{z} + \bar{x}y\bar{z} + \bar{x}\bar{y}z$

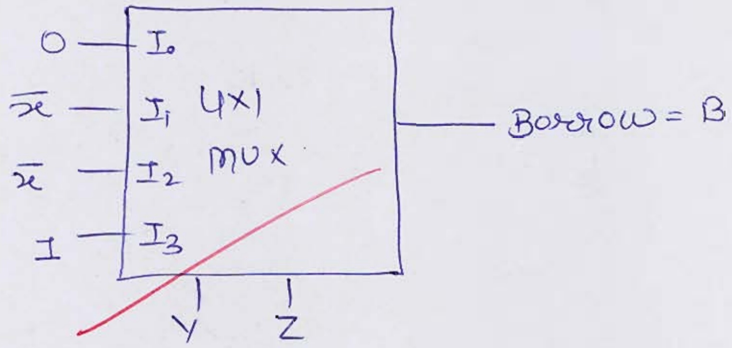
Borrow $B = \bar{x}y + yz + \bar{x}z$

$= \bar{x}y(z+\bar{z}) + (x+\bar{x})(yz) + \bar{x}(y+\bar{y})z$

$= \bar{x}yz + \bar{x}y\bar{z} + xyz + \bar{x}\bar{y}z$

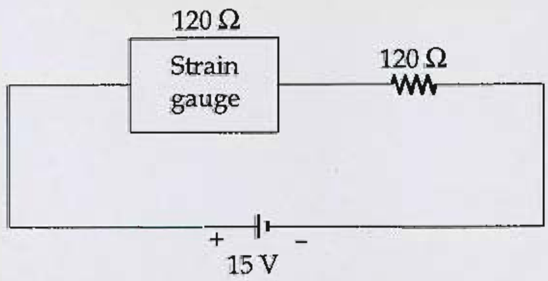
$= yz + \bar{x}y\bar{z} + \bar{x}\bar{y}z$





11

1 (c) A strain gauge having a resistance of 120 Ω and a gauge factor of 2 is connected in series with a ballast resistance of 120 Ω across 15 V supply as shown below:



Calculate the difference between the output voltage (voltage across the strain gauge) with no stress applied and with a stress of 150 MN/m². The modulus of elasticity of the material undergoing strain is 200 GN/m².

[12 marks]

With No stress Applied,

voltage across strain Gauge $\Rightarrow V_{SG} = \dfrac{15 \times 120}{120+120} = 7.5\ V$

Now, stress = 150 MN/m²

$G_f = 2 \qquad Y = 200\ GN/m^2$

$Y = \dfrac{\text{stress}}{\text{strain}}$

$\text{strain} = \dfrac{\text{stress}}{Y} = \dfrac{150 \times 10^6}{200 \times 10^9} = 0.75 \times 10^{-3}$

$G_f = \dfrac{\Delta R/R}{\Delta l/l} = \dfrac{\Delta R/R}{\text{strain}} \Rightarrow \Delta R = G_f \times \text{strain} \times R$

$\Delta R = G_f \times \text{strain} \times R$

$= 2 \times 0.75 \times 10^{-3} \times 120$

$\Delta R = 0.18\ \Omega$

Depending on compressive or extensive stress, new Resistance of Gauge could be :-

$R_1 = R + \Delta R$ OR $R_2 = R - \Delta R$

$R_1 = 120 + 0.18$

$= 120.18\ \Omega$

⇓

$V_{SG} = \dfrac{15 \times 120.18}{120.18 + 120}$

$V_{SG} = 7.5056\ V$

$\Delta V_{SG} = 7.5056 - 7.5$

$= 5.629 \times 10^{-3}\ V$

Ans.

$R_2 = 120 - 0.18$

$= 119.82\ \Omega$

⇓

$V_{SG} = \dfrac{15 \times 119.82}{119.82 + 120}$

$= 7.4943$

$\Delta V_{SG} = 7.5 - 7.4943$

$= 5.629 \times 10^{-3}\ V$

Ans.

10

1 (d) A typical paper clip weighs 0.59 g. Assume that it is made from BCC iron. Calculate:

(i) The number of unit cells.

(ii) The number of iron atoms in the paper clip.

(Assume for BCC iron, lattice parameter $a_0 = 2.866 \times 10^{-8}$ cm, atomic mass = 55.847 g/mol, Density $\rho = 7.87$ g/cm^3)

[12 marks]

volume of unit cell = $(a_0)^3$

$= (2.866 \times 10^{-8})^3$ cm^3

$= 23.5411 \times 10^{-24}$ cm^3

weight of clip = 0.59 gm

$\rho = \frac{\text{weight of clip}}{\text{volume of clip}}$

$7.87 = \frac{0.59}{\text{volume of clip}}$

volume of clip = $\frac{0.59}{7.87} = 7.4968 \times 10^{-2}$ cm^3

i) No. of unit cells = $\frac{\text{volume of clip}}{\text{volume of unit cell}} = \frac{7.4968 \times 10^{-2}}{23.5411 \times 10^{-24}}$

$= 0.3184 \times 10^{22}$

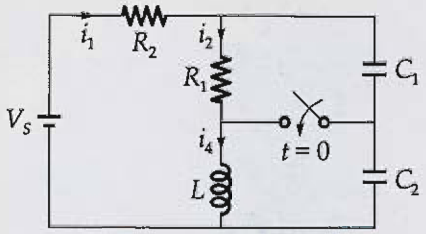
ii) BCC :→ In one unit cell

$N = \frac{1}{8} \times 8 + 1 \times 1 = 2$ atoms.

No. of Iron atoms = $2 \times 0.3184 \times 10^{22}$

$= 0.6368 \times 10^{22}$

Q.1 (e) Determine the current i_1, i_2 and i_4 at $t = 0^+$ for the circuit given below. Assume that the voltage source is applied at $t = -\infty$.

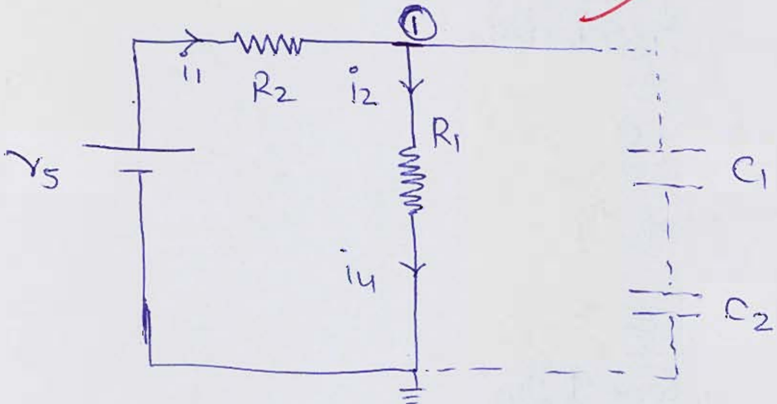


[12 marks]

At $t = 0^-$, switch is open

& steady state occurs, L behaves as short ckt

C_1 & C_2 behaves as open ckt.



$t=0^- \Rightarrow \quad i_1 = i_2 = i_4 = \dfrac{V_S}{R_1+R_2}$

voltage at node ① :→ $V_① = \dfrac{V_S \cdot R_1}{R_1+R_2}$

voltage across $C_1 = \dfrac{V_① \times C_2}{C_1+C_2} = \left(\dfrac{V_S R_1}{R_1+R_2}\right) \cdot \left(\dfrac{C_2}{C_1+C_2}\right)$

voltage across $C_2 = \dfrac{V_① \times C_1}{C_1+C_2} = \left(\dfrac{V_S R_1}{R_1+R_2}\right) \left(\dfrac{C_1}{C_1+C_2}\right)$

Now at $t = 0^+$,

Inductor does not allow sudden change in current, so $i_4(t=0^+) = i_4(t=0^-) = \dfrac{V_S}{R_1+R_2}$

Capacitor does not allow sudden change in voltage.

So, voltage across R_1 = voltage across C_1

At $t=0^+ \Rightarrow$ $i_2 = \frac{\text{voltage across } C_1 \text{ at } 0^-}{R_1}$

$$i_2 = \left(\frac{V_S R_1}{R_1+R_2}\right)\left(\frac{C_2}{C_1+C_2}\right)\left(\frac{1}{R_1}\right) = \frac{V_S C_2}{(R_1+R_2)(C_1+C_2)}$$

Similarly

At $t=0^+ \Rightarrow$ $i_1 = \frac{V_S - V_{①}}{R_2} = \frac{V_S - \frac{V_S R_1}{R_1+R_2}}{R_2}$

$$= \frac{V_S R_2}{R_2} = V_S$$

2 (a) (i) An assembly line has 3 safe sensors and one emergency shutdown switch. The line should keep moving unless any of the following conditions arise:

1. If the emergency switch is pressed.
2. If the sensor 1 and sensor 2 are activated at the same time.
3. If sensor 2 and sensor 3 are activated at the same time.
4. If all the sensors are activated at the same time.

Design a combinational circuit for above case using only two input NAND gates.

(ii) In a particular design which uses 5 bits for integral part and 7 bits for fractional part, the result of some operation is 7B8 Hex. Find the corresponding decimal equivalent.

[15 + 5 marks]

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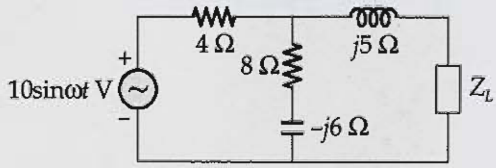
Q.2 (b) **(i)** Determine the load impedance Z_L that maximizes the average power drawn from the circuit shown below,



Find the maximum power drawn from the circuit.

(ii) Derive the expression for the average power of an AC circuit.

[20 marks]

Q.2 (c) **(i)** A long n-type semiconductor bar is placed along positive x-axis and is illuminated by a constant light source at one end (at $x = 0$). This illumination resulted in the generation of excess carriers at the surface (at $x = 0$) and these excess carriers are diffused into the bar. The variation of the minority carriers, in the bar along the positive x-axis, is given by,

$$p_n(x) = p_{n0} + p_n' e^{-\frac{x}{L_p}} \; ; x > 0$$

Prove that L_p is the average distance travelled by the diffusing carriers before they recombine.

(ii) In an n^+-p-n^+ transistor with uniform base doping, the neutral base width (that is, the undepleted portion of the base) is 1.2 μm when $V_{CB} = 5$ V. The depletion layer of the collector-base junction extended into base region for this case is 0.3 μm. Find the value of the collector-base voltage V_{CB} at which the entire base gets depleted. Neglect the built-in potential as well as the depletion layer width at the emitter-base junction.

[14 + 6 marks]

Q.3 (a) A combinational circuit consists of inputs A, B, S_1S_0 (= S) and an output F defined as follows:

(i) If ($S = 0$) then $F = \overline{A}$.

(ii) If ($S = 1$) then $F = A + B$.

(iii) If ($S = 2$) then $F = A - B$.

(iv) If ($S = 3$) then $F = \overline{B}$.

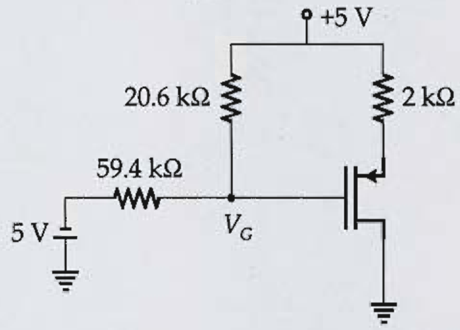
Build the logic circuit with a full adder, a 4 × 1 multiplexer, XOR gate and an inverter.

[20 marks]

Q.3 (b) Consider a CsBr crystal (one $Cs^+ - Br^-$ pair per unit cell) with a lattice parameter (a) of 0.430 nm. The electronic polarizability of Cs^+ and Br^- ions are 3.35×10^{-40} F-m^2 and 4.5×10^{-40} F-m^2 respectively, and the mean ionic polarizability per ion pair is 5.8×10^{-40} F-m^2. Determine the value of dielectric constant at **(i)** very low frequencies and **(ii)** optical frequencies.

[20 marks]

Q.3 (c) Consider the circuit shown in the figure below:



The transistor parameters are $V_{TP} = -1.5$ V and $\frac{\mu_p C_{ox} W}{2L} = 0.5\ \text{mA/V}^2$. Calculate:

(i) The value of V_{SG}.

(ii) The value of I_D.

(iii) The value of V_{SD}.

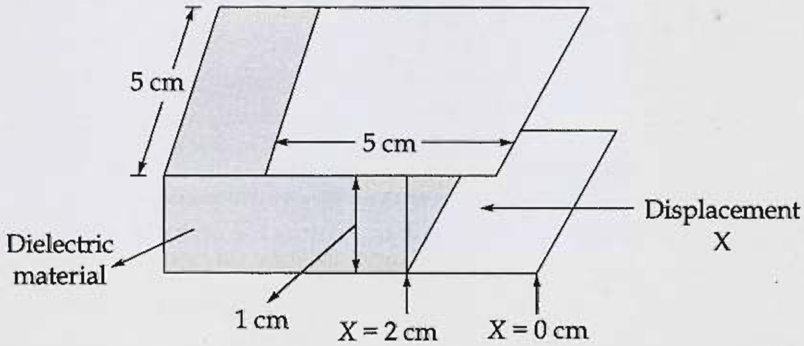
[20 marks]

4 (a) **(i)** A $4\frac{1}{2}$ digit and a $3\frac{1}{2}$ digit voltmeter on 100 V and 10 V range respectively are used for voltage measurements.

1. Find the resolution of each meter.

2. How would a voltage of 0.4312 V be displayed on these two meters?

(ii) A capacitive transducer is used for the measurement of linear displacement, X as shown below:



The parallel plate has a dimension of 5.0 cm × 5.0 cm and is separated by a distance of 1.0 cm. The space between the plates is filled with a dielectric material 1.0 cm thick, which has a dielectric constant of 4.0. If dielectric constant for air is 1.0, determine the value of the capacitance when X is equal to

1. 0.0 cm **2.** 2.0 cm

[6 + 14 marks]

(a) (i) $4\frac{1}{2}$ digit

4 full digits

Resolution = $\frac{1}{10^4}$ = 0.0001

0-1   0-9   0-9 . 0-9   0-9   ⇐ 100 V range

min^m reading :- 0 00 . 01 = 0.01
⇓ sensitivity

0.4312 will be read as ⇒ 0.43

(ii) $3\frac{1}{2}$ digit

3 full digits

Resolution = $\frac{1}{10^3}$ = 0.001

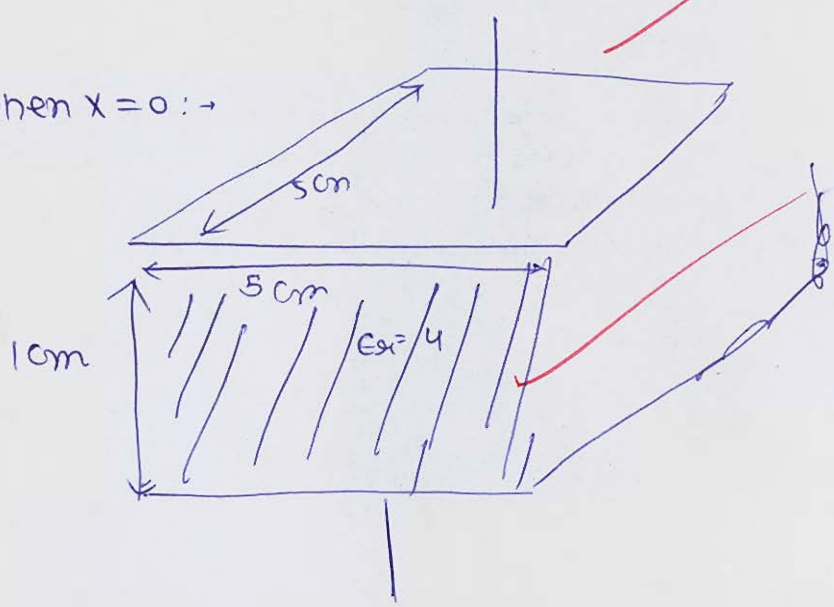
0-1   0-9 . 0-9   0-9   ⇐ 10V range

0 0 . 0 1

↳ sensitivity = 0.01

0.4312 will be read as ⇒ 0.43

(ii) When x = 0 :-



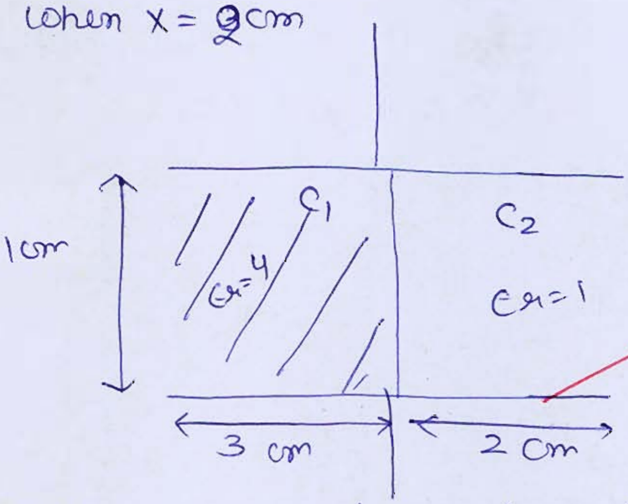
completely filled dielectric $\epsilon_r = 4$;

$$C = \frac{A \epsilon_0 \epsilon_r}{d} = \frac{5 \times 5 \times 10^{-2} \times 10^{-2} \times 4 \times 8.854 \times 10^{-12}}{1 \times 10^{-2}}$$

$= 8.854 \times 10^{-12}$ F

$= 8.854$ pF

when $x = 2$ cm



It can be assumed as two capacitor in parallel C_1 & C_2.

$$C_1 = \frac{A \epsilon_0 \epsilon_r}{d} = \frac{15 \times 10^{-4} \times 8.854 \times 10^{-12} \times 4}{1 \times 10^{-2}}$$

$= 531.24 \times 10^{-14}$ F

$= 5.3124$ pF

$$C_2 = \frac{A \epsilon_0}{d} = \frac{10 \times 10^{-4} \times 8.854 \times 10^{-12}}{1 \times 10^{-2}}$$

$= 8.854 \times 10^{-13}$

$= 0.8854$ pF

$C_{eq} = C_1 + C_2 = 6.1978$ pF

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Q.4 (b) **(i)** Design RS flip-flop using NMOS.

(ii) A 50 kVA, 2000/200V, 50 Hz transformer has an iron loss of 400 watt. The high voltage winding has a resistance of 0.6 Ω and low voltage winding have a resistance of 0.006Ω. Calculate the efficiency if the load has power factor 0.8 (lagging) and operating at half-load.

[10 + 10 marks]

(ii) $R_{H.V} = 0.6\,\Omega$, $R_{L.V} = 0.0006\,\Omega$, $L_i = 400$

$\cos\phi = 0.8$ lag, $x = 1/2 \rightarrow$ half load

$$\eta = \frac{x\, S_{rated} \cos\phi}{x\, S_{rated}\cos\phi + L_i + (L_{cu,fl})\cdot x^2}$$

$$I_{rated\ L.V} = \frac{50\times10^3}{200} = 250\ A$$

$R_{L.V} = 0.0006\,\Omega$

$$L_{cu.fl.} = (I_{rated\ L.V})^2 \times R_{LV}$$

$$= (250)^2 \times 0.0006\,\Omega = 37.5W$$

$$\eta = \frac{\frac{1}{2}\times 50\times10^3 \times 0.8}{\frac{1}{2}\times 50\times 10^3 \times 0.8 + 400 + \frac{1}{4}\times 37.5}$$

$\eta = 97.994\,\%$

10

(i) RS F.F Truth Table

R	S	Q^+
0	0	Q
0	1	0
1	0	1
1	1	X

R	S	Q	Q^+
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	X
1	1	1	X

K Map:

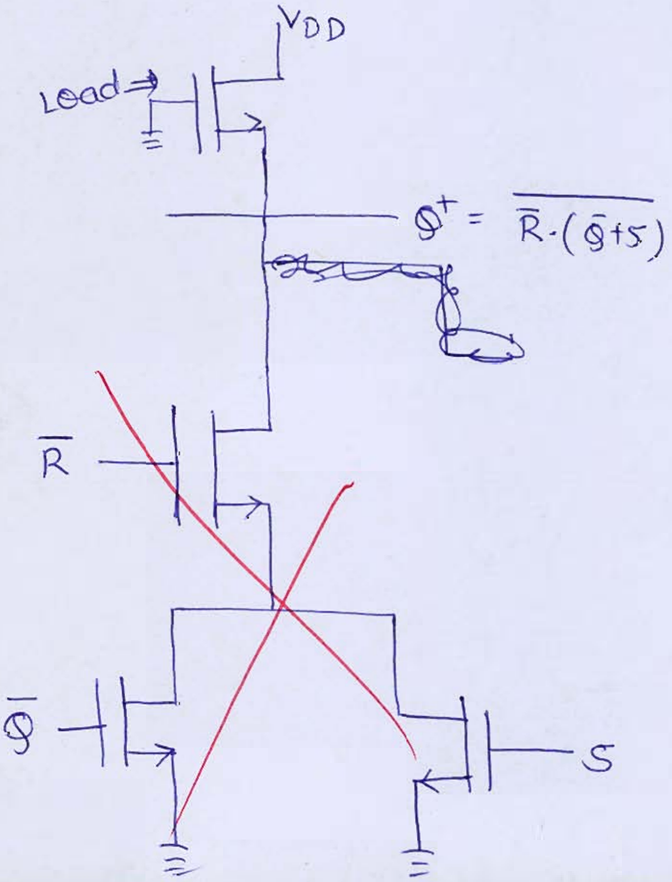
	$\bar{S}\bar{Q}$	$\bar{S}Q$	$S\bar{Q}$	SQ
$\bar{R}$	0 (0)	1 (1)	0 (3)	0 (2)
R	1 (4)	1 (5)	X (7)	X (6)

$$Q^+ = R + Q\bar{S}$$

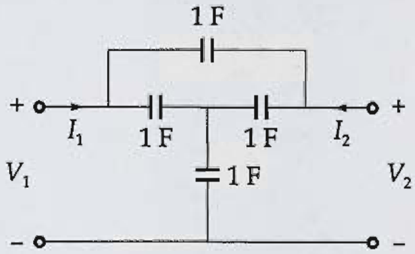
$$= \overline{\overline{R + Q\bar{S}}}$$

$$= \overline{\bar{R} \cdot \overline{Q\bar{S}}}$$

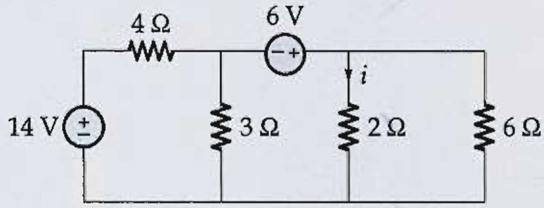
$$= \overline{\bar{R} \cdot (\bar{Q} + S)}$$



Q.4 (c) (i) Determine y-parameters of circuit shown below in s-domain.



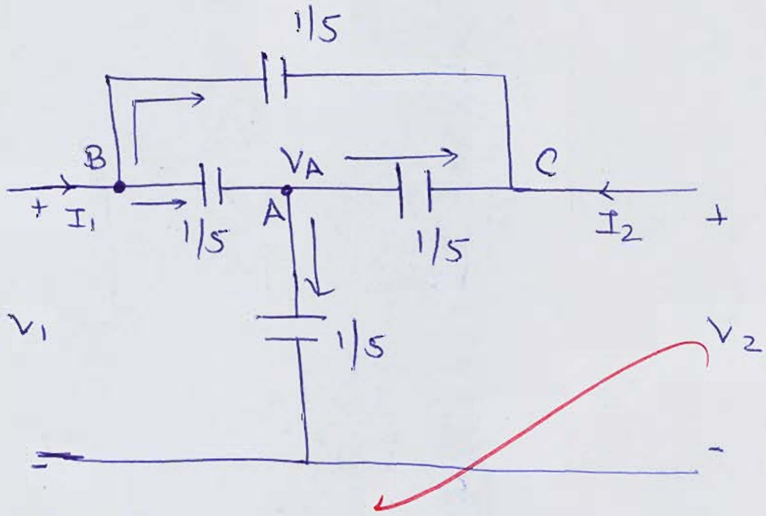
(ii) For the circuit shown below,



find the current i.

[12 + 8 marks]

(i)



KCL at B :→

$$I_1 = (V_1 - V_A)s + (V_1 - V_2)s \quad -(1)$$

KCL at A :→

$$(V_1 - V_A)s = V_A s + (V_A - V_2)s$$

⇓

$$(V_1 + V_2)s = 3V_A s$$

⇓

$$\boxed{V_A = \frac{V_1 + V_2}{3}}$$

Puttiy in ①:-

$$I_1 = \left(V_1 - \left(\frac{V_1+V_2}{3}\right) + V_1 - V_2\right)\mho$$

$$\boxed{I_1 = \frac{5}{3}V_1\mho - \frac{4}{3}\mho V_2} \quad -②$$

KCL at ⓒ :→

$$I_2 + (V_A - V_2)\mho + (V_1 - V_2)\mho = 0$$

$$I_2 = (2V_2 - V_1 - V_A)\mho$$

$$I_2 = \left(2V_2 - V_1 - \left(\frac{V_1+V_2}{3}\right)\right)\mho$$

$$\boxed{I_2 = \left(\frac{5}{3}\mho V_2 - \frac{4}{3}V_1\mho\right)} \quad -③$$

The General y-parameter eqns

$$I_1 = Y_{11}V_1 + Y_{12}V_2$$

$$I_2 = Y_{21}V_1 + Y_{22}V_2$$

comparis we get ⇒

$$Y = \begin{bmatrix} \frac{5}{3}\mho & -\frac{4}{3}\mho \\ -\frac{4}{3}\mho & \frac{5}{3}\mho \end{bmatrix}$$

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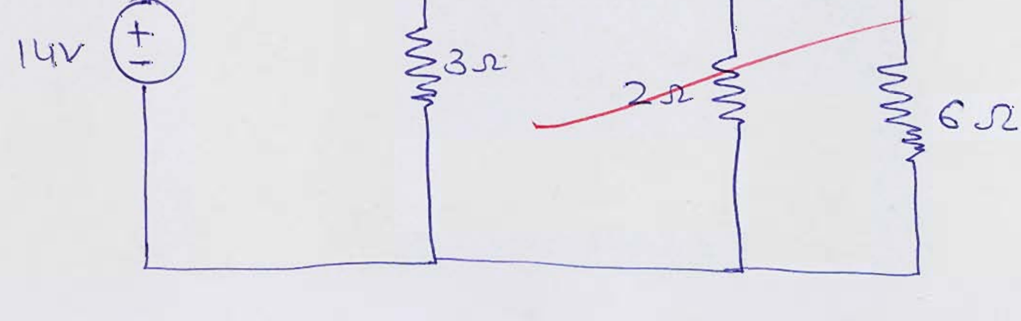
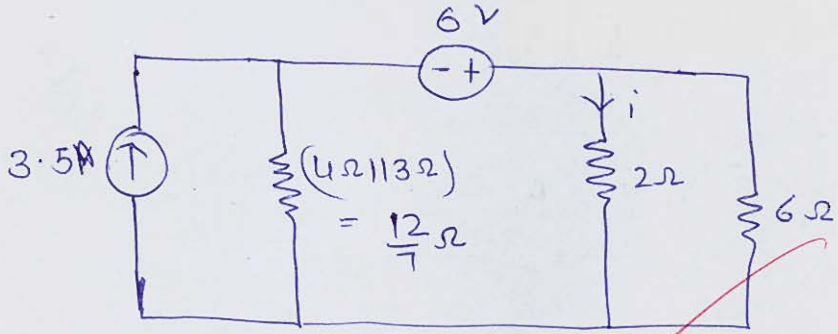
(ii)



Using source transf.



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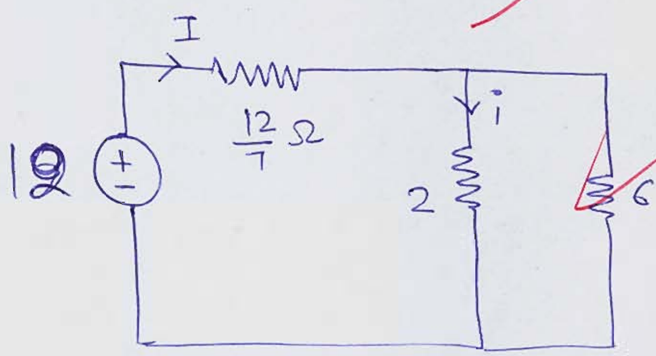

$6\Omega \parallel 2\Omega = \frac{12}{8} = \frac{3}{2}$

$$I = \frac{12}{\frac{12}{7} + \frac{3}{2}} = 3.73\ A$$

$$i = \frac{I \times 6}{2+6} = \frac{3.73 \times 6}{8} = 2.8\ A$$

8

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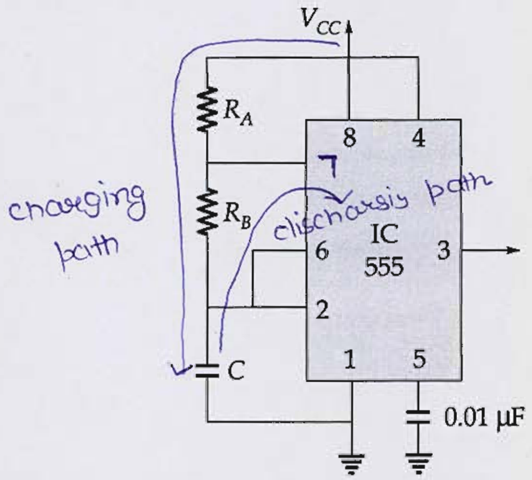
Section B

.5 (a) In the astable multivibrator shown in figure below:



where, R_A = 2.2 kΩ, R_B = 3.9 kΩ and C = 0.1 μF

Determine:

(i) Positive pulse width.

(ii) Negative pulse width.

(iii) Free running frequency.

(iv) Duty cycle.

[12 marks]

$T_{charging}$ = Positive pulse width

$= 0.69\,(R_A + R_B)C$

$= 0.69\,(2.2 + 3.9) \times 10^3 \times 0.1 \times 10^{-6}$

$= 4.209 \times 10^{-4}$ sec.

$T_{discharging}$ = −ve pulse width

$= 0.69\, R_B\, C$

$= 0.69 \times 3.9 \times 10^3 \times 0.1 \times 10^{-6}$

$= 2.691 \times 10^{-4}$ secc.

Time period $T = T_{+ve} + T_{-ve} = 6.9 \times 10^{-4}$ sec.

freq $= \frac{1}{T} = 0.1449 \times 10^4$ Hz $= 1.449$ KHZ

Duty cycle $= \frac{+ve \text{ pulse width}}{\text{Time period}} = \frac{4.209 \times 10^{-4}}{6.9 \times 10^{-4}} \times 100 = 61\%$.

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Q.5 (b) A thermistor has a resistance of 3.9 kΩ at the ice point (0°C) and 794 Ω at 50°C. The resistance temperature relationship is given by,

$R = \alpha R_0^{\beta/T}$

where, R = resistance at temperature (T) in Kelvin,

R_0 = resistance at ice point (273 K),

α, β are constants.

(i) Calculate the constants α and β.

(ii) Calculate the range of resistance measured in the case of temperature variation from 40°C-100°C.

[12 marks]

(i) $R = \alpha R_0^{\beta/T}$ — ①

R = 3.9 kΩ at T = 0°C = 273 K

R = 794 Ω at T = 50°C = 323 K

Putting in ①

$3900 = \alpha \cdot (3900)^{\beta/273}$ — ②

$794 = \alpha (3900)^{\beta/323}$ — ③

Divide ② & ③ :-

$\frac{3900}{794} = (3900)^{\beta\left(\frac{1}{273} - \frac{1}{323}\right)}$

$4.911 = (3900)^{\beta\left(\frac{1}{273} - \frac{1}{323}\right)}$

$\log 4.911 = \beta\left(\frac{1}{273} - \frac{1}{323}\right)\log 3900$

$\frac{0.1924}{\left(\frac{1}{273} - \frac{1}{323}\right)} = \beta$

$\beta = 339.4$

$\alpha = 0.1338$

(ii) $R = \alpha (R_o)^{\beta / T}$

$\beta = 339.4$, $\alpha = 0.1338$

$R_o = 3900$

$$R = 0.1338 \times (3900)^{\frac{339.4}{T}}$$

At $T = 40^\circ C = 313\,K$

$$R_{313K} = 0.1338 \times (3900)^{\frac{339.4}{313}}$$

$$= 1048.11\ \Omega$$

At $T = 100^\circ C = 373\,K$

$$R_{373K} = 0.1338 \times (3900)^{\frac{339.4}{373}}$$

$$= 247.76\ \Omega$$

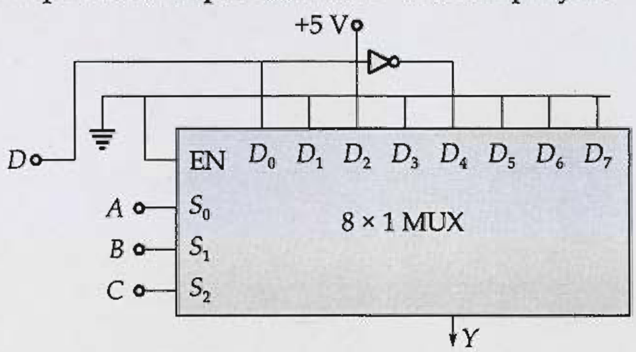
So Range of Resistence :- $1048.11\ \Omega - 247.76\ \Omega$

11

Q.5 (c) The circuit given below shows how an 8-bit MUX can be used to generate a four-variable logic function, even though the MUX has only 3 SELECT inputs. Three of the logic variables A, B and C are connected to the SELECT inputs. The fourth variable D and its inverse $\bar{D}$ are connected to selected data inputs of the MUX as required by the desired logic function. The other MUX data inputs are tied to a LOW or a HIGH as required by the function.

(i) Setup a truth table showing the output-Y for the 16 possible combinations of input variables.

(ii) Write the sum of products expression for Y and simplify it.



[12 marks]

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S_2 C	S_1 B	S_0 A	D	Y
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	1
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

11

$$Y = \bar{C}\bar{B}\bar{A}D + \bar{C}B\bar{A}\bar{D} + \bar{C}BA\bar{D} + C\bar{A}\bar{B}\bar{D}$$

Do not write in this margin

~~$Y = \bar{C}\bar{D}(C\oplus A)$~~

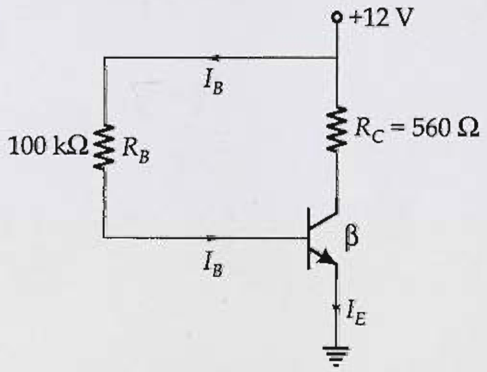
$$\boxed{Y = \bar{C}B\bar{D} + \bar{A}\bar{B}(C \oplus D)}$$

K Map :-

	$\bar{A}\bar{D}$	$\bar{A}D$	$A\bar{D}$	$\bar{A}D$
$\bar{C}\bar{B}$	0 (0)	1 (1)	0 (3)	0 (2)
$\bar{C}B$	1 (4)	1 (5)	0 (7)	0 (6)
$C\bar{B}$	0 (12)	0 (13)	0 (15)	0 (14)
CB	1 (8)	0 (9)	0 (11)	0 (10)

$Y = \bar{A}\bar{D}BC + \bar{C}B\bar{A} + \bar{A}D\bar{C}$

Q.5 (d) A base bias circuit in figure below is subjected to an increase in temperature from 25°C to 75°C. If β = 100 at 25° and 150 at 75°C, determine the percentage change in Q-point values (V_{CE} and I_C) over this temperature range.



(Neglect any change in V_{BE} and the effects of any leakage current)

[12 marks]

$T = 25^\circ C \Rightarrow \beta = 100$

$T = 75^\circ C \Rightarrow \beta = 150$

(Neglecting change in V_{BE} & I_{CO}) with temp. as given

~~when β = 100~~ →

$$12 - 100 I_B - V_{BE} = 0$$

$$I_B = \frac{12 - 0.7}{100} \text{ mA} = \frac{11.7}{100} \text{ mA}$$

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when $\beta = 100 \Rightarrow I_C = I_{C_1} = 100 \times I_B$

$= 11.7\ mA$

$\beta = 150 \Rightarrow I_C = I_{C_2} = 150 \times I_B$

$= 17.55\ mA$

$\%$ change in $I_C = \frac{I_{C_2} - I_{C_1}}{I_{C_1}} \times 100$

$= \frac{17.55 - 11.7}{11.7} \times 100 \simeq 50\%$

$12 - I_C(0.56) - V_{CE} = 0$

$V_{CE} = 12 - I_C(0.56)$

$\beta = 100 \Rightarrow V_{CE_1} = 12 - (11.7)0.56 = 5.448\ V$

$\beta = 150 \Rightarrow V_{CE_2} = 12 - (17.55)0.56 = 2.172\ V$

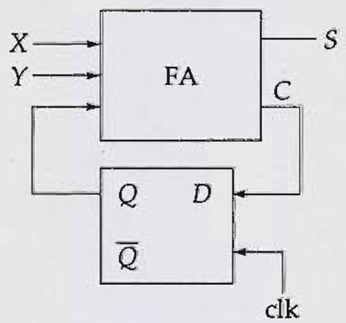
$\%$ change in $V_{CE} = \frac{V_{CE_2} - V_{CE_1}}{V_{CE_1}} \times 100\%$

$= \frac{2.172 - 5.448}{5.448} \times 100\%$

$\approx 60.132\%$

10

Q.5 (e) A sequential circuit has one flip-flop, two inputs X and Y, and one output S. It consists of a full adder circuit connected to a D flip-flop as shown. Derive the state table and state diagram of the given sequential circuit.



[12 marks]

$S = x \oplus y \oplus Q$, $C = xy + yQ + xQ$

$D = C \Rightarrow Q^+ = D \Rightarrow Q^+ = C$

$= xy + yQ + xQ$

I/P		P.S.	N.S.	O/P
x	y	Q	Q^+	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

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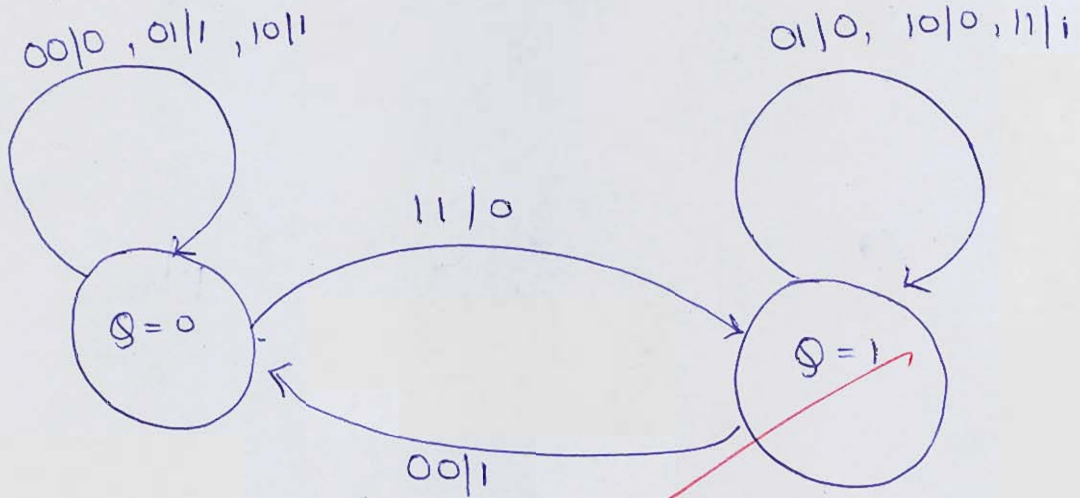
State diagram :→



11

Ruf ⇒

$-\left(-\frac{1}{2} - 2\left(\frac{1}{2}\right) + 4\left(\frac{1}{2}\right) - 7\left(\frac{1}{2}\right)\right.$

$-\left(1 + 2 \quad -4 + 7\right.$

$1\left(\frac{1}{2}\right) + \quad -2\left(\frac{1}{2}\right) \quad 4\left(\frac{1}{2}\right) - 7\left(\frac{1}{2}\right)$

-4

$-\left(\frac{-1}{2} + 2\left(\frac{1}{2}\right) + 4\left(\frac{1}{2}\right) - 7\left(\frac{1}{2}\right)\right)$

$-8 + 6 \quad -1$

Ruf

$-\left(4\left(\frac{1}{2}\right) - 3\left(\frac{1}{2}\right) - 7\left(\frac{1}{2}\right)\right.$

$-\left(\right.$

$2\left(\frac{1}{2}\right) - 5\left(\frac{1}{2}\right) + 7\left(\frac{1}{2}\right)$

$-\left(\right.$

$1\left(\frac{1}{2}\right) + 2\left(\frac{1}{2}\right) \quad + 4\left(-\frac{1}{2}\right) + 7\left(\frac{1}{2}\right)$

$-\left(\frac{1}{2} + 6\left(-\frac{1}{2}\right)\frac{1}{2}\right)$

$+7\left(\frac{1}{2}\right)$

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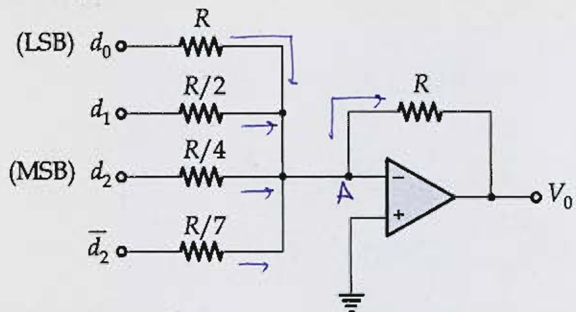
Q.6 (a) (i) Consider the circuit shown in the figure below:

$$d_i = \begin{cases} +\frac{1}{2}\text{ V}; & \text{for logic}-0 \\ -\frac{1}{2}\text{ V}; & \text{for logic}-1 \end{cases}$$



Derive an expression for output voltage V_0 in terms of input logic values. Using the result obtained, determine the value of V_0 for all the possible binary combinations of input and comment on the operation performed by the circuit.

(ii) Define the terms resolution, settling time and temperature sensitivity with respect to digital-to-analog converters.

[15 + 5 marks]

(i) $V^+ = V^- = 0\text{ V}$, Let current dirns as shown:-

Applying KCL at A:-

$$\frac{d_0}{R} + \frac{2d_1}{R} + \frac{4d_2}{R} + \frac{7\bar{d_2}}{R} = -\frac{V_0}{R}$$

$$V_0 = -(d_0 + 2d_1 + 4d_2 + 7\bar{d_2})$$

	MSB d_2	d_1	LSB d_0	V_0
0	0	0	0	0
1	0	0	1	-1
2	0	1	0	-2
3	0	1	1	-3
4	1	0	0	3
5	1	0	1	2
6	1	1	0	1
7	1	1	1	0

If i/p is less than 4 :→

$$V_0 = -V_i$$

If i/p is Greater than or equal to 4 ⇒

$$V_0 = 7 - V_i$$

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(ii) Resolution :- of the DAC is defined as the step size or min^m O/P voltage change by changing only 1-bit of the digital IP.

If Ref. voltage is $= V_{Ref}$

no. of bits of digital I/P $= n$

$$\text{Resolution} = \text{step size} = \frac{V_{Ref}}{2^n - 1}$$

Settling time :→ It is the time taken by O/P to come into the desired error range. Eg:- For 2% settling time, it is the time so that O/P comes in ±2% of the steady state value.

Temperature sensitivity :- It is defined as the amount of change in O/P of the DAC w.r.t change in output of the DAC.

2

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Q.6 (b) In a dynamometer type wattmeter the moving coil has 500 turns of mean diameter 30 mm. Estimate the torque if the axes of the field and the moving coils are at (i) 60° (ii) 90° when the flux density produced by field coil is 15×10^{-3} Wb/m^2, the current in moving coil is 0.05 A and the power factor is 0.866.

[20 marks]

Q.6 (c) (i) In a silicon *pn*-junction, the ratio of electron current crossing the depletion region to the total current is defined as the electron injection efficiency. Determine the expression for electron injection efficiency as a function of N_a/N_d where N_a = acceptor concentration on *p*-side and N_d = donor concentration on *n*-side. Assume $T = 300\text{K}$, $\tau_{p0} = \tau_{n0}$ and $\mu_n = 2.4\,\mu_p$.

(ii) The critical magnetic field for a superconducting tin at 4 K and 3 K are 0.02T and 0.03T respectively. Calculate the critical magnetic field at 2 K.

[10 + 10 marks]

(ii) $B_c(T = 4K) = 0.02T$

$B_c(T = 3K) = 0.03T$

$$B_c = B_o\left(1 - \left(\frac{T}{T_c}\right)^2\right)$$

At $T = 4K$:→

$$0.02 = B_o\left(1 - \left(\frac{4}{T_c}\right)^2\right)$$

At $T = 3K$:→

$$0.03 = B_o\left(1 - \left(\frac{3}{T_c}\right)^2\right)$$

$$\frac{2}{3} = \frac{1 - \frac{16}{T_c^2}}{1 - \frac{9}{T_c^2}} \Rightarrow 2 - \frac{18}{T_c^2} = 3 - \frac{48}{T_c^2}$$

$$\frac{30}{T_c^2} = 1$$

$$\boxed{T_c^2 = 30}$$

$$0.02 = B_o\left(1 - \frac{16}{T_c^2}\right)$$

$$0.02 = B_o\left(1 - \frac{16}{30}\right) \Rightarrow B_o = \frac{0.02 \times 30}{140} = \frac{6}{140} = \frac{3}{70}$$

$$B_c = B_0\left(1 - \left(\frac{T}{T_c}\right)^2\right)$$

$$B_c = \frac{3}{70}\left(1 - \frac{T^2}{30}\right)$$

At $T = 2K$

10

$$B_c = \frac{3}{70}\left(1 - \frac{4}{30}\right)$$

$$= \frac{3}{70} \times \frac{26}{30} = 0.03714\ T$$

(i) Electron current in PN Jn :-

$$I_n = \left[A q n_i^2 \left(\frac{D_N}{L_N N_A}\right)\right] \cdot \left(e^{V_A/\eta V_T} - 1\right)$$

Hole current in PN Jn

$$I_p = \left[A q n_i^2 \left(\frac{D_P}{L_P N_D}\right)\right] \left(e^{V_A/\eta V_T} - 1\right)$$

$D_N = \mu_n V_T$, $D_P = \mu_P V_T$, $\tau_N = \tau_P = \tau$

$L_N = \sqrt{D_N \tau_N}$, $L_P = \sqrt{D_P \tau_P}$

Now, e^- injection effeciency $\eta_i = \frac{I_n}{I_p + I_n}$

$$\eta_i = \frac{\left[A q n_i^2 \frac{D_N}{L_N N_A}\right]\left(e^{V_A/\eta V_T} - 1\right)}{A q n_i^2 \left(\frac{D_P}{L_P N_D} + \frac{D_N}{L_N N_A}\right)\left(e^{V_A/\eta V_T} - 1\right)}$$

$$\eta_i = \frac{\frac{D_N}{L_N N_A}}{\frac{D_N}{L_N N_A} + \frac{D_P}{L_P N_D}} = \frac{\frac{\mu_n V_T}{(\sqrt{\mu_n V_T \cdot \tau_n})\, N_A}}{\frac{\mu_n V_T}{(\sqrt{\mu_n V_T \tau_n})\, N_A} + \frac{\mu_p V_T}{N_D(\sqrt{\mu_p V_T \tau_p})}}$$

8

$$\eta_i = \frac{\frac{2.4\,\mu_P}{(\sqrt{2.4\,\mu_P V_T \tau})\, N_A}}{\frac{2.4\,\mu_p}{(\sqrt{2.4\,\mu_p V_T \tau_p})\, N_A} + \frac{\mu_p V_T}{(\sqrt{\mu_p \tau V_T})\, N_D}} = \frac{\frac{2.4}{\sqrt{2.4}\, N_A}}{\frac{2.4}{\sqrt{2.4}\, N_A} + \frac{1}{N_D}} = \frac{1.549\, N_A}{1.549\, N_A + N_D}$$

Ans.

Q.7 (a) **(i)** Draw a logic circuit for 4-bit even parity checker.

(ii) Solve for 'x' in the below equations for the given number systems:

1. $(70)_8 + (122)_6 = (211)_x$

2. $(135)_{12} = (x)_8 + (78)_9$ **[14 + 6 marks]**

(i) Even parity checker ⇒ odd no. of bits should be 1 so that when checking with a check bit, ~~total bits~~ total bits become even.

D_3	D_2	D_1	D_0	O/P = Z
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	1

				O/P = z
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

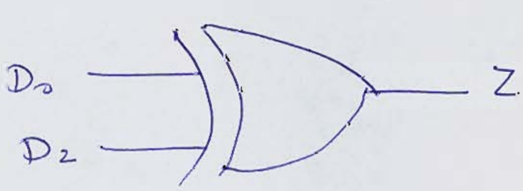
K-Map:→

	$\bar{D}_1\bar{D}_0$	$\bar{D}_1 D_0$	$D_1\bar{D}_0$	$D_1 D_0$
$\bar{D}_3\bar{D}_2$	0 (0)	1 (1)	0 (3)	1 (2)
$\bar{D}_3 D_2$	1 (4)	0 (5)	1 (7)	0 (6)
$D_3\bar{D}_2$	0 (12)	1 (13)	0 (15)	1 (14)
$D_3 D_2$	1 (8)	0 (9)	1 (11)	0 (10)

$$Z = \bar{D}_3\bar{D}_2\bar{D}_1 D_0 + \bar{D}_3\bar{D}_2 D_1 D_0 + \bar{D}_3 D_2 \bar{D}_1\bar{D}_0 + \bar{D}_3 D_2 D_1 \bar{D}_0 + D_3\bar{D}_2\bar{D}_1 D_0 + D_3\bar{D}_2 D_1 D_0 + D_3 D_2 \bar{D}_1\bar{D}_0 + D_3 D_2 D_1 \bar{D}_0$$

$$Z = \bar{D}_3\bar{D}_2 D_0 + \bar{D}_3 D_2 \bar{D}_0 + D_3\bar{D}_2 D_0 + D_3 D_2 \bar{D}_0$$

$$Z = D_2\bar{D}_0 + \bar{D}_2 D_0 = D_0 \oplus D_2$$



(ii) (1) $(70)_8 + (122)_6 = (211)_x$

$$\left([(8)^1 \times 7] + (8)^0 \times 0\right) + \left((6)^2 \times 1 + (6)^1 \times 2 + (6)^0 \times 2\right)$$

$$= (x)^2 \times 2 + (x)^1 \times 1 + (x)^0 \times 1)$$

$$56 + 36 + 12 + 2 = 2x^2 + x + 1$$

$$2x^2 + x - 105 = 0 \Rightarrow x = 7$$

(2) $(135)_{12} = (x)_8 + (78)_9$

$$(12)^2 \times 1 + 12 \times 3 + (12)^0 \times 5 = x \times (8)^0 + 9 \times 7 + 8 \times 9^0$$

$$144 + 36 + 5 = x + 63 + 8$$

$$\boxed{x = 114}$$

6

Q.7 (b) A star connected 3 phase alternator delivers a 3-phase star connected load at a power factor of 0.8 lagging. A wire connects the load and alternator. The terminal voltage at no load is 2500 V, and at full load of 1460 kW is 2200 V. Determine the terminal voltage when it delivers a 3 phase star connected load having a resistance of 6 Ω and reactance of 8 Ω per phase. Assume constant current and field excitation. Neglect the armature resistance.

[20 marks]

7 (c) Design a parallel binary multiplier that multiplies a 4-bit number $B = B_3B_2B_1B_0$ by a 3-bit number $A = A_2A_1A_0$ to form the product $P = P_6P_5P_4P_3P_2P_1P_0$.

[20 marks]

$B_3\ B_2\ B_1\ B_0$

$A_2\ A_1\ A_0$

	A_0B_3	A_0B_2	A_0B_1	A_0B_0
A_1B_3	A_1B_2	A_1B_1	A_1B_0	×
A_2B_2	A_2B_1	A_2B_0	×	×

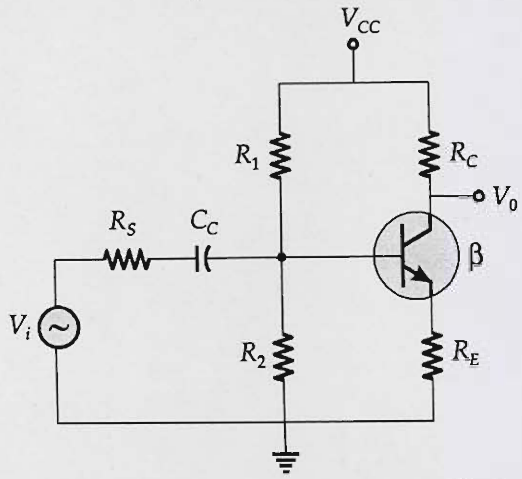
A_0B_0

8 (a) (i) A 3 phase, 10 kW, 6 pole, 50 Hz, 400 V delta connected induction motor runs at 960 rpm on full load. If it draws 85 A on direct on line starting, calculate the ratio of the starting torque to full load torque with Y-Δ starter. Power factor and full load efficiency are 0.88 and 90% respectively.

(ii) A 6 pole, 50 Hz, three phase induction motor has a maximum torque of 200 N-m when it is running at a speed of 900 rpm. The resistance of the rotor is 0.25 Ω. Neglecting the stator impedance, determine the torque at 5% slip.

[10 + 10 marks]

8 (b) Consider the common emitter amplifier as shown in the figure below:



Derive the expression for the overall voltage gain of the transistor $A_V(s)$ as a function of input capacitance C_C. Also draw the approximate magnitude plot for the voltage gain $A_V(s)$. [Assume $V_A = \infty$]

[20 marks]

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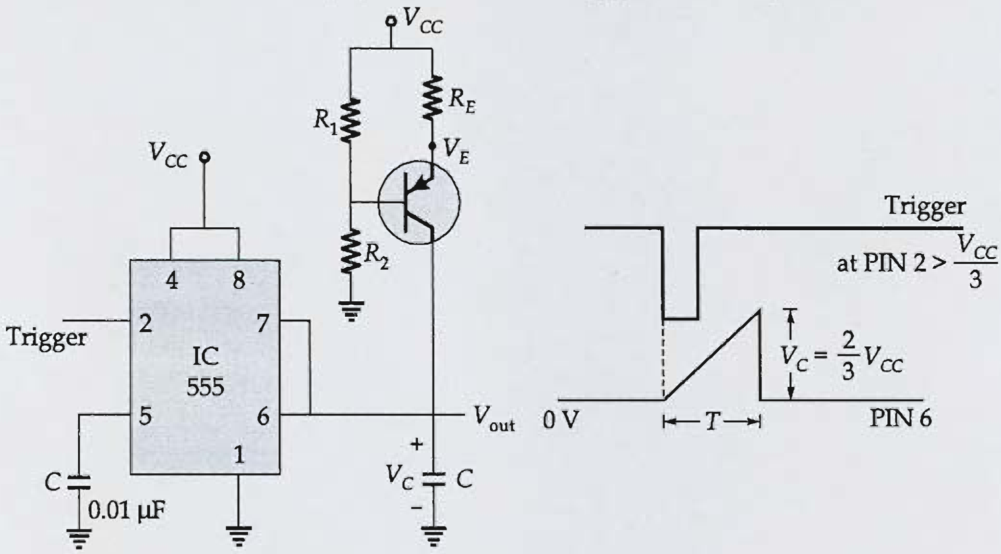
Q.8 (c) Consider the linear ramp generator with trigger and ramp waveform as shown below:



Determine the time period (T) of ramp waveform.

[20 marks]

OOOO

Space for Rough Work

Space for Rough Work

$12v_2 - 12v_1 + 3v_2 + 8v_2 - 4v_1 + 60v_2 - 30v_1 - 1200 = 0$

$83v_2 - 46v_1 = 1200$

$10v_1 - 10v_4 + 360 + 6v_1 = 60$

$16v_1 - 40 = \frac{12}{60}$ (

$28v_1 - 12v_2 = 40$

$\frac{v_2 - v_1}{5} + \frac{v_2}{20} + \frac{v_2 + 5\left(\frac{v_2 - v_1}{5}\right)}{15}$

$+ \frac{v_2 + 5\left(\frac{v_2 - v_1}{5}\right) - v}{2}$

$\frac{v_2 - v_1}{5} + \frac{v_2}{20} + \frac{2v_2 - v_1}{15} + \frac{2v_2 -}{}$